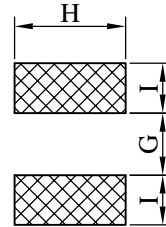
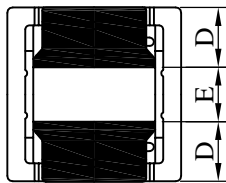
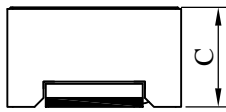
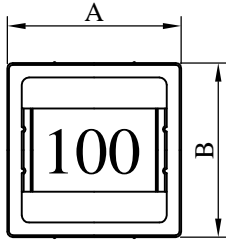


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	QS3818□□□□L□-□□□		
		REV.	20150320-B	PAGE	1

I . Configuration and dimensions :



(PCB Pattern)

Unit : m/m

A	B	C	D	E	G	H	I
3.80 ±0.2	3.80 ±0.2	1.80 ±0.2	1.30 ±0.2	1.20 ±0.2	1.10 ref.	4.30 ref.	1.60 ref.

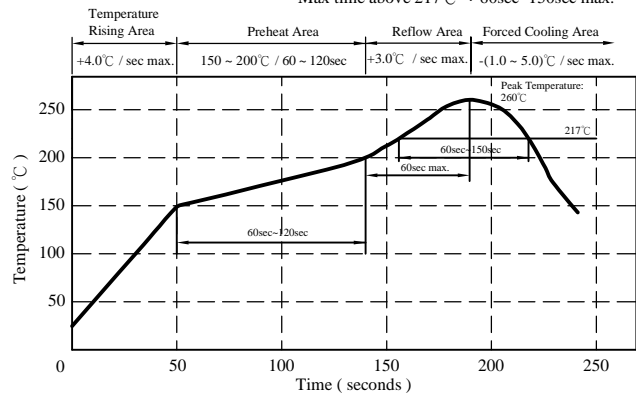
II . Description :

- a . Ferrite drum core construction.
- b . Magnetically shielded.
- c . Enamelled copper wire : H class
- d . Product weight : 0.11 g (ref.)
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen free available

Peak Temp : 260°C max.
 Max. Peak Temp - 5°C : 30sec max.
 Max time above 217°C : 60sec~150sec max.

III . General specification :

- a . Storage temp. : -40°C ----+125°C
- b . Operating temp. : -40°C ----+125°C
(Temp. rise included)
- c . Resistance to solder heat : 260°C .10 secs.



AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	QS3818□□□□L□-□□□		
		REV.	20150320-B	PAGE	2

IV . Electrical characteristics :

DWG No.	Inductance (μ H)	SRF (MHz) typ.	RDC (m Ω)		Isat (A) typ.	Irms (A) typ.
			typ.	max.		
QS38181R0YL□-□□□	1.0 \pm 30%	165	27.5	37.0	2.10	3.60
QS38181R5YL□-□□□	1.5 \pm 30%	125	33.0	47.0	1.60	3.10
QS38182R2YL□-□□□	2.2 \pm 30%	95	45.5	60.0	1.40	2.60
QS38182R7YL□-□□□	2.7 \pm 30%	90	49.5	65.0	1.30	2.50
QS38183R3YL□-□□□	3.3 \pm 30%	75	64.5	85.0	1.15	2.10
QS38183R9YL□-□□□	3.9 \pm 30%	70	80.0	105.0	1.05	2.00
QS38184R7YL□-□□□	4.7 \pm 30%	60	90.0	120.0	0.95	1.90
QS38186R8YL□-□□□	6.8 \pm 30%	50	125.0	160.0	0.80	1.60
QS3818100ML□-□□□	10.0 \pm 20%	40	180.0	235.0	0.65	1.30
QS3818150ML□-□□□	15.0 \pm 20%	30	260.0	340.0	0.55	1.05
QS3818220ML□-□□□	22.0 \pm 20%	25	395.0	500.0	0.43	0.80
QS3818330ML□-□□□	33.0 \pm 20%	20	590.0	740.0	0.34	0.65
QS3818470ML□-□□□	47.0 \pm 20%	15	825.0	1030.0	0.30	0.56
QS3818680ML□-□□□	68.0 \pm 20%	13	1280.0	1600.0	0.24	0.46
QS3818101ML□-□□□	100.0 \pm 20%	11	2040.0	2600.0	0.20	0.34

- 1). □: Packaging information : □ Code
- 2). "-□□□" : Reference code
- 3). Electrical specifications at 25°C
- 4). Inductance Test Freq :100KHz /0.1V
- 5). Isat base on $\Delta L / L0A=35\%$ typ.(Approximately transient current)
- 6). Irms base on Temp. rise 40°C typ.

AR-001C

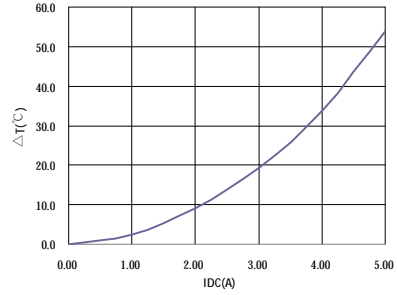
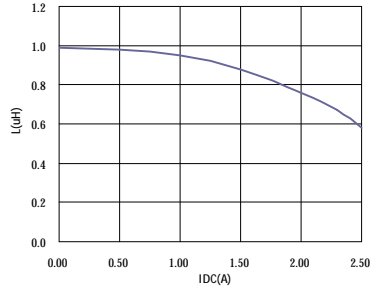
SPECIFICATION FOR APPROVAL

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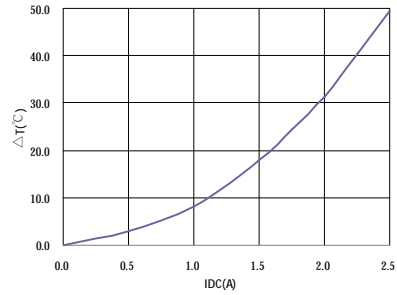
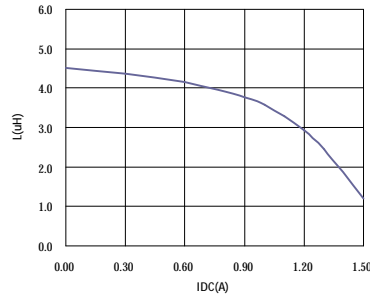
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.		QS3818□□□□L□-□□□	
		REV.	20150320-B	PAGE	3

V . Curve :

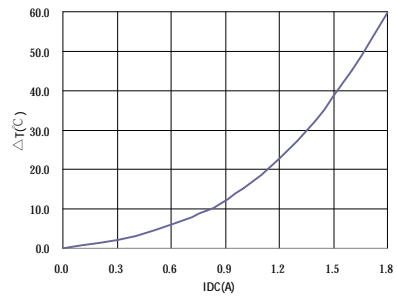
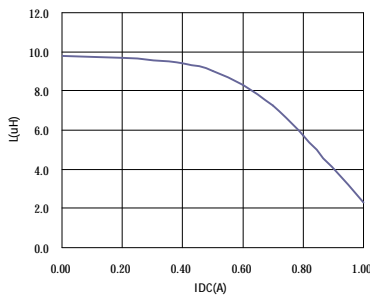
QS38181R0YL□-□□□



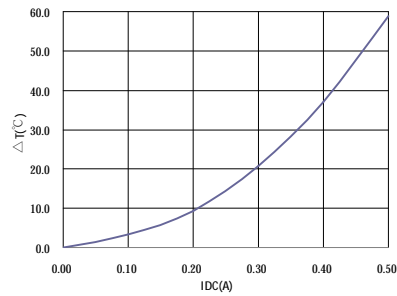
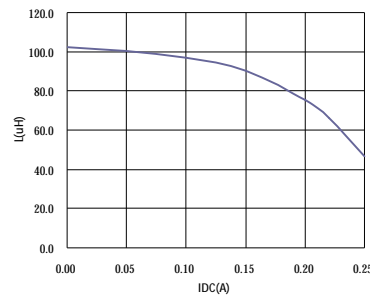
QS38184R7YL□-□□□



QS3818100ML□-□□□



QS3818101ML□-□□□



AR-001C



千如電子集團
ABC ELECTRONICS GROUP

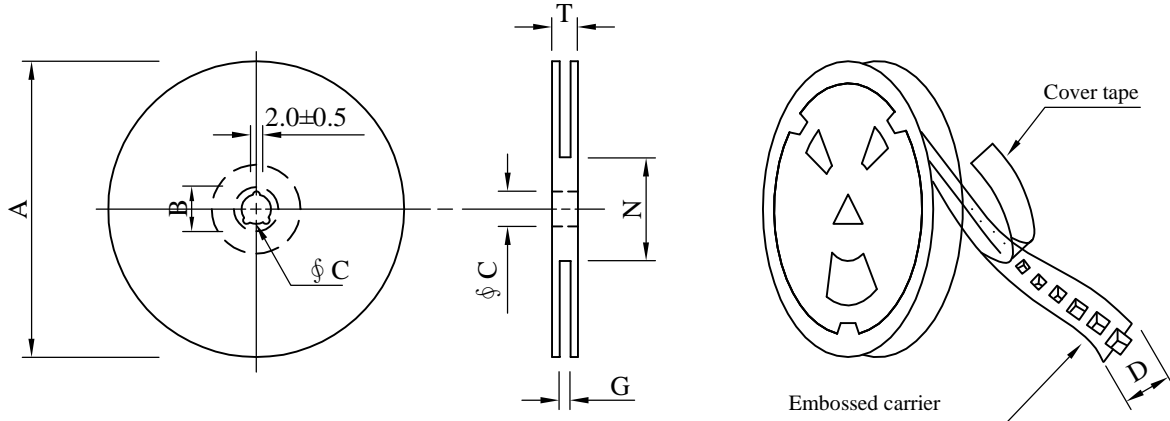
SPECIFICATION FOR APPROVAL

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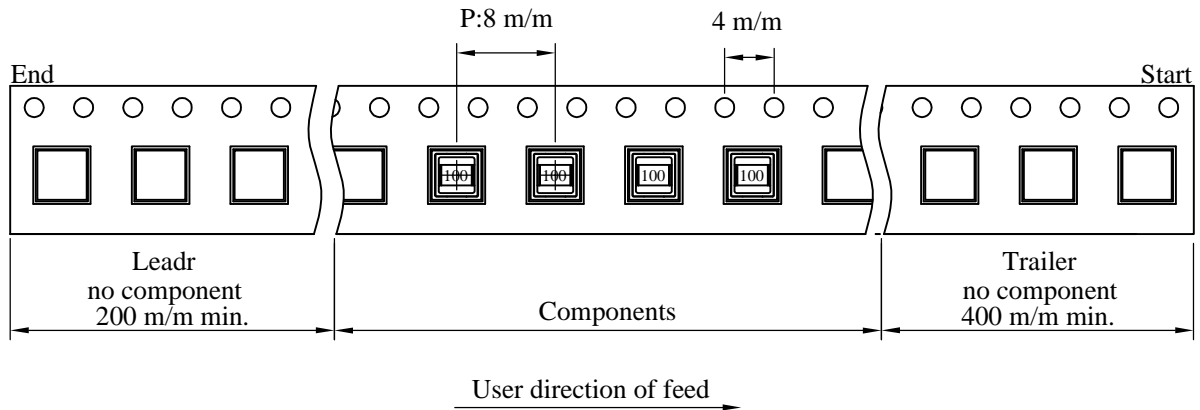
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.		QS3818□□□□L□-□□□	
		REV.	20150320-B	PAGE	4

VI . Packaging information :

(1) Configuration



※Carrier tape width : D



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
07 - 12	178	21±0.8	13	12	14 ⁺⁰	50 ⁻⁰	16.5

(3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	1,000	220	07 - 12	40,000	10.0	42 x 41 x 24

SPECIFICATION FOR APPROVAL

REF. :

PROD.	Shielded SMD Power Inductor	ABC'S DWG NO.		QS3818□□□□L□-□□□	
NAME		REV.	20150320-B	PAGE	5

VII . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125°C 2.Time:96 hours.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
2.Temperature Cycling	JESD22 Method JA-104	1.Temperature: -40°C ~ 125°C 2.Number of cycle:96 cycle 3.Dwell time:30 minutes	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature: 85±5 °C 2.Time:96 Hours 3.Humidity: 85±5% RH.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
4.Operational Life	MIL-PRF-27	1.Temperature: 125°C 2.Time:96 hours. 3.Apply rated current.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
5.External Visual	MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22 Method JB-100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in appearance. 2.No marking blurred. 3.Inductance shall not change more than ±20%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitud : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210	1.Highest temperature : 260±5°C. 2.Time (temp. ≥ 217°C) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
10.Rated current	MIL-STD-202 Method 330	Apply rated current for 5 second.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
11.Temperature rise	MIL-PRF-27	Apply rated current for 10 minutes.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
12.Over load	MIL-PRF-27	Apply double as rated current for 5 minutes. (It's not application to some special design)	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
13.Solderability Test	J-STD-002	1.Baking in pre-testing : 155±5°C / 16Hours±30 min. 2.Peak temperature : 240±5°C 3.Time (temp. ≥ 217°C) : 60~150 second. 4.IR reflow times : 1 times.	The terminal shall be at least 95% covered with fresh solder.
14.Electrical Characteriazation	User Spec.	1.Operating temperature : -40°C~125°C 2.Room temperature : 25°C.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
15.Withstanding Voltage Test	MIL-STD-202 Method 201	1.DC:500V 2.Time:1minutes	1.During the test no breakdown. 2.The characteristic is normal after test.
16.Drop	JESD22-B111	Packaged & Drop down from 1m.In 1 angle 1ridges & 2 surfaces orientation.	1.No case deformation or change in appearance. 2.Inductance shall not change more than ±20%.
17.Terminal Strength Test	JIS-C-6429	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

AR-001C