

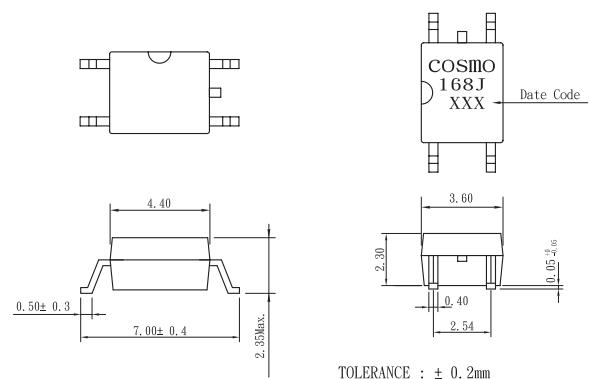
Features

1. Opaque type, mini-flat package.
2. Subminiature type
(The volume is smaller than that of our conventional DIP type by as far as 30%)
3. Isolation voltage between input and output (Viso:2500Vrms).

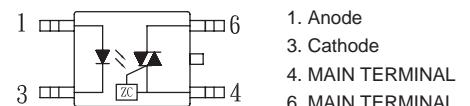
For 115/240 Vac (rms) Application:

1. Solenoid/Valve Controls.
2. Lighting Controls.
3. Static Power Switches.
4. AC Motor Drives.
5. Temperature Controls.
6. E.M. Contactors.
7. AC Motor Stators.
8. Solid State Relays.
9. Programmable controllers.

Outside Dimension:Unit (mm)



Schematic:Top View



Absolute Maximum Ratings

(Ta=25°C)

Parameter		Symbol	Rating	Unit
Input	Forward current	I _F	50	mA
	Peak forward current (100us)	I _{FM}	1	A
	Reverse voltage	V _R	6	V
	Power dissipation	P _D	70	mW
Output	Off-State Output Terminal voltage	V _{DRM}	600	Vpeak
	On-State R. M. S. Current	I _{T(RMS)}	70	mA
	Peak Repetitive Surge Current (PW=10ms, DC 10%)	I _{TSM}	1	A
	Power dissipation	P _D	150	mW
Total power dissipation		P _{Tot}	200	mW
Isolation voltage 1 minute		V _{iso}	2500	Vrms
Operating temperature		T _{opr}	-40 to +100	°C
Storage temperature		T _{tsg}	-50 to +125	°C
Soldering temperature 10 second		T _{sol}	260	°C

Electro-optical Characteristics

(Ta=25°C)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input	Forward voltage	V _F	I _F =10mA		1.2	1.4	V
	Peak forward voltage	V _{FM}	I _{FM} =0.5A			3.5	V
	Reverse Leakage Current	I _R	V _R =5V			10	μA
Output	Peak Blocking Current	I _{DRM}	V _{DRM} =600V			1.0	nA
	ON-State Voltage	V _{TM}	I _T =70mA		1.6	2.8	V
Transfer characteristics	Holding Current	I _H			1.0		mA
	Critical rate of rise of OFF-state voltage	dV/dt	V _{DRM} = (1/2) * Rated	600	1000		V/μS
	Isolation resistance	R _{iso}	DC500V	5x10 ¹⁰	10 ¹¹		ohm
	Minimum trigger current	I _{FT}	Main Terminal Voltage=3V	放电		3	mA
	Inhibit Voltage (MT2 Voltage above which device not trigger.)	V _{INH}	I _F =Rated I _{FT}			50	V
	Leakage in Inhibited State	I _{DRM2}	I _F =Rated I _{FT} , V _T =Rated V _{DRM}		200	600	μA

Fig.1 Forward Current vs. Ambient Temperature

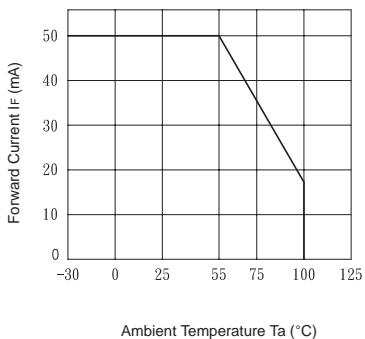


Fig.2 Diode Power Dissipation vs. Ambient Temperature

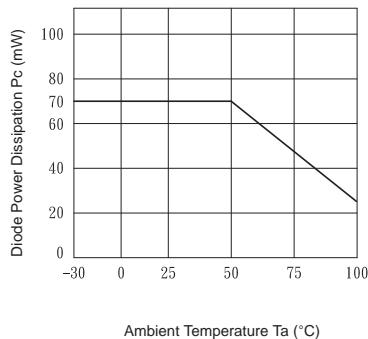


Fig.3 On-State R. M. S. Current vs. Ambient Temperature

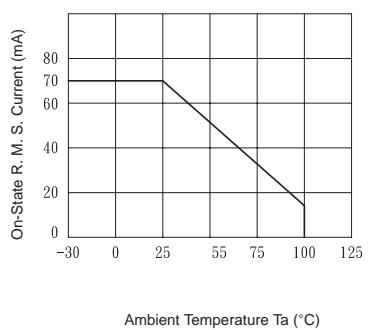


Fig.4 Total Power Dissipation vs. Ambient Temperature

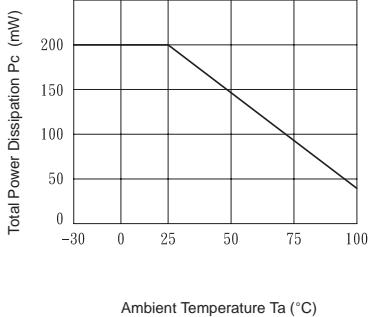


Fig.5 Peak Forward Current vs. Duty Ratio

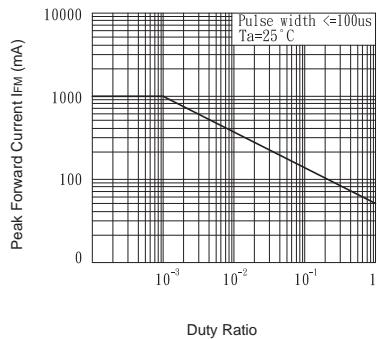


Fig.6 Forward Current vs. Forward Voltage

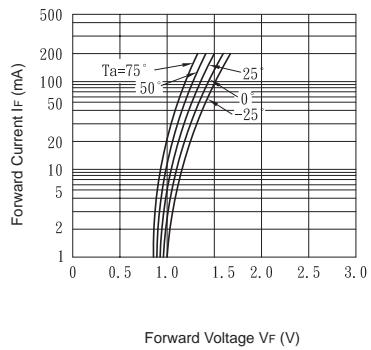


Fig.7 On-State Characteristics

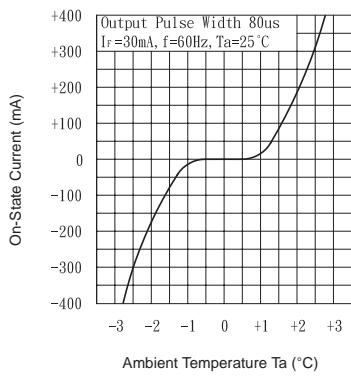


Fig.8 Leakage with LED off vs. Ambient Temperature

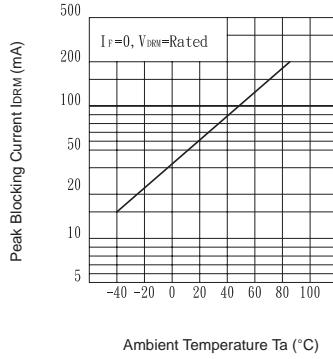


Fig.9 Trigger Current vs. Ambient Temperature

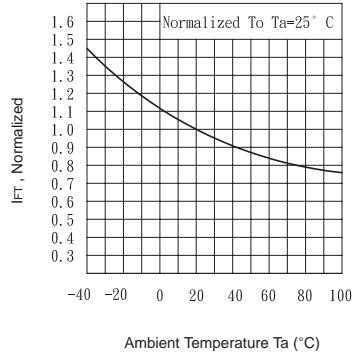


Fig.10 Inhibit Voltage vs. Ambient Temperature

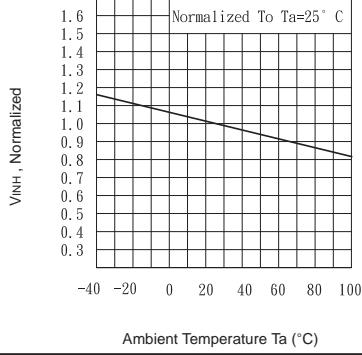


Fig.11 IdRM2, Leakage in Inhibit vs. Ambient Temperature

