NSSH NBO

R1511x-Y Series

300 mA 36 V Input Regulator for High Temperature Applications

NO. EA-345-221111

OUTLINE

The R1511x is a CMOS-based high-voltage resistant and fast response voltage regulator that provides the minimum 300mA of output current. Internally, R1511x consists of an Output Short-circuit Protection Circuit, an Over-current Protection Circuit, and a Thermal Shutdown Circuit in addition to the basic regulator circuits. The operating temperature range is between –40°C to 105°C, and the maximum input voltage is 36V. All these features allow the R1511x to become an ideal power source for industrial equipments such as FAs and smart meters.

R1511x is available in R1511xxxxB with the fixed output voltage type: 3.0V / 3.3V / 3.4V / 5.0V / 6.0V / 8.0V / 8.5V / 9.0V, and R1511x001C with adjustable output voltage type with external resistors. The output voltage accuracy is $\pm 1.0\%$.

R1511x is available in two types of packages for ultra high wattage: HSOP-6J and TO-252-5-P2.

FEATURES

	Input Voltage Range (Maximum Rating) · · · · · 3.5V to 36V (50 V)
lacktriangle	Operating Temperature Range · · · · · · -40 to 105°C (※)
lacktriangle	Supply Current ····· Typ. 100µA

● Standby Current····· Typ. 0.1µA (R1511xxxxB)

• Output Voltage Range ·······R1511xxxxB: 3.0V / 3.3V / 3.4V / 5.0V / 6.0V / 8.0V / 8.5V / 9.0V

Contact Ricoh sales representatives for other voltages.

R1511x001C: 3.0V to 12.0V (Adjustable with external resistor)

● Feedback Voltage · · · · · R1511x001C: 3.0V ±1.0% (Ta=25°C)

■ Output Voltage Temperature-Drift Coefficient · · · · · · Typ. ±60ppm/°C

Package Option ······ HSOP-6J, TO-252-5-P2

Built-in Output Short-circuit Protection Circuit ······ Typ. 50mA

Built-in Over-current Protection Circuit ····· Typ. 450mA

● Built-in Thermal Shutdown Circuit · · · · · · Thermal Shutdown Temperature: Typ. 160°C

● Ripple Rejection·····Typ. 65dB (1kHz)

Ceramic capacitors are recommended to be used with this IC

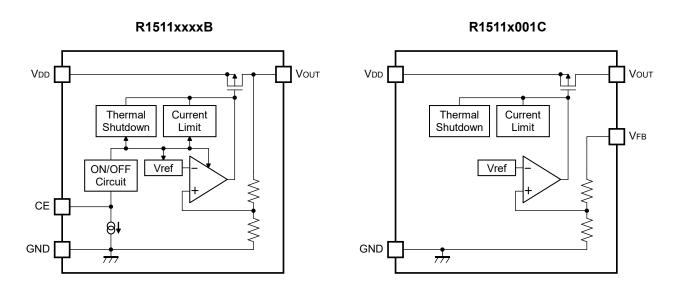
 $\cdots C_{IN}=1.0\mu F$ or more, $C_{OUT}=6.8\mu F$ or more

X This product is usable for the high-temperature applications since have passed a test at the high temperature. In addition, this product has a high-reliability since having passed Ricoh's rigorous quality standards. To distinguish from the consumer products, "-Yx" is added at the end of the product name.

APPLICATIONS

- Industrial equipments such as FAs and smart meters
- Equipments used under high-temperature conditions
- Equipments accompanied by self-heating

BLOCK DIAGRAMS



SELECTION GUIDE

The output voltage, version and the package type for the ICs can be selected at the user's request.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1511Sxxx*-E2-YE	HSOP-6J	1,000 pcs	Yes	Yes
R1511Jxxx*-T1-YE	TO-252-5-P2	3,000 pcs	Yes	Yes

xxx : Specify the set output voltage (VSET)

R1511xxxxB: 3.0V (030) / 3.3V (033) / 3.4V (034) / 5.0V (050) / 6.0V (060) / 8.0V (080) / 8.5V (085) / 9.0V (090)

Contact Ricoh sales representatives for other voltages.

R1511x001C: Only (001)

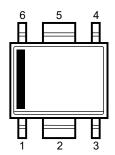
*: Specify the version

(B): Fixed output and Built-in Chip Enable (Active-high)

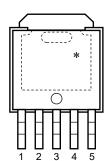
(C): Adjustable output

PIN DESCRIPTIONS





• TO-252-5-P2



HSOP-6J

Pin No.	Symbol	Description			
1	V _{DD}	Input Pin			
2	GND*1	Ground Pin			
3	GND*1	Ground Pin			
4	CE	R1511SxxxB	Chip Enable Pin (Active-high)		
4	V _{FB}	R1511S001C	Feed Back Pin		
5	GND*1	Ground Pin			
6	Vouт	Output Pin			

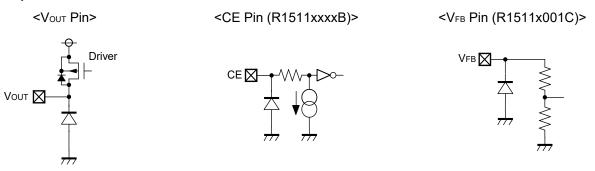
¹ The GND pin must be wired together when it is mounted on board.

TO-252-5-P2

Pin No.	Symbol	Description			
1	V _{DD}	Input Pin			
2	GND*2	Ground Pin			
3	GND*2	Ground Pin			
4	CE	R1511JxxxB	Chip Enable Pin (Active-high)		
4	V _{FB}	R1511J001C	Feed Back Pin		
5	Vout	Output Pin			

^{*)} The tab on the bottom of the package enhances thermal performance and is electrically connected to GND (substrate level). It is recommended that the tab be connected to the ground plane on the board, or otherwise be left open.

PIN EQUIVALENT CIRCUIT DIAGRAMS



^{*2} The GND pin must be wired together when it is mounted on board.

ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating		Unit
Vin	Input Voltage	-0.3 to 5	0	V
Vin	Peak Input Voltage*1	60		V
Vce	Input Voltage (CE Pin)	-0.3 to 5	0	V
V _{FB}	Input Voltage (V _{FB} Pin)	-0.3 to 50		V
Vоит	Output Voltage	-0.3 to V _{IN} +0.3 ≤ 50		V
Іоит	Output Current	450		mA
Б	Power Dissipation (HSOP-6J)*2	JEDEC STD. 51-7 2700		
P□	Power Dissipation (TO-252-5-P2)*2	JEDEC STD. 51-7 3800		mW
Tj	Junction Temperature	-40 to 125		°C
Tstg	Storage Temperature Range	-55 to 12	.5	°C

^{*1} Duration time: 200ms

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Rating	Unit
Vin	Input Voltage	3.5 to 36	V
Ta	Operating Temperature Range	-40 to 105	°C

RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

^{*2} Refer to PACKAGE INFORMATION for detailed information.

ELECTRICAL CHARACTERISTICS

C_{IN}=1.0μF, C_{OUT}=6.8μF, unless otherwise noted.

The specifications surrounded by \square are guaranteed by design engineering at -40°C \leq Ta \leq 105°C.

R1511xxxxB	(Ta=25°C)
INIUIIAAAAD	(1a-25 O)

Symbol	Item	Cond	litions	Min.	Тур.	Max.	Unit
Iss	Supply Current	VIN=VSET+1.0V, IOUT	=0mA		100	180	μА
Istandby	Standby Current	VIN=36V, VCE=0V			0.1	2.0	μΑ
Vout	Output Voltage	VIN=VSET+2.0V	Ta=25°C	×0.99		×1.01	V
V 001	Output voltage	Iouт=1mA	–40°C≤Ta≤105°C	×0.98		×1.02	V
ΔV оυт	Load Regulation	VIN=VSET+2.0V	V _{SET} ≤5.0V	-20		100	mV
/ΔΙουτ	Load Negulation	1mA≤Iо∪т≤300mA	5.0V <v<sub>SET</v<sub>	-20		120	IIIV
ΔV out $/\Delta V$ in	Line Regulation	Vset+0.5V≤Vin≤36V	, Ιουτ=1mA		0.01	0.02	%/V
VDIF	Dropout Voltage	Iоит=300mA				roduct-sp naracteris	
ILIM	Output Current Limit	V _{IN} =V _{SET} +2.5V			450		mA
Isc	Short Current Limit	Vоит=0V			50		mA
VCEH	CE Input Voltage "H"			2.2		36	V
VCEL	CE Input Voltage "L"			0		1.0	V
	OF Dull days Overset	V _{CE} =5.0V			0.2	0.6	^
I PD	CE Pull-down Current	Vce=36V			0.5	1.3	μA
TTSD	Thermal Shutdown Temparature	Junction Temperature			160		°C
Trsr	Thermal Shutdown Released Temperature	Junction Temperate	ure		135		°C

All test items listed under Electrical Characteristics are done under the pulse load condition (Tj≈Ta=25°C).

_			
01	151	14,	·V
_	151		X = I

Product-specific Electrical Characteristics

The specifications surrounded by \square are guaranteed by design engineering at -40°C \leq Ta \leq 105 °C.

 $(Ta = 25^{\circ}C)$

Product Name	$(1a = 25^{\circ}C)$		roduct $(Ta = 25^{\circ}C)$ $(-40^{\circ}C \le Ta \le 105^{\circ}C)$			V _{DIF} (V) (I _{OUT} = 300 mA)		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	TYP.	MAX.
R1511x30xx	2.970	3.000	3.030	2.940	3.000	3.060	0.980	1.500
R1511x33xx	3.267	3.300	3.333	3.234	3.300	3.366	0.040	4.400
R1511x34xx	3.366	3.400	3.434	3.332	3.400	3.468	0.940	1.400
R1511x50xx	4.950	5.000	5.050	4.900	5.000	5.100	0.640	1.000
R1511x60xx	5.940	6.000	6.060	5.880	6.000	6.120	0.590	0.900
R1511x80xx	7.920	8.000	8.080	7.840	8.000	8.160	0.540	0.800
R1511x85xx	8.415	8.500	8.585	8.330	8.500	8.670	0.470	0.700
R1511x90xx	8.910	9.000	9.090	8.820	9.000	9.180	0.470	0.700

 V_{OUT} = V_{FB} , C_{IN} =1.0μF, C_{OUT} =6.8μF, $un\underline{less}$ otherwise noted.

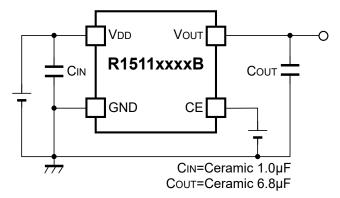
The specifications surrounded by ___ are guaranteed by design engineering at -40°C ≤ Ta ≤ 105 °C.

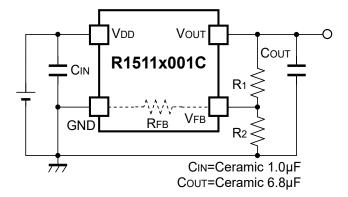
R1511x001C (Ta=25°C)

Symbol	Item	Cond	ditions	Min.	Тур.	Max.	Unit
Iss	Supply Current	VIN=4.0V, IOUT=0m	A		100	180	μΑ
Vout	Output Valtage	V _{IN} =5.0V	Ta=25°C	2.97		3.03	V
V 001	Output Voltage	Iоит=1mA	–40°C≤Ta≤105°C	2.94		3.06	V
Δ V ουτ /Δ I ουτ	Load Regulation	V _{IN} =5.0V 1mA≤I _{О∪т} ≤300mA				40	mV
Δ V out /Δ V in	Line Regulation	Vset+0.5V≤Vin≤36V Iout=1mA			0.01	0.02	%/V
VDIF	Dropout Voltage	І оυт= 300mA			0.98	1.5	V
Ішм	Output Current Limit	VIN= VSET+2.5V			450		mA
Isc	Short Current Limit	Vоит=0V			50		mA
R _{FB}	V _{FB} Pin Resistanse			1.0	3.0		МΩ
Trsd	Thermal Shutdown Temparature	Junction Temperature			160		°C
TTSR	Thermal Shutdown Released Temperature	Junction Temperature			135		°C

All test items listed under Electrical Characteristics are done under the pulse load condition (Tj≈Ta=25°C).

TYPICAL APPLICATIONS





TECHNICAL NOTES

PCB Layout and GND Wiring

Ensure the V_{DD} and GND lines are sufficiently robust. If their impedance is too high, noise pickup or unstable operation may result. Connect a C_{IN} capacitor with 1.0 μ F or more value between the V_{DD} and GND pins, and as close as possible to the pins. Likewise, connect a C_{OUT} capacitor with suitable values between the V_{OUT} and GND pins, and as close as possible to the pins (refer to the Typical Application above).

In the case of using HSOP-6J package, make sure to wire No. 2, No. 3, and No. 5 pins to the GND plane. Also, in the case of using TO-252-5-P2 package, make sure to wire No. 2 and No. 3 pins to the GND plane.

Phase Compensation

In the R1511x, phase compensation is provided to secure stable operation even when the load current is varied. For this purpose, make sure to use a Cout capacitor.

In case of using a tantalum type capacitor and the ESR (Equivalent Series Resistance) value of the capacitor is large, the output might be unstable. Evaluate the circuit including consideration of frequency characteristics.

Depending on the capacitor size, manufacturer, and part number, the bias characteristics and temperature characteristics are different. Evaluate the circuit taking actual characteristics into account.

Thermal Shutdown

R1511x contains a thermal shutdown circuit, which stops regulator operation if the junction temperature of R1511x becomes higher than 160°C (Typ.). Additionally, if the junction temperature after the regulator being stopped decreases to a level below 135°C (Typ.), it restarts regulator operation. As a result the operation of the thermal shutdown circuit causes the regulator repeatedly to turn off and on until the causes of overheating are removed. As a consequence a pulse shaped output voltage occurs.

Adjustable Output Voltage Setting (R1511x001C)

The output voltage of R1511x001C can be adjusted by using the external divider resistors (R1, R2). By using the following equation, the output voltage (V_{OUT}) can be determined. The voltage which is fixed inside the IC is described as V_{FB} .

$$V_{OUT} = V_{FB} x ((R1 + R2) / R2)$$

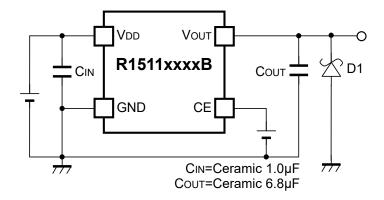
Recommended Range: 3.0 V ≤ V_{OUT} ≤ 12.0 V

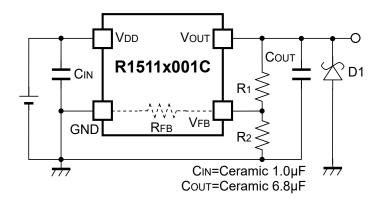
 $V_{FB} = 3.0 \text{ V}$

 R_{FB} of the R1511x001C is approximately Min. 1.0 M Ω (guaranteed by design). For better accuracy, setting R1 << R_{FB} reduces errors. The resistance value for R2 should be set to 39 k Ω or lower. It is easily affected by noises when setting the value of R1 and R2 larger, which makes the impedance of V_{FB} pin larger.

R_{IC} could be affected by the temperature, therefore evaluate the circuit taking the actual conditions of use into account when deciding the resistance values for R1 and R2.

TYPICAL APPLICATION FOR IC CHIP BREAKDOWN PREVENTION





When a sudden surge of electrical current travels along the V_{OUT} pin and GND due to a short-circuit, electrical resonance of a circuit involving an output capacitor (C_{OUT}) and a short circuit inductor generates a negative voltage and may damage the device or the load devices. Connecting a schottky diode (D1) between the V_{OUT} pin and GND has the effect of preventing damage to them.

PACKAGE INFORMATION

POWER DISSIPATION (HSOP-6J)

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following measurement conditions are based on JEDEC STD. 51-7.

Measurement Conditions

Item	Measurement Conditions
Environment	Mounting on Board (Wind Velocity = 0 m/s)
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)
Board Dimensions	76.2 mm × 114.3 mm × 0.8 mm
Copper Ratio	Outer Layer (First Layer): Less than 95% of 50 mm Square Inner Layers (Second and Third Layers): Approx. 100% of 50 mm Square Outer Layer (Fourth Layer): Approx. 100% of 50 mm Square
Through-holes	φ 0.3 mm × 28 pcs

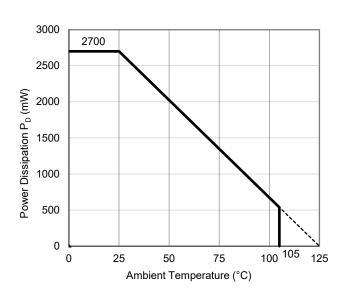
Measurement Result

 $(Ta = 25^{\circ}C, Tjmax = 125^{\circ}C)$

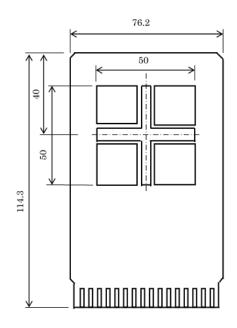
Item	Measurement Result
Power Dissipation	2700 mW
Thermal Resistance (θja)	θja = 37°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 7°C/W

 θ ja: Junction-to-Ambient Thermal Resistance

ψjt: Junction-to-Top Thermal Characterization Parameter

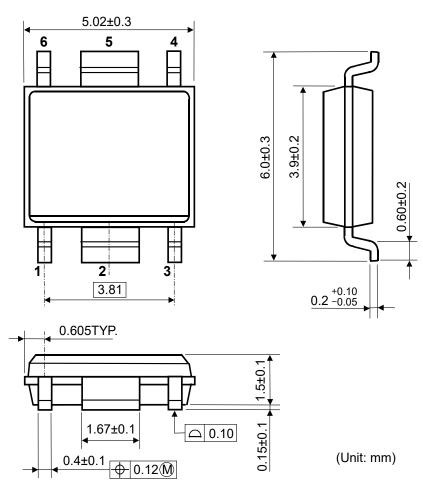


Power Dissipation vs. Ambient Temperature



Measurement Board Pattern

PACKAGE DIMENSIONS (HSOP-6J)

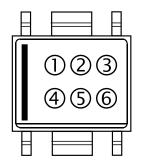


Package Dimensions (HSOP-6J)

MARK SPECIFICATION (HSOP-6J)

①②③④: Product Code ... Refer to MARK SPECIFICATION TABLE

⑤⑥: Lot Number ... Alphanumeric Serial Number



Mark Specification (HSOP-6J)

MARK SPECIFICATION TABLE (HSOP-6J)

R1511SxxxB

Product Name	1 2 3 4	V _{SET}
R1511S030B	S 3 0 B	3.0 V
R1511S033B	S 3 3 B	3.3 V
R1511S034B	S 3 4 B	3.4 V
R1511S050B	S 5 0 B	5.0 V
R1511S060B	S 6 0 B	6.0 V
R1511S080B	S 8 0 B	8.0 V
R1511S085B	S 8 5 B	8.5 V
R1511S090B	S 9 0 B	9.0 V

R1511S001C

(Adjustable Output Voltage Setting Type)

		<i>,</i> ,
Product Name	1 2 3 4	V _{SET}
R1511S001C	S 0 0 C	-

POWER DISSIPATION (TO-252-5-P2)

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following measurement conditions are based on JEDEC STD. 51-7.

Measurement Conditions

Item	Measurement Conditions
Environment	Mounting on Board (Wind Velocity = 0 m/s)
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)
Board Dimensions	76.2 mm × 114.3 mm × 0.8 mm
Copper Ratio	Outer Layer (First Layer): Less than 95% of 50 mm Square Inner Layers (Second and Third Layers): Approx. 100% of 50 mm Square Outer Layer (Fourth Layer): Approx. 100% of 50 mm Square
Through-holes	φ 0.3 mm × 21 pcs

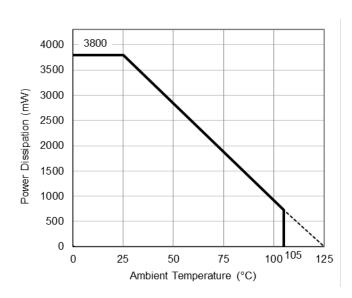
Measurement Result

 $(Ta = 25^{\circ}C, Tjmax = 125^{\circ}C)$

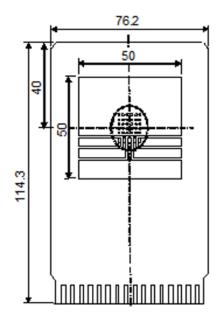
Item	Measurement Result
Power Dissipation	3800 mW
Thermal Resistance (θja)	θja = 26°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 7°C/W

 θ ja: Junction-to-Ambient Thermal Resistance

ψjt: Junction-to-Top Thermal Characterization Parameter

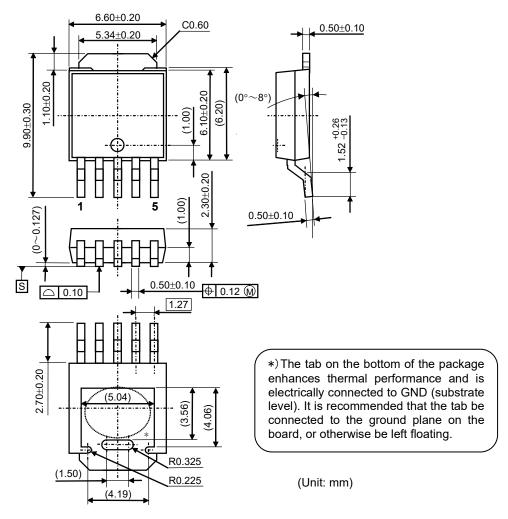


Power Dissipation vs. Ambient Temperature



Measurement Board Pattern

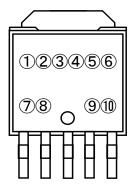
PACKAGE DIMENSIONS (TO-252-5-P2)



Package Dimensions (TO-252-5-P2)

MARK SPECIFICATION (TO-252-5-P2)

①②③④⑤⑥⑦⑧: Product Code ... Refer to MARK SPECIFICATION TABLE



Mark Specification (TO-252-5-P2)

MARK SPECIFICATION TABLE (TO-252-5-P2)

R1511JxxxB

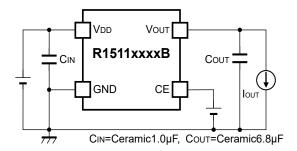
Product Name	02345678	V _{SET}
R1511J030B	H1J030B	3.0 V
R1511J033B	H 1 J 0 3 3 B	3.3 V
R1511J034B	H 1 J 0 3 4 B	3.4 V
R1511J050B	H1J050B	5.0 V
R1511J060B	H1J060B	6.0 V
R1511J080B	H1J080B	8.0 V
R1511J085B	H1J085B	8.5 V
R1511J090B	H1J090B	9.0 V

R1511J001C

(Adjustable Output Voltage Setting Type)

Product Name	02345678	V _{SET}
R1511J001C	H1J001C	-

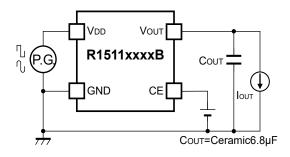
TEST CIRCUITS

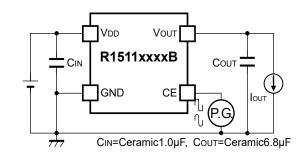


A CIN=Ceramic1.0μF, Cout=Ceramic6.8μF

R1511xxxxB Basic Test Circuit

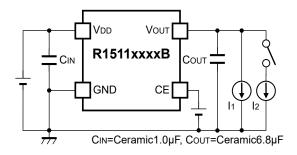
R1511xxxxB Test Circuit for Supply Current

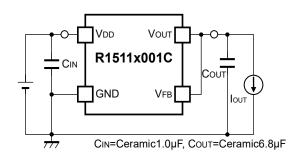




R1511xxxxB Test Circuit for Ripple Rejection and Regulator Input Transient Response

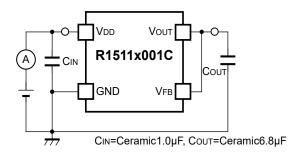
R1511xxxxB Test Circuit for CE Start-up





R1511xxxxB Test Circuit for Load Transient Response

R1511x001C Basic Test Circuit



O—VDD VOUT OR1511x001C $R_2=30$ kΩ $R_3=10$ kΩ

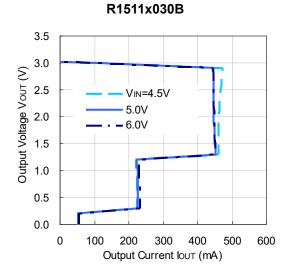
R1511x001C Test Circuit for Supply Current

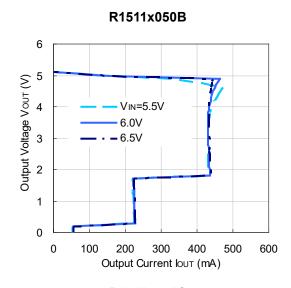
R1511x001C Case of output voltage adjustment by external resistors

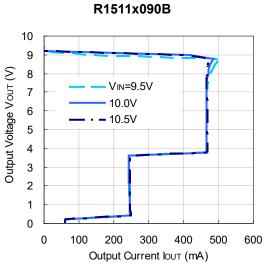
TYPICAL CHARACTERISTICS

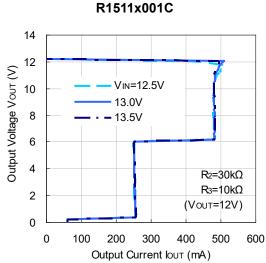
Note: Typical Characteristics are intended to be used as reference data; they are not guaranteed.

1) Output Voltage vs. Output Current (Ta=25°C)

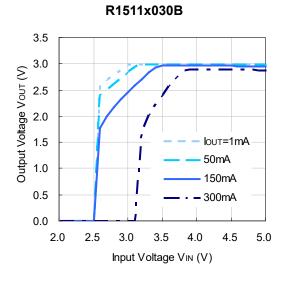


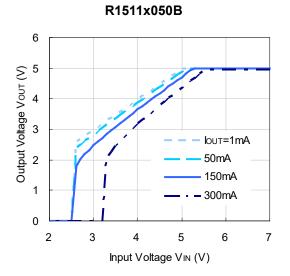






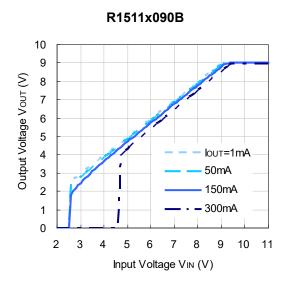
2) Output Voltage vs. Input Voltage (Ta=25°C)

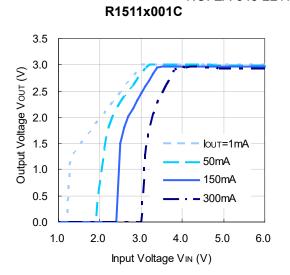




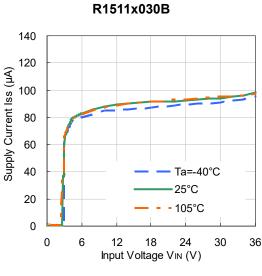
R1511x-Y

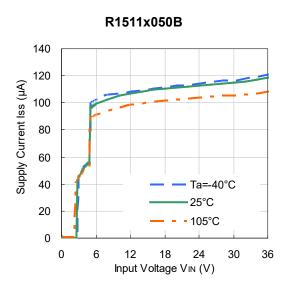
NO. EA-345-221111

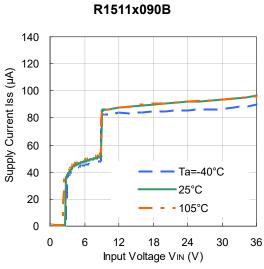


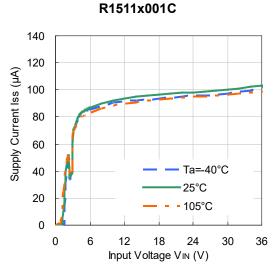


3) Supply Current vs. Input Voltage



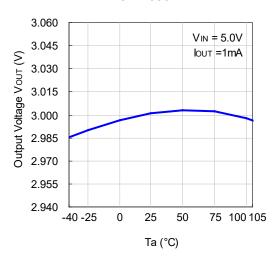




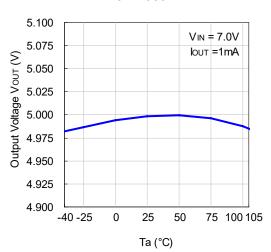


4) Output Voltage vs. Ambient Temperature

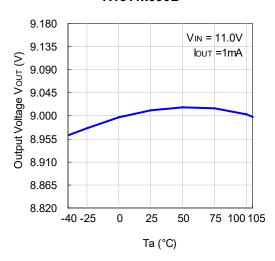




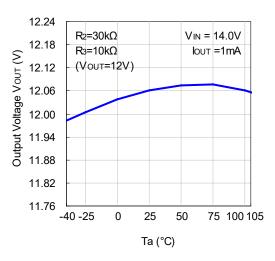
R1511x050B



R1511x090B

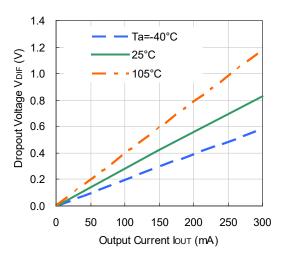


R1511x001C

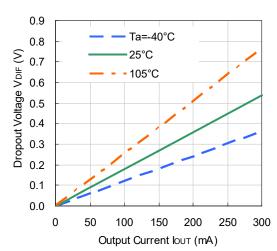


5) Dropout Voltage vs. Output Current

R1511x030B/ R1511x001C



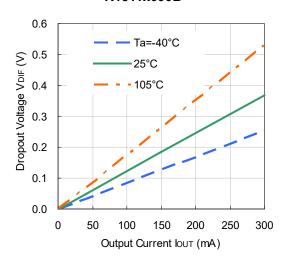
R1511x050B



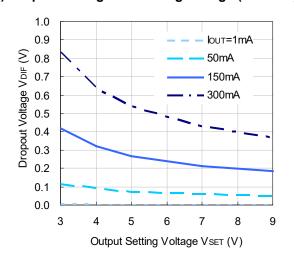
R1511x-Y

NO. EA-345-221111

R1511x090B

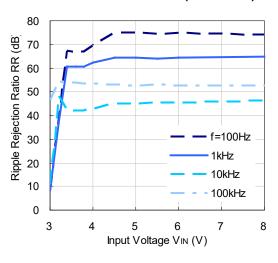


6) Dropout Voltage vs. Setting Voltage (Ta=25°C)

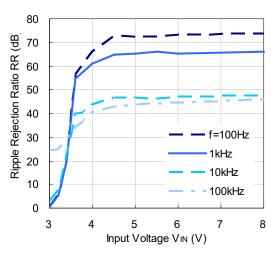


7) Ripple Rejection vs. Input Bias Voltage (Ta=25°C, Ripple=0.5Vpp)

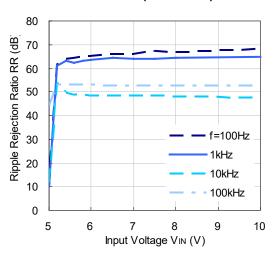
R1511x030B/R1511x001C (IOUT=1mA)



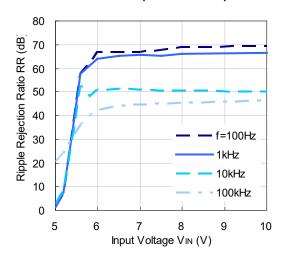
R1511x030B/R1511x001C (IOUT=100mA)



R1511x050B (IOUT=1mA)



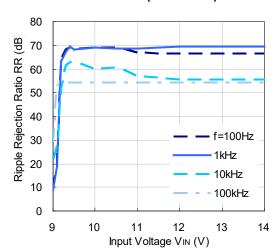
R1511x050B (IOUT=100mA)



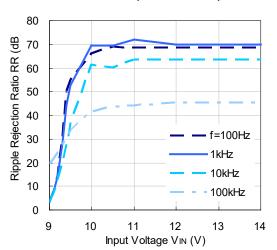
R1511x-Y

NO. EA-345-221111

R1511x090B (IOUT=1mA)

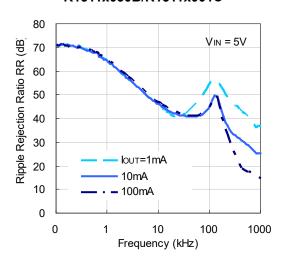


R1511x090B (IOUT=100mA)

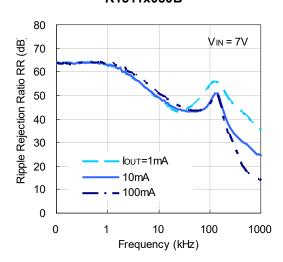


8) Ripple Rejection vs. Frequency (Ta=25°C, Ripple=0.5Vpp)

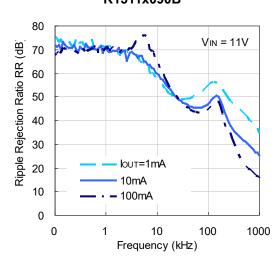
R1511x030B/R1511x001C



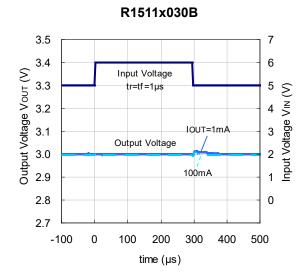
R1511x050B

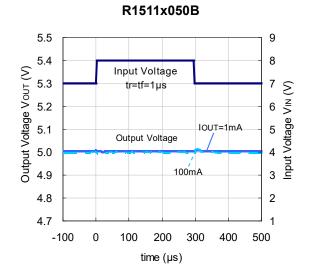


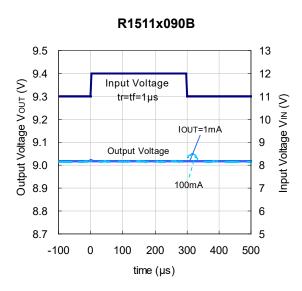
R1511x090B



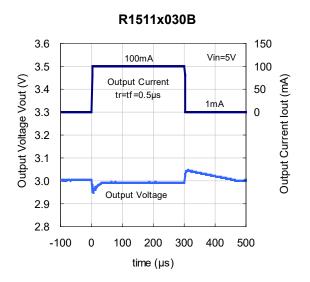
9) Input Transient Response (Ta=25°C)

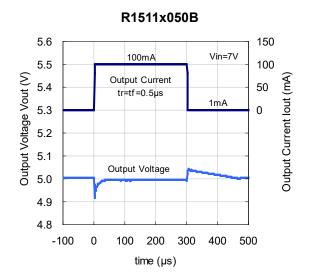


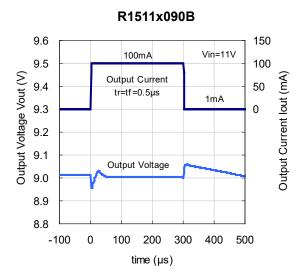




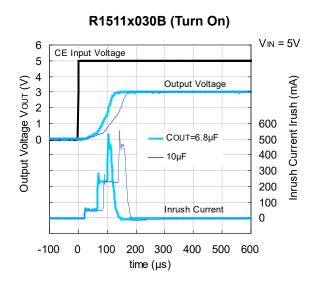
10) Load Transient Response (Ta=25°C)

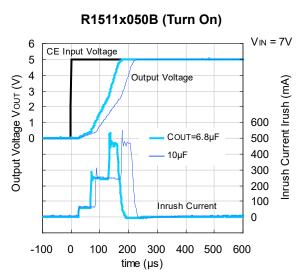


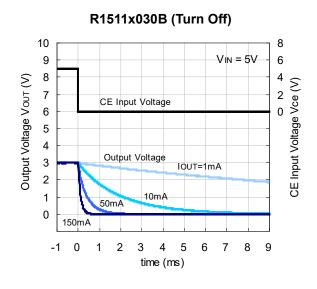


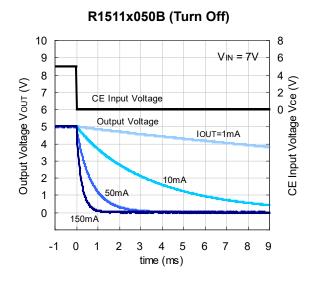


11) CE Response (Ta=25°C)





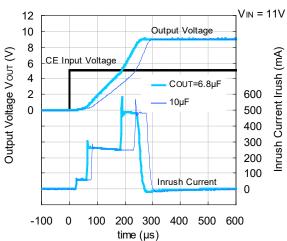




R1511x-Y

NO. EA-345-221111

R1511x090B (Turn On)



20 8 6 18 VIN = 11V 16 4 2 O N A CE Input Voltage Vce (V) Output Voltage Vo∪⊤ (V) 14 CE Input Voltage 12 10 Output Voltage IOUT=1mA 8 6 10mA

50mA

3

4

time (ms)

5 6 8

7

4

2

0

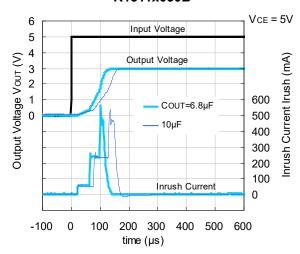
-1 0

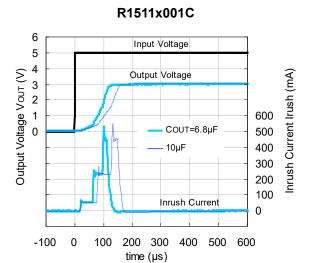
150mA

R1511x090B (Turn Off)

12) Start Up Waveform (Ta=25°C)

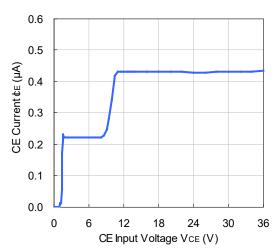






13) CE Pin Current Vs. CE Input Voltage

R1511xxxxB



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- 8. Quality Warranty
 - 8-1. Quality Warranty Period
 - In the case of a product purchased through an authorized distributor or directly from us, the warranty period for this product shall be one (1) year after delivery to your company. For defective products that occurred during this period, we will take the quality warranty measures described in section 8-2. However, if there is an agreement on the warranty period in the basic transaction agreement, quality assurance agreement, delivery specifications, etc., it shall be followed.
 - 8-2. Quality Warranty Remedies
 - When it has been proved defective due to manufacturing factors as a result of defect analysis by us, we will either deliver a substitute for the defective product or refund the purchase price of the defective product.
 - Note that such delivery or refund is sole and exclusive remedies to your company for the defective product.
 - 8-3. Remedies after Quality Warranty Period
 - With respect to any defect of this product found after the quality warranty period, the defect will be analyzed by us. On the basis of the defect analysis results, the scope and amounts of damage shall be determined by mutual agreement of both parties. Then we will deal with upper limit in Section 8-2. This provision is not intended to limit any legal rights of your company.
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- 10. The X-ray exposure can influence functions and characteristics of the products. Confirm the product functions and characteristics in the evaluation stage.
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