



General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

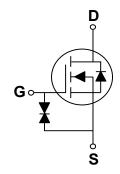
BV _{DSS}	R _{DS(ON)}	I _D
20 V	32 mΩ	4.31 A

Features

- $R_{DS(ON)} \leq 32 m \Omega @V_{GS} = 4.5 V$
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available
- ESD Protected

TSOT-23 Pin Configuration





Applications

- Notebook
- Load Switch
- · Hand-Held Instruments

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	±8	V
I _D	Drain Current - Continuous (T _C =25°C)	4.31	Α
I _{DM}	Drain Current - Pulsed (NOTE 1)	16.39	Α
P_{D}	Power Dissipation (T _C =25°C)	1.56	W
T _J	Operating Junction Temperature Range	-50 to 150	°C
T _{STG}	Storage Temperature Range	-50 to 150	°C

Thermal Characteristics					
Symbol Parameter Typ. Max				Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		80	°C/W	





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	20			V
I _{DSS}	Drain-Source Leakage Current	V_{DS} =20V , V_{GS} =0V			1	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±8V , V_{DS} =0V	-		±10	uA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} =4.5V , I_D =1A			32	
		V_{GS} =2.5V , I_D =1A			40	mΩ
		V _{GS} =1.8V , I _D =1A			55	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=250uA$	0.4		0.9	V

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge			5.7		
Q_{gs}	Gate-Source Charge	V_{DS} =6V , V_{GS} =4.5V , I_{D} =1.5A		0.6		nC
Q_{gd}	Gate-Drain Charge			1.3		
$T_{d(on)}$	Turn-On Delay Time	V_{DS} =6V , V_{GEN} =4.5V , R_{G} =6 Ω , I_{D} =1A		10.6		
T _r	Rise Time		-	4.5		ns
$T_{d(off)}$	Turn-Off Delay Time			34.3		115
T _f	Fall Time			5.7		
C _{iss}	Input Capacitance	V _{DS} =6V , V _{GS} =0V , F=200kHz		603.5		
C _{oss}	Output Capacitance			75		pF
C _{rss}	Reverse Transfer Capacitance			61.5		

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A			1.5	V

NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 3. Essentially independent of operating temperature.





Characteristics Curves

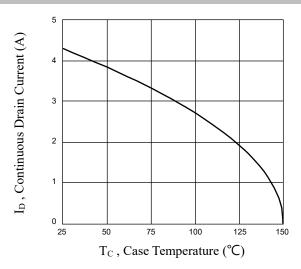


Fig.1 Continuous Drain Current vs. Tc

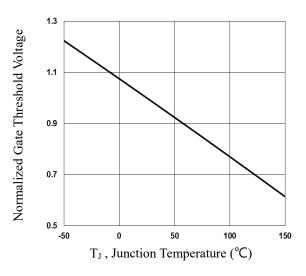


Fig.3 Normalized V_{th} vs. T_J

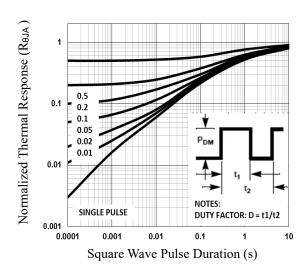


Fig.5 Normalized Transient Impedance

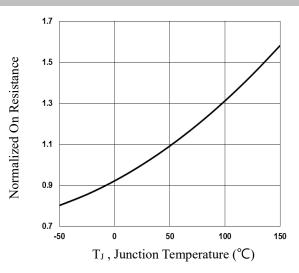


Fig.2 Normalized RDSON vs. TJ

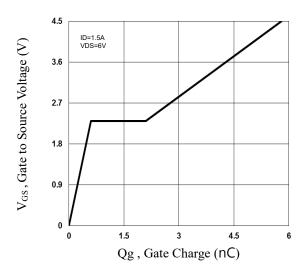


Fig.4 Gate Charge Waveform

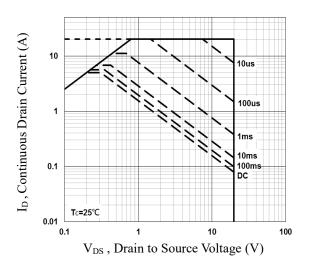
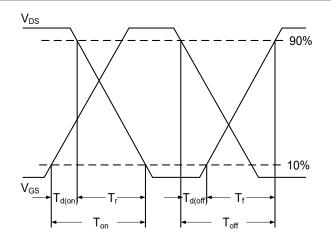


Fig.6 Maximum Safe Operation Area





Characteristics Curves



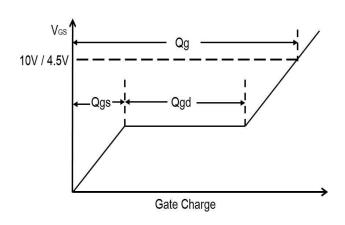
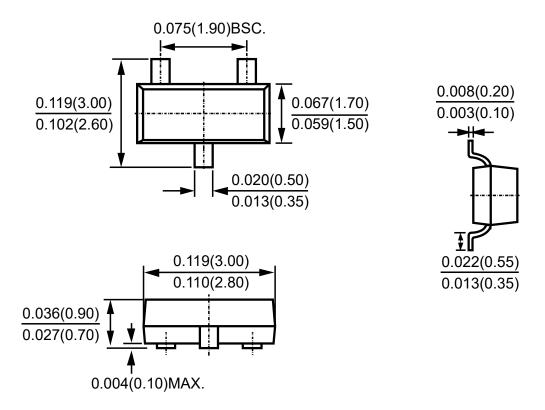


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

Package Outline Dimensions



TSOT-23
Dimensions in inches and (millimeters)





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