



General Description

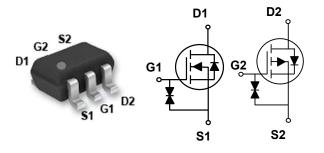
These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications

BV _{DSS}	R _{DS(ON)}	I_D
20 V	350 mΩ	0.72 A
-20 V	600 mΩ	-0.53 A

Features

- ESD Protection
- · Green Device Available
- Reliable and Rugged

SOT-363 Pin Configuration



Applications

- · Power Supply Converter Circuits
- · Load / Power Switches

Absolute Maxim	bsolute Maximum Ratings T _J =25°C unless otherwise noted								
Symbol	Parameter		Rating		Units				
V_{DS}	Drain-Source Voltage		20	-20	V				
V_{GS}	Gate-Source Voltage		±8	±8	V				
I_	Drain Current - Continuous (T _A =25°C)		0.72	-0.53	Α				
I _D	Drain Current - Continuous (T _A =70°C)		0.57	-0.42	Α				
I _{DM}	Drain Current - Pulsed (NOTE 1)		0.9	-0.9	Α				
P _D	Power Dissipation (T _A =25°C)		250		mW				
T _J	Operating Junction Temperature Range		-55 to 150		°C				
T _{STG}	Storage Temperature Range		-55 to	150	°C				

Thermal Characteristics						
Symbol	Parameter	Тур.	Max.	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		500	°C/W		





N Channel Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	20	-		V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =16V , V _{GS} =0V		-	1	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS} =±8V , V_{DS} =0V			±10	uA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} =4.5V , I_D =0.2A			350	
		V_{GS} =2.5V , I_D =0.2A			460	mΩ
		V _{GS} =1.8V , I _D =0.1A			900	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	0.48		1.0	V

Dynamic and switching Characteristics (NOTE 4)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge			1		
Q_{gs}	Gate-Source Charge	V_{DS} =10V , V_{GS} =4.5V , I_{D} =0.72A		0.3		nC
Q_{gd}	Gate-Drain Charge			0.1		
$T_{d(on)}$	Turn-On Delay Time			1.2		
T_r	Rise Time	V_{DS} =10V , V_{GS} =4.5V , R_{GEN} =6 Ω		24.5		nS
$T_{d(off)}$	Turn-Off Delay Time	, I _D =0.72A		13.6		110
T_f	Fall Time			14.6		
C _{iss}	Input Capacitance			40		
C _{oss}	Output Capacitance	V _{DS} =10V , V _{GS} =0V , F=1MHz		17		pF
C _{rss}	Reverse Transfer Capacitance			9.9		

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V_{SD}	Diode Forward Voltage	V_{GS} =0V , I_{S} =0.3A			1.1	V



TUMBB350



20V N+P Dual Channel MOSFETs

Characteristics Curves

FIG. 1- Gate Threshold Voltage

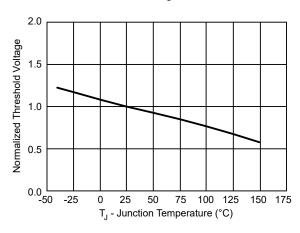


FIG. 2- Drain-Source On-Resistance

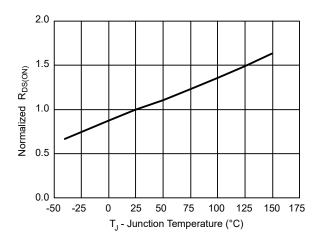


FIG. 3- Source-Drain Diode Forward

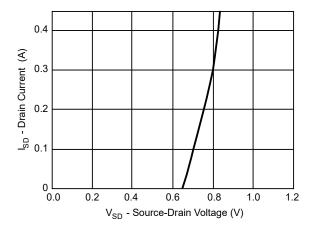


FIG. 4- Power Dissipation

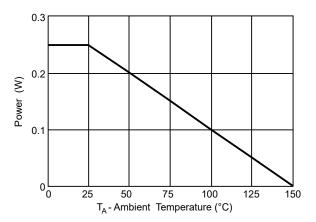
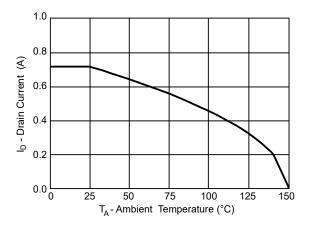


FIG. 5- Drain Current







P Channel Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V , I _D = -250uA	-20	-		V
I _{DSS}	Drain-Source Leakage Current	V _{DS} = -16V , V _{GS} = 0V		-	-1	uA
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 8V$, $V_{DS} = 0V$			±10	Α

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} = -4.5V , I_{D} = -0.2A		-	600	
		V_{GS} = -2.5V , I_{D} = -0.2A			850	mΩ
		V _{GS} = -1.8V , I _D = -0.1A			1200	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250uA$	-0.48	-	-1.0	V

Dynamic and switching Characteristics (NOTE 4)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge	V = 40V V = 45V		1		
Q_{gs}	Gate-Source Charge	V_{DS} = -10V , V_{GS} = -4.5V , I_{D} = -0.43A		0.17		nC
Q_{gd}	Gate-Drain Charge	ID0.43A		0.18		
$T_{d(on)}$	Turn-On Delay Time			0.4		
T _r	Rise Time	V _{DS} = -10V , V _{GS} = -4.5V ,		0.03		nS
$T_{d(off)}$	Turn-Off Delay Time	$R_{GEN}=6\Omega$, $I_D=-0.43A$		0.04		113
T_f	Fall Time			1.1		
C _{iss}	Input Capacitance			57.8		
C _{oss}	Output Capacitance	V _{DS} = -10V , V _{GS} = 0V , F= 1MHz		5.6		pF
C_{rss}	Reverse Transfer Capacitance			4.3		

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S = -0.22A			-1.1	V

NOTES:

- 1. Max. current is limited by bonding wire.
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 3. Essentially independent of operating temperature.
- 4. Guaranteed by design, not subject to production testing.





Characteristics Curves

FIG. 6- On-Resistance vs. V_{GS}

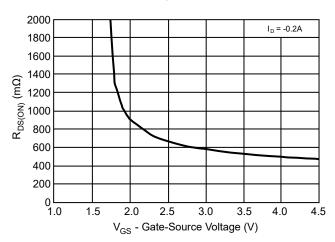


FIG. 7- Gate Threshold Voltage

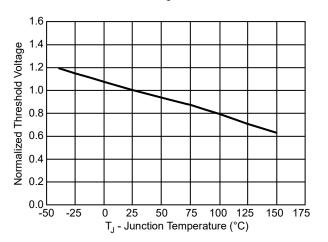


FIG. 8- Drain-Source On-Resistance

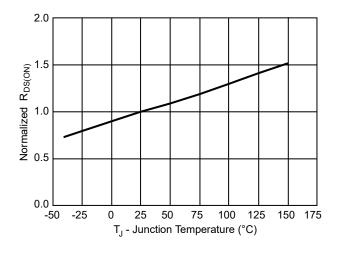
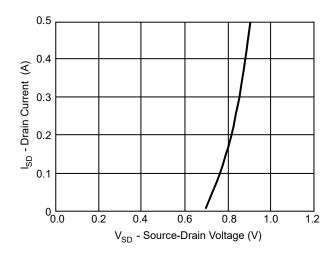
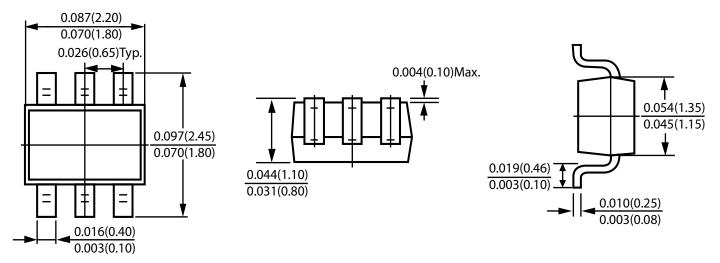


FIG. 9- Source-Drain Diode Forward



Package Outline Dimensions



SOT-363

Dimensions in inches and (millimeters)





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