



General Description

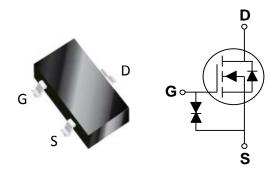
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV _{DSS}	R _{DS(ON)}	I _D
60 V	3 Ω	300 mA

Features

- 60V, 300mA, $R_{DS(ON)}$ =3 Ω @ V_{GS} =10V
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available

SOT-523 Pin Configuration



Applications

- Notebook
- · Load Switch
- · Hand-held Instruments
- Battery Protection

Absolute Maxim	bsolute Maximum Ratings T _C =25°C unless otherwise noted						
Symbol	Parameter	Rating	Units				
V_{DS}	Drain-Source Voltage	60	V				
V_{GS}	Gate-Source Voltage	±20	V				
I-	Drain Current - Continuous (T _A =25°C)	300	mA				
I _D	Drain Current - Continuous (T _A =70°C)	240	mA				
I _{DM}	Drain Current - Pulsed (NOTE 1)	1.2	Α				
P_{D}	Power Dissipation (T _C =25°C)	313	mW				
ı D	Power Dissipation - Derate above 25°C	2.5	mW/°C				
T _J	Operating Junction Temperature Range	-50 to 150	°C				
T _{STG}	Storage Temperature Range	-50 to 150	°C				
Marking Code		J					

Thermal Characteristics					
Symbol	Parameter	Тур.	Max.	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		400	°C/W	





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	60			V
I _{DSS}	IDrain-Source Leakage Current	V_{DS} =48V , V_{GS} =0V , T_{J} =25°C			1	uA
		V_{DS} =48V , V_{GS} =0V , T_{J} =85 $^{\circ}$ C			10	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±20	uA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} =10V , I_D =0.3A		1.1	3	0
		V _{GS} =4.5V , I _D =0.2A		1.3	4	12
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.2	2.0	2.5	V

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input Capacitance	V _{DS} =30V , V _{GS} =0V , F=1MHz		23	46	
C _{oss}	Output Capacitance			16	32	pF
C _{rss}	Reverse Transfer Capacitance			10	20	

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current			300	mA
I _{SM}	Pulsed Source Current				600	mA
V_{SD}	Diode Forward Voltage	V_{GS} =0V , I_S =0.2A , T_J =25 $^{\circ}$ C			1	V

NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width \leqq 300us , duty cycle \leqq 2%.
- 3. Essentially independent of operating temperature.





Characteristics Curves

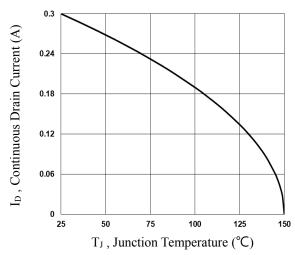
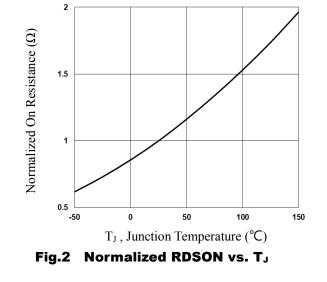


Fig.1 Continuous Drain Current vs. T_J



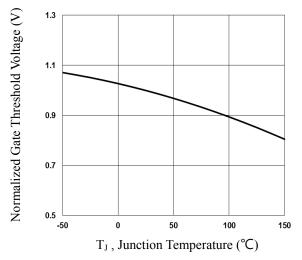
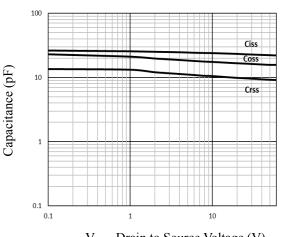


Fig.3 Normalized V_{th} vs. T_J



V_{DS}, Drain to Source Voltage (V)

Fig.4 Capacitance Characteristics

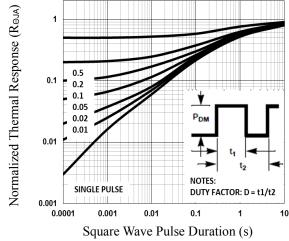


Fig.5 Normalized Transient Response

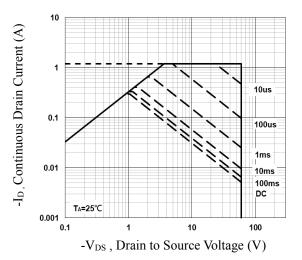
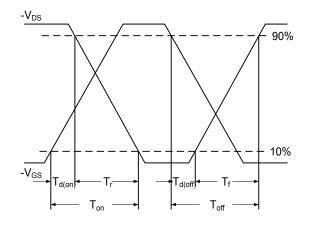


Fig.6 Maximum Safe Operation Area





Characteristics Curves



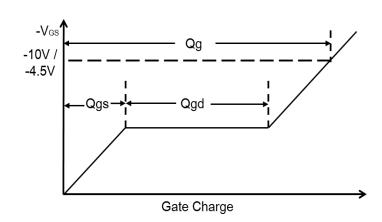
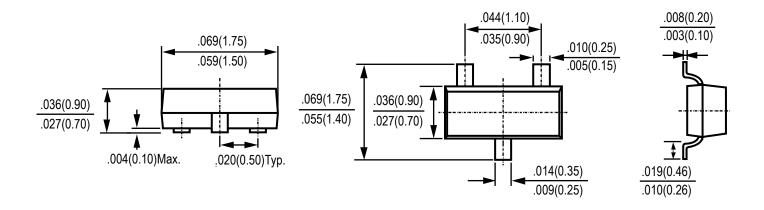


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

Package Outline Dimensions



SOT-523 Dimensions in inches and (millimeters)





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