



#### **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

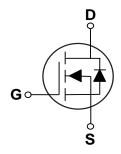
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
30 V	35 mΩ	1.8 A

#### **Features**

- 30V, 1.8A,  $R_{DS(ON)}$ =35m  $\Omega$  @ $V_{GS}$ =10V
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available

#### SOT-323 Pin Configuration





### **Applications**

- Notebook
- · Load Switch
- · Battery Protection
- · Hand-Held Instruments

bsolute Maximum Ratings T <sub>c</sub> =25°C unless otherwise noted						
Symbol	Parameter	Rating	Units			
$V_{DS}$	Drain-Source Voltage	30	V			
$V_{GS}$	Gate-Source Voltage	±12	V			
I_	Drain Current - Continuous (T <sub>C</sub> =25°C)	1.8	Α			
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> =100°C)	1.15	Α			
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	7.2	Α			
$P_{D}$	Power Dissipation (T <sub>C</sub> =25°C)	275	mW			
' D	Power Dissipation - Derate above 25°C	2.2	mW/°C			
$T_J$	Operating Junction Temperature Range	-50 to 150	°C			
$T_{STG}$	Storage Temperature Range	-50 to 150	°C			
Marking Code		f				

Thermal Characteristics					
Symbol	Parameter	Тур.	Max.	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		450	°C/W	





### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	30			٧
I <sub>DSS</sub>	IDrain-Source Leakage Current	$V_{DS}$ =30V , $V_{GS}$ =0V , $T_J$ =25°C			1	uA
		$V_{DS}$ =24V , $V_{GS}$ =0V , $T_J$ =125°C			10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±12V , $V_{DS}$ =0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	IStatic Drain-Source On-Resistance	$V_{GS}$ =10V , $I_D$ =1A		29	35	mΩ
		$V_{GS}$ =4.5V , $I_D$ =0.5A		35	46	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	0.5	0.8	1.2	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>S</sub> =2A		2.5		S

#### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	V <sub>DS</sub> =24V , V <sub>GS</sub> =10V , I <sub>D</sub> =2A		7.4	11	
$Q_gs$	Gate-Source Charge	$V_{DS}=24V$ , $V_{GS}=10V$ , $I_{D}=2A$ (NOTE 2 \ 3)		0.9	1.3	nC
$Q_gd$	Gate-Drain Charge	(10122 3)		1.4	2.1	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}$ =24V , $V_{GS}$ =10V , $R_{G}$ =3.3 $\Omega$ , $I_{D}$ =1A (NOTE 2 $\times$ 3)		2.2	4.5	
T <sub>r</sub>	Rise Time			6.9	13.8	nS
$T_{d(off)}$	Turn-Off Delay Time			15.2	30.4	113
$T_f$	Fall Time			4.5	9	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , F=1MHz		241	360	
C <sub>oss</sub>	Output Capacitance			33	50	pF
$C_{rss}$	Reverse Transfer Capacitance			15	23	
Rg	Gate resistance	$V_{GS}$ =0V , $V_{DS}$ =0V , F=1MHz		1.1		Ω

#### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current			1.8	Α
I <sub>SM</sub>	Pulsed Source Current	V <sub>G</sub> -V <sub>D</sub> -0V, Force Current			3.6	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =0.2A , T <sub>J</sub> =25°C			1	V
Trr	_	$V_{GS}$ =0V , $I_S$ =2A ,		87		nS
Qrr	Reverse Recovery Charge	di/dt=100A/us , T <sub>J</sub> =25°C		390		nC

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- ${\it 3. Essentially independent of operating temperature.}\\$





#### **Characteristics Curves**

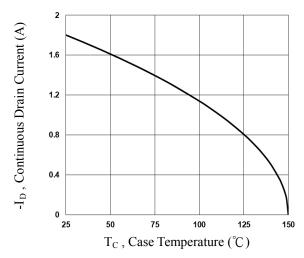


Fig.1 Continuous Drain Current vs.  $T_c$ 

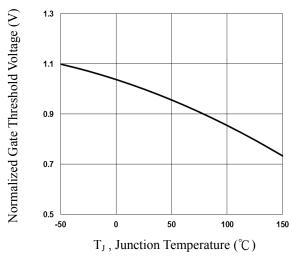


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

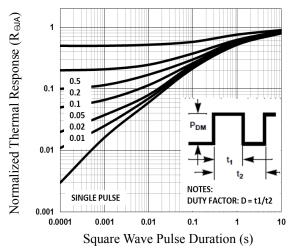


Fig.5 Normalized Transient Response

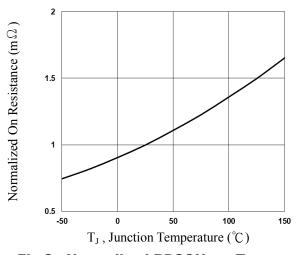


Fig.2 Normalized RDSON vs. T<sub>J</sub>

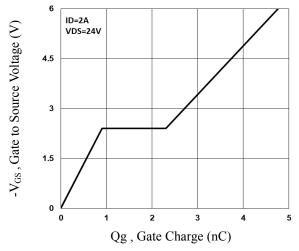


Fig.4 Gate Charge Waveform

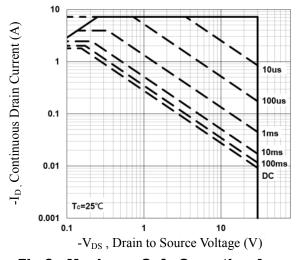
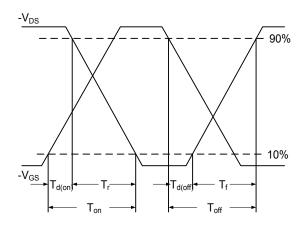


Fig.6 Maximum Safe Operation Area





#### **Characteristics Curves**





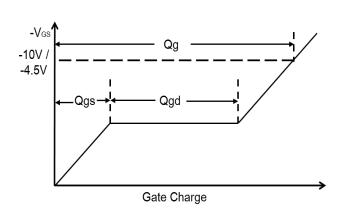
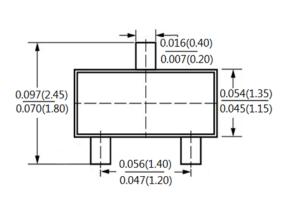
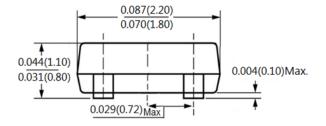
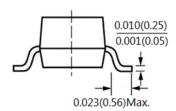


Fig.8 Gate Charge Waveform

### **Package Outline Dimensions**







**SOT-323** Dimensions in inches and (millimeters)





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