



General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

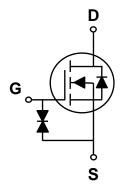
BV _{DSS}	R _{DS(ON)}	I _D
20 V	300 mΩ	800 mA

Features

- 20V, 800mA, $R_{DS(ON)}$ =300m Ω @ V_{GS} =4.5V
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available
- · Suit for 1.5V Gate Drive Applications

SOT-323 Pin Configuration





Applications

- Notebook
- · Load Switch
- · Battery Protection
- · Hand-Held Instruments

Symbol	Parameter	Rating	Unit	
V_{DS}	Drain-Source Voltage	20	V	
V_{GS}	Gate-Source Voltage	±8	V	
I _D	Drain Current - Continuous (T _C =25°C)	800	mA	
	Drain Current - Continuous (T _C =100°C)	510	m <i>A</i>	
I _{DM}	Drain Current - Pulsed (NOTE 1)	3.2	Α	
P_{D}	Power Dissipation (T _C =25°C)	275	m۷	
ı D	Power Dissipation - Derate above 25°C	2.2	mW/	
T_J	Operating Junction Temperature Range	-50 to 150	°C	
T_{STG}	Storage Temperature Range	-50 to 150	°C	
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Thermal Characteristics						
Symbol	Parameter	Тур.	Max	Unit		
$R_{ heta JA}$	Thermal Resistance Junction to Ambient		450	°C/W		





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	20			V
I _{DSS}	IDrain-Source Leakage Current	V_{DS} =20V , V_{GS} =0V , T_J =25 $^{\circ}$ C			1	uA
		V _{DS} =16V , V _{GS} =0V , T _J =125°C			10	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±6V , V_{DS} =0V			±20	uA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} =4.5V , I_D =0.5A		200	300	
		V_{GS} =2.5V , I_D =0.4A		235	400	
		V _{GS} =1.8V , I _D =0.2A		295	550	mΩ
		V _{GS} =1.5V , I _D =0.1A		365	800	
		V _{GS} =1.2V , I _D =0.1A		600	1500	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	0.3	0.6	1.0	V

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge (NOTE 2 · 3)			1	2	
Q_{gs}	Gate-Source Charge (NOTE 2 \ 3)	V_{DS} =10V , V_{GS} =4.5V , I_{D} =0.5A		0.26	0.5	nC
Q_{gd}	Gate-Drain Charge (NOTE 2 \ 3)	7		0.2	0.4	
$T_{d(on)}$	Turn-On Delay Time (NOTE 2 \ 3)			5	10	
T _r	Rise Time (NOTE 2 \ 3)	V_{DD} =10V , V_{GS} =4.5V , R_{G} =10 Ω		3.5	7	ns
$T_{d(off)}$	Turn-Off Delay Time (NOTE 2 \ 3)	, I _D =0.5A		14	28	115
T_f	Fall Time (NOTE 2 · 3)			6	12	
C _{iss}	Input Capacitance			38.2	75	
C _{oss}	Output Capacitance	V_{DS} =10V , V_{GS} =0V , F=1MHz		14.4	28	pF
C_{rss}	Reverse Transfer Capacitance]		6	12	

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V_G = V_D = $0V$, Force Current			8.0	Α
I _{SM}	Pulsed Source Current				1.6	Α
V_{SD}	Diode Forward Voltage	V_{GS} =0V , I_{S} =0.2A , T_{J} =25 $^{\circ}$ C			1	V

NOTES:

- ${\bf 1.}\ Repetitive\ Rating: Pulsed\ width\ limited\ by\ maximum\ junction\ temperature.$
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- $\ \ 3.\ Essentially\ independent\ of\ operating\ temperature.$





Characteristics Curves

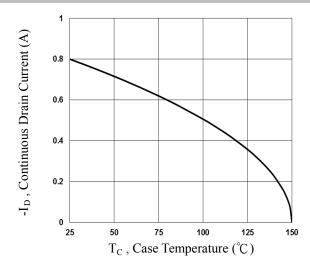


Fig.1 Continuous Drain Current vs. T_c

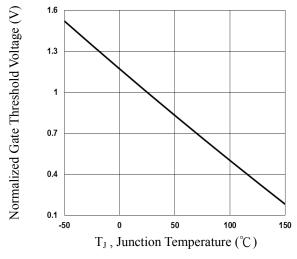


Fig.3 Normalized V_{th} vs. T_J

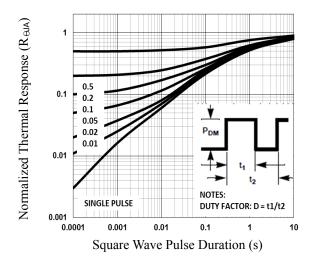


Fig.5 Normalized Transient Response

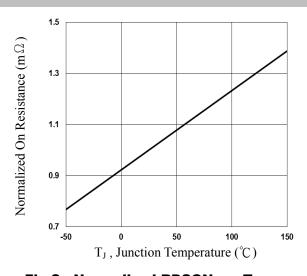


Fig.2 Normalized RDSON vs. T_J

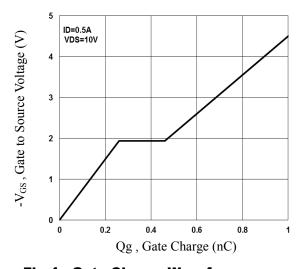


Fig.4 Gate Charge Waveform

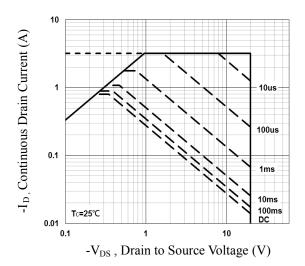
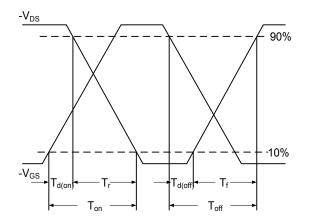


Fig.6 Maximum Safe Operation Area





Characteristics Curves



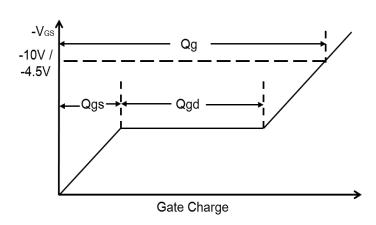
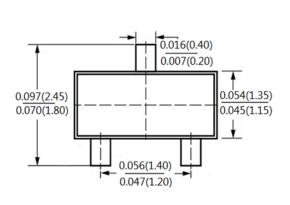
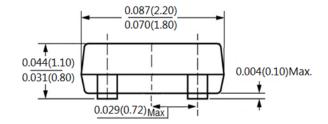


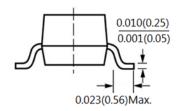
Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

Package Outline Dimensions







SOT-323Dimensions in inches and (millimeters)





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