



#### **General Description**

The TNMPM650 is the high cell density trenched P-ch MOSFETs, which provides excellent  $R_{\text{DSON}}$  and efficiency for most of the small power switching and load switch applications.

The TNMPM650 meets the RoHS and Green Product requirement with full function reliability approved.

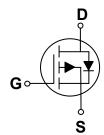
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	Ι <sub>D</sub>
-100V	650 mΩ	-0.9 A

#### **Features**

- $R_{DS(ON)} \leq 650 \text{m}\Omega @V_{GS} = -10V$
- · Super Low Gate Charge
- · Excellent Cdv/dt effect decline
- · Green Device Available
- · Advanced high cell density Trench technology

#### SOT-23 Pin Configuration





#### **Applications**

- · Battery Protection
- · Load Switch
- · Uninterruptible Power Supply

Symbol	Parameter Rating		Units
$V_{DS}$	Drain-Source Voltage	-100	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current - Continuous (T <sub>A</sub> =25°C)	-0.9	Α
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	-1.8	Α
$P_D$	Total Power Dissipation (T <sub>A</sub> =25°C) (NOTE 3)	1	W
$T_J$	Operating Junction Temperature Range	-50 to 150	°C
$T_{STG}$	Storage Temperature Range	-50 to 150	°C
Marking Code		AE , 0107	

Thermal Characteristics					
Symbol	Parameter	Тур.	Max	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		125	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction to Case		80	°C/W	





#### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ = 0V , $I_D$ = -250uA	-100			V
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ = -80V , $V_{GS}$ = 0V , $T_{J}$ =25 $^{\circ}$ C			-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = ±20V , $V_{DS}$ = 0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	IStatic Drain-Source On-Resistance	$V_{GS} = -10V$ , $I_{D} = -0.8A$			650	mΩ
		$V_{GS}$ = -4.5V , $I_{D}$ = -0.4A			700	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250uA$	-1.0		-2.5	V
gfs	Forward Transconductance	$V_{DS}$ = -5V , $I_{D}$ = -0.8A		3		S

#### **Dynamic and switching Characteristics**

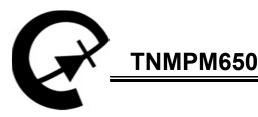
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	V - 45V V - 45V		4.5		
$Q_{gs}$	Gate-Source Charge	V <sub>DS</sub> = -15V , V <sub>GS</sub> = -4.5V , I <sub>D</sub> = -0.5A		1.14		nC
$Q_{gd}$	Gate-Drain Charge	1 <sub>D</sub> 0.5/\		1.5		
T <sub>d(on)</sub>	Turn-On Delay Time			13.6		
T <sub>r</sub>	Rise Time	$V_{DD}$ = -50V , $V_{GS}$ = -10V , $R_{G}$ = 3.3 $\Omega$ , $I_{D}$ = -0.5A		6.8		
$T_{d(off)}$	Turn-Off Delay Time			34		ns
T <sub>f</sub>	Fall Time			3		
$C_{iss}$	Input Capacitance	V <sub>DS</sub> = -15V , V <sub>GS</sub> = 0V , F= 1MHz		553		
$C_{oss}$	Output Capacitance			29		pF
$C_{rss}$	Reverse Transfer Capacitance			20		
Rg	Gate resistance	V <sub>GS</sub> =0V , V <sub>DS</sub> =0V , F=1MHz		16		Ω

#### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current (NOTE 4)	V <sub>G</sub> = V <sub>D</sub> = 0V,Force Current		-	-0.9	Α
I <sub>SM</sub>	Pulsed Source Current (NOTE 2 \ 4)			-	-1.8	Α
$V_{SD}$	Diode Forward Voltage (NOTE 2)	$V_{GS}$ = 0V , $I_{S}$ = -1A , $T_{J}$ = 25 $^{\circ}$ C			-1.2	V

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 3. The power dissipation is limited by 150°C junction temperature.
- 4. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.





#### **Characteristics Curves**

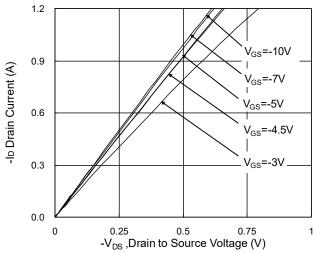


Fig.1 Typical Output Characteristics

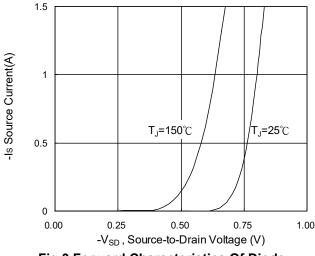


Fig.3 Forward Characteristics Of Diode

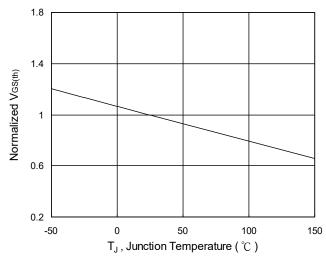


Fig.5 Normalized  $V_{\text{GS(th)}}$  vs.  $T_{\text{J}}$ 

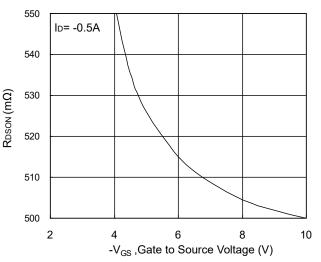


Fig.2 On-Resistance vs. Gate-Source Voltage

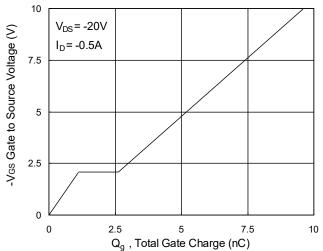


Fig.4 Gate-Charge Characteristics

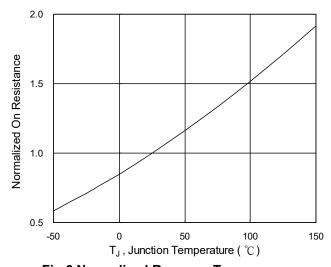


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>





#### **Characteristics Curves**

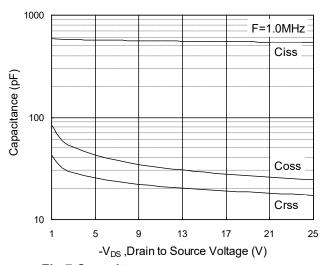


Fig.7 Capacitance

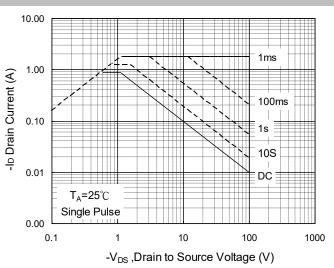


Fig.8 Safe Operating Area

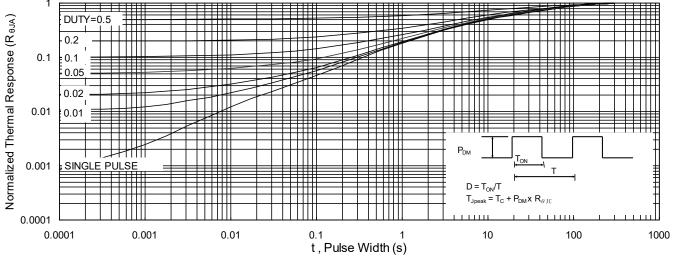


Fig.9 Normalized Maximum Transient Thermal Impedance

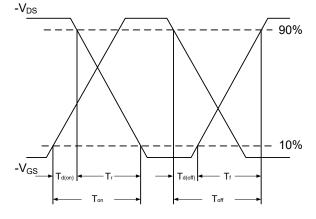


Fig.10 Switching Time Waveform

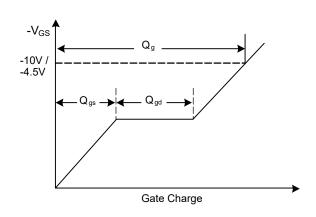
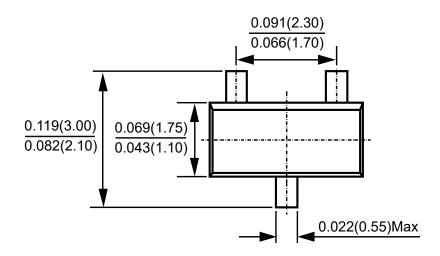


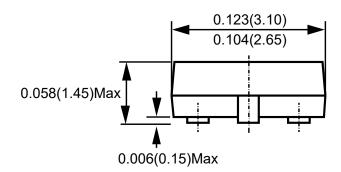
Fig.11 Gate Charge Waveform

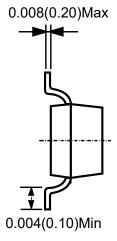




#### **Package Outline Dimensions**







**SOT-23** Dimensions in inches and (millimeters)





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