



### General Description

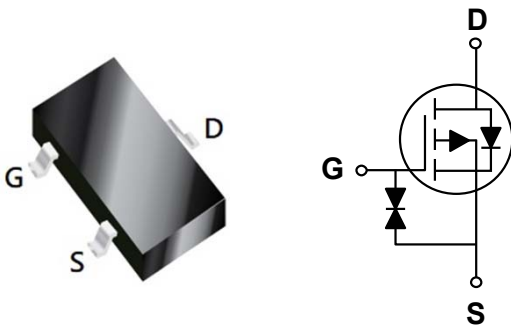
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

<b>BV<sub>DSS</sub></b>	<b>R<sub>DS(ON)</sub></b>	<b>I<sub>D</sub></b>
-20 V	460 mΩ	-0.64 A

### Features

- $R_{DS(ON)} \leq 460m\Omega @ V_{GS} = -4.5V$
- Fast Switching
- Green Device Available
- ESD Protection

SOT-23 Pin Configuration



### Applications

- Small Signal Switch
- Load Switch

### Absolute Maximum Ratings $T_J=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-Source Voltage	±12	V
$I_D$	Drain Current - Continuous ( $T_A=25^\circ\text{C}$ )	-0.64	A
$I_{DM}$	Drain Current - Pulsed ( $T_A=25^\circ\text{C}$ ) (NOTE 1)	-1.5	A
$P_D$	Power Dissipation ( $T_A=25^\circ\text{C}$ )	0.35	W
$T_J$	Operating Junction Temperature Range	-55 to 150	°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
Marking Code		11	

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	350	°C/W



### Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

#### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu\text{A}$	-20	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-16V, V_{GS}=0V$	---	---	-1	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	---	---	$\pm 10$	$\mu\text{A}$

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-4.5V, I_D=-0.55\text{A}$	---	---	460	m $\Omega$
		$V_{GS}=-2.5V, I_D=-0.45\text{A}$	---	---	865	
		$V_{GS}=-1.8V, I_D=-0.35\text{A}$	---	---	1556	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu\text{A}$	-0.5	---	-1	V
$g_{fs}$	Forward Transconductance	$V_{DS}=-5V, I_S=-0.55\text{A}$	---	1	---	S

#### Dynamic and switching Characteristics (NOTE 3)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$Q_g$	Total Gate Charge	$V_{DS}=-10V, V_{GS}=-4.5V, I_D=-0.55\text{A}$	---	1	---	nC
$Q_{gs}$	Gate-Source Charge		---	0.17	---	
$Q_{gd}$	Gate-Drain Charge		---	0.18	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=-10V, V_{GS}=-4.5V, R_{GEN}=3\Omega, I_D=-1\text{A}$	---	0.4	---	nS
$T_r$	Rise Time		---	0.03	---	
$T_{d(off)}$	Turn-Off Delay Time		---	0.04	---	
$T_f$	Fall Time		---	1.1	---	
$C_{iss}$	Input Capacitance	$V_{DS}=-10V, V_{GS}=0V, F=1\text{MHz}$	---	58	---	pF
$C_{oss}$	Output Capacitance		---	5.7	---	
$C_{riss}$	Reverse Transfer Capacitance		---	4.4	---	

#### Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=-0.55\text{A}$	---	---	-1.1	V

#### NOTES :

1. Max. current is limited by junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$ .
3. Guaranteed by design, not subject to production testing.



### Characteristics Curves

FIG. 1-Drain Current

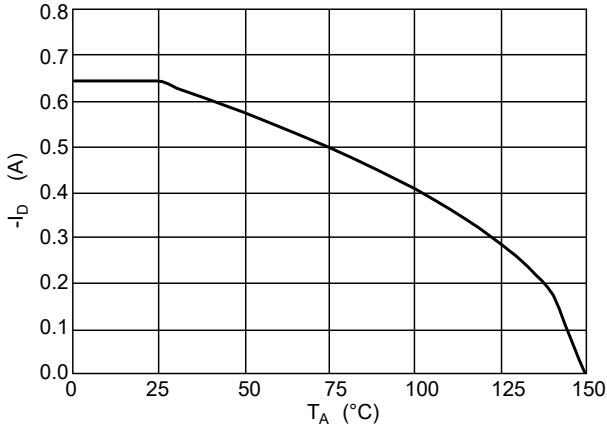


FIG. 2-Gate Threshold Voltage

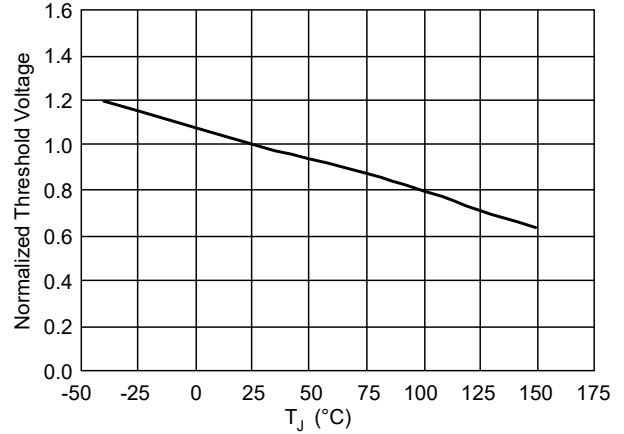


FIG. 3-Drain-Source On-Resistance

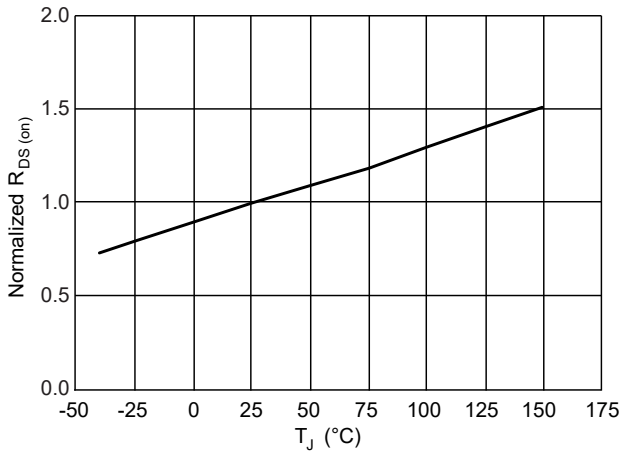


FIG. 4-Gate Charge Characteristics

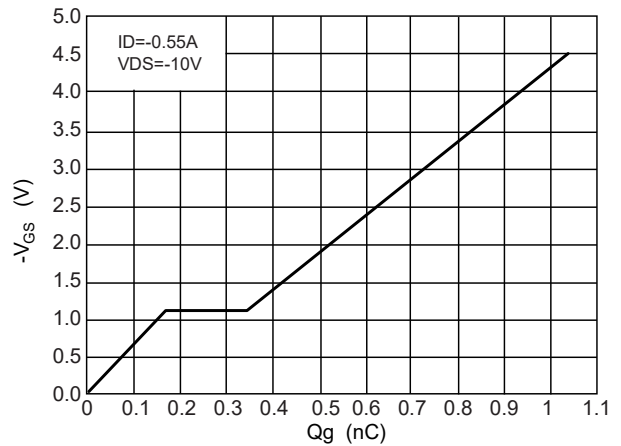


FIG. 5- $R_{\theta JA}$  Transient Thermal Impedance

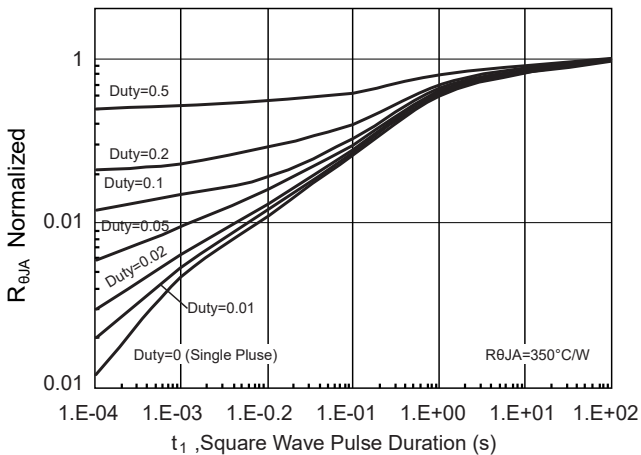
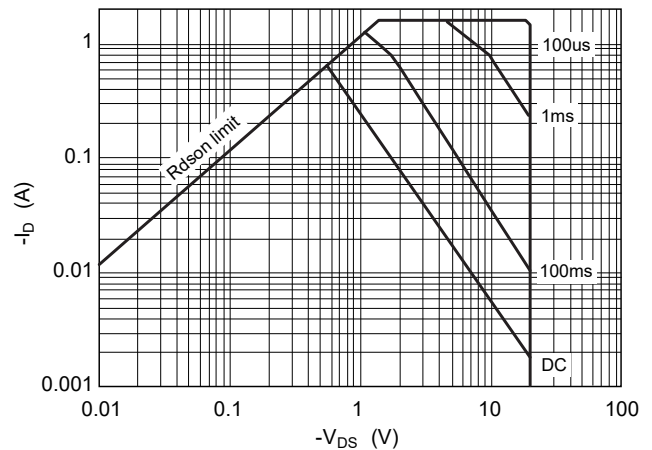
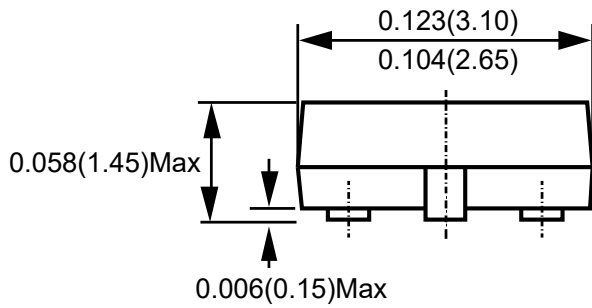
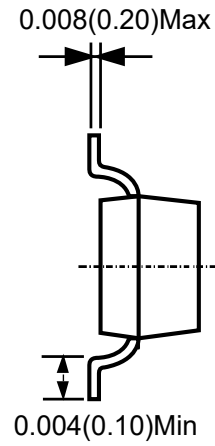
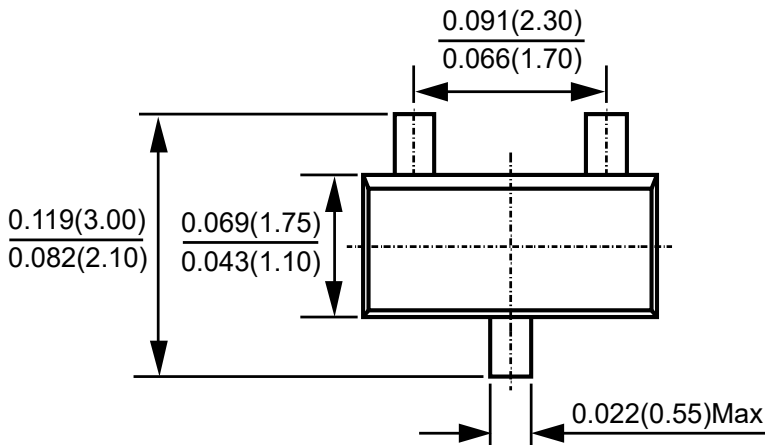


FIG. 6-Safe Operating Area





Package Outline Dimensions



SOT-23

Dimensions in inches and (millimeters)



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