



## 100V N-Channel MOSFETs

### General Description

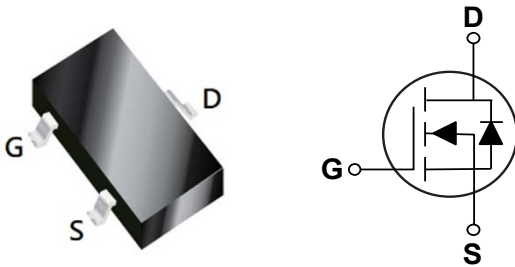
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$
100 V	125 m $\Omega$	5 A

### Features

- $R_{DS(ON)} \leq 125m\Omega @ V_{GS}=10V$
- Improved dv/dt Capability
- Fast Switching
- Green Device Available

SOT-23 Pin Configuration



### Applications

- Uninterruptible Power Supply
- Load Switch
- Battery Protection

### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current - Continuous ( $T_A=25^\circ\text{C}$ )	5	A
$I_{DM}$	Drain Current - Pulsed (NOTE 1)	20	A
$P_D$	Power Dissipation ( $T_A=25^\circ\text{C}$ )	1.47	W
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
Marking Code		1005	

### Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	85	$^\circ\text{C/W}$



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Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	100	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	---	---	1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	---	---	125	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	---	---	135	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	---	2.5	V

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A	---	12	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	2.2	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	2.5	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, R <sub>G</sub> =1.8Ω, I <sub>D</sub> =5A	---	7	---	nS
T <sub>r</sub>	Rise Time		---	5	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	16	---	
T <sub>f</sub>	Fall Time		---	6	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	---	610	---	pF
C <sub>oss</sub>	Output Capacitance		---	40	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	25	---	

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	5	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =5A	---	---	1.2	V

NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
3. Essentially independent of operating temperature.



### Characteristics Curves

FIG. 1- $I_S$  vs  $V_{SD}$

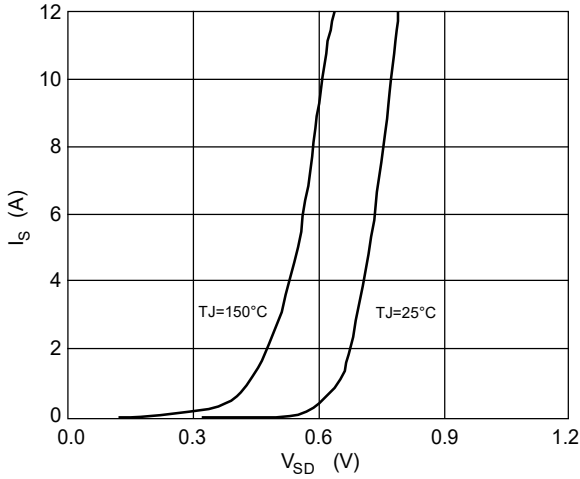


FIG. 2-Normalized  $R_{DS(ON)}$  vs  $T_J$

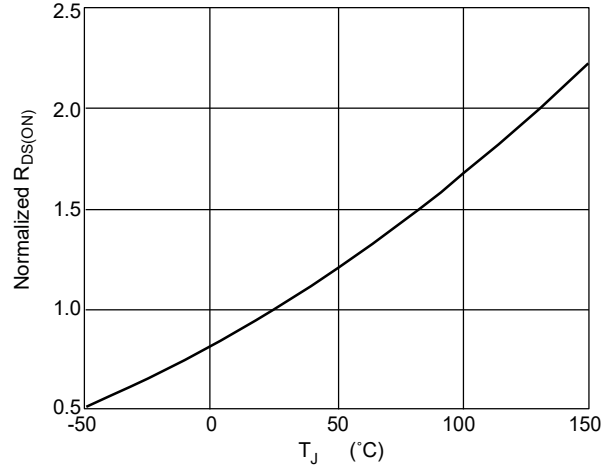


FIG. 3-Normalized  $V_{th}$  vs  $T_J$

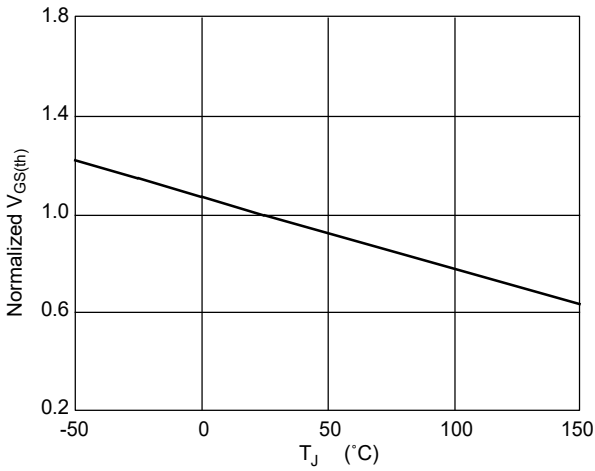


FIG. 4- $R_{DS(ON)}$  vs  $V_{GS}$

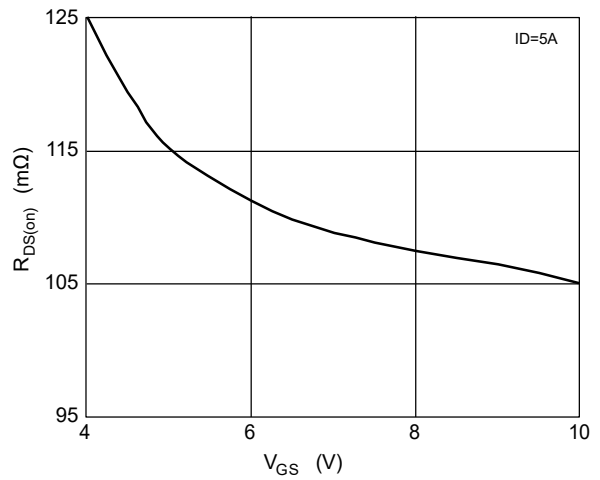


FIG. 5-Switching Time Waveform

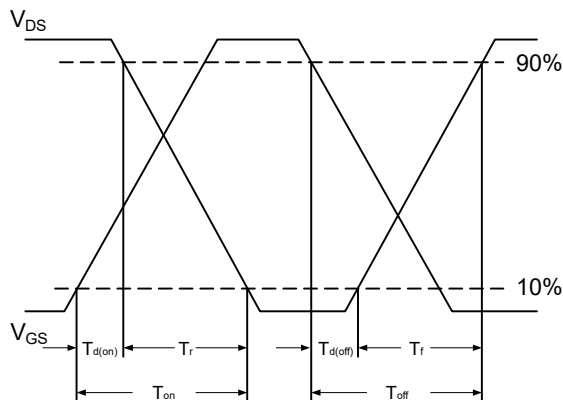
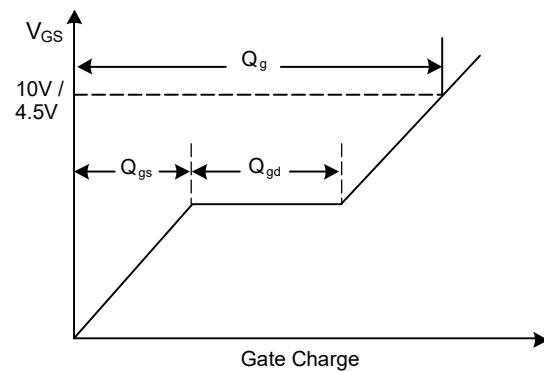
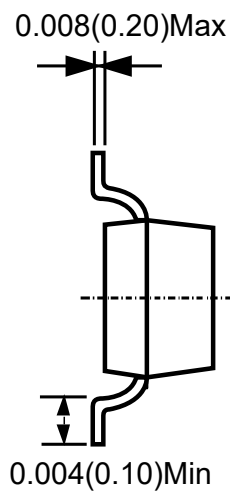
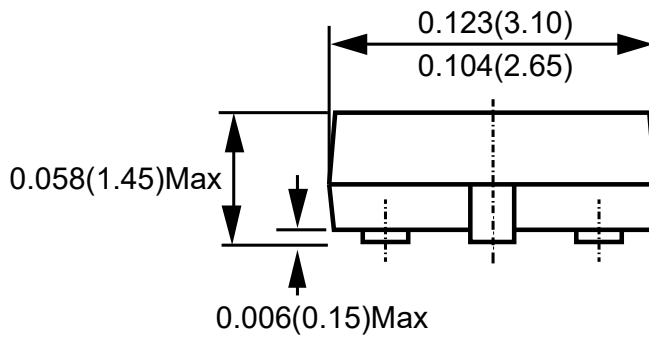
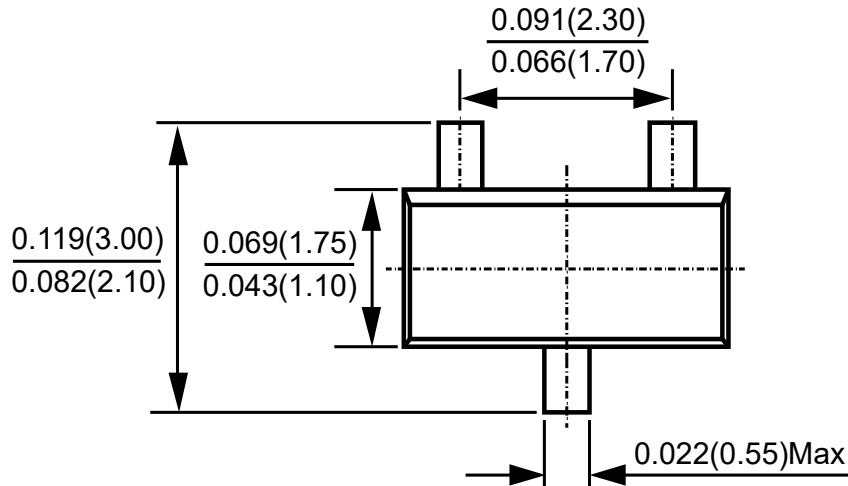


FIG. 6-Gate Charge Waveform





Package Outline Dimensions



SOT-23

Dimensions in inches and (millimeters)



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