



### **General Description**

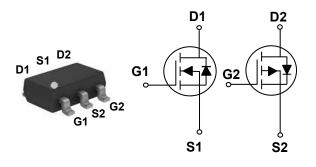
These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	Ι <sub>D</sub>
30 V	30 mΩ	4 A
-30 V	65 mΩ	-3 A

### **Features**

- · Fast switching
- · Green Device Available
- Suit for 4.5V Gate Drive Applications

### SOT-23-6 Pin Configuration



### **Applications**

- DC Fan
- Motor Drive Applications
- Networking
- · Half / Full Bridge Topology

bsolute Maximum Ratings T <sub>C</sub> =25°C unless otherwise noted								
Symbol	Parameter		Rating		Units			
$V_{DS}$	Drain-Source Voltage		30	-30	V			
$V_{GS}$	Gate-Source Voltage		±20	±20	V			
I_	Drain Current - Continuous (T <sub>A</sub> =25°C)		4	-3	Α			
I <sub>D</sub>	Drain Current - Continuous (T <sub>A</sub> =100°C)		2.5	-1.8	Α			
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)		16	-12	Α			
$P_D$	Power Dissipation (T <sub>A</sub> =25°C)		:	2	W			
$T_J$	Operating Junction Temperature Range		-55 to 150		°C			
$T_{STG}$	Storage Temperature Range		-55 to 150		°C			
Marking Code			k					

Thermal Characteristics							
Symbol	Parameter	Тур.	Max.	Unit			
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62.5	°C/W			





## N Channel Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

## **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	30	-		V
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =30V , $V_{GS}$ =0V , $T_J$ =25°C		-	1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS}$ =10V , $I_D$ =4A			30	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =2A			37	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.2	1.6	2.5	V
gfs	Forward Transconductance	$V_{DS}$ =10V , $I_{D}$ =3A		6.5		S

## **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =3A		4.1		
$Q_gs$	Gate-Source Charge	$V_{DS}$ -15V, $V_{GS}$ -4.5V, $I_D$ -3A (NOTE 2 \ 3)		1.0		nC
$Q_{gd}$	Gate-Drain Charge	(NOTE 2 × 3)		2.1		
$T_{d(on)}$	Turn-On Delay Time			2.8		
T <sub>r</sub>	Rise Time	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$ ,		7.2		nS
$T_{d(off)}$	Turn-Off Delay Time	I <sub>D</sub> =1A (NOTE 2 \ 3)		15.8		113
$T_f$	Fall Time			4.6		
C <sub>iss</sub>	Input Capacitance			345		
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ =25V , $V_{GS}$ =0V , F=1MHz		55		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			32		
$R_g$	Gate Resistance	V <sub>GS</sub> =0V , V <sub>DS</sub> =0V , F=1MHz		3.2		Ω

## **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	−V <sub>G</sub> =V <sub>D</sub> =0V,Force Current -			4	Α
I <sub>SM</sub>	Pulsed Source Current				8	Α
$V_{SD}$	Diode Forward Voltage	$V_{GS}$ =0V , $I_{S}$ =1A , $T_{J}$ =25 $^{\circ}$ C			1	V





#### **Characteristics Curves**

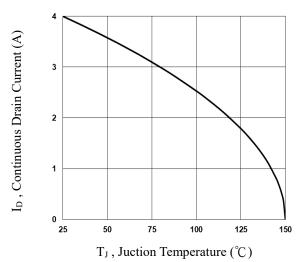


Fig.1 Continuous Drain Current vs. Tc

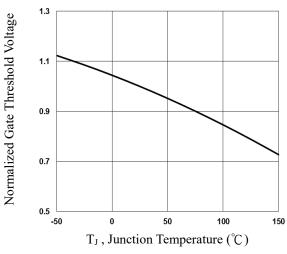


Fig.3 Normalized  $V_{th}$  vs.  $T_J$ 

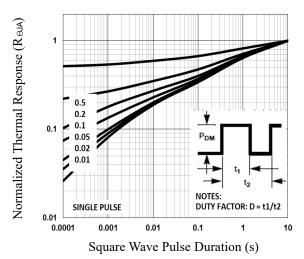


Fig.5 Normalized Transient Response

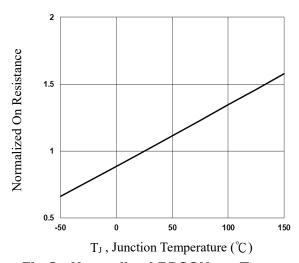


Fig.2 Normalized RDSON vs. T<sub>J</sub>

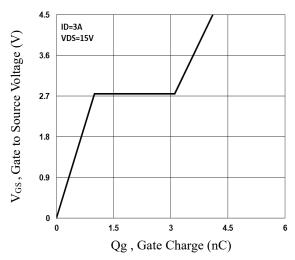


Fig.4 Gate Charge Waveform

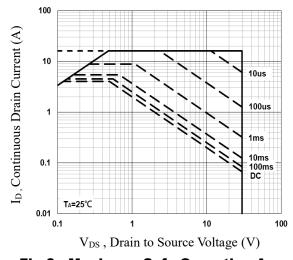


Fig.6 Maximum Safe Operation Area





## P Channel Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ = 0V , $I_D$ = -250uA	-30			V
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ = -30V , $V_{GS}$ = 0V , $T_{J}$ =25 $^{\circ}$ C			-1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = ±20V , $V_{DS}$ = 0V			±100	nA

### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS}$ = -10V , $I_D$ = -3A			65	mΩ
		$V_{GS}$ = -4.5V , $I_{D}$ = -2A			90	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250uA$	-1.2	-1.6	-2.2	V
gfs	Forward Transconductance	$V_{DS}$ =-10V , $I_{D}$ =-3A		3.7		S

## **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	V <sub>DS</sub> = -30V , V <sub>GS</sub> = -4.5V ,		5		
$Q_{gs}$	Gate-Source Charge	I <sub>D</sub> = -2A (NOTE 2 \ 3)		1.4		nC
$Q_{gd}$	Gate-Drain Charge	102A (1101E 2 \ 3)		1.7		
$T_{d(on)}$	Turn-On Delay Time	101		3.4		
T <sub>r</sub>	Rise Time	$V_{DD}$ = -30V , $V_{GS}$ = -10V , $R_{G}$ =6 $\Omega$ , $I_{D}$ = -1A		10.8		nS
$T_{d(off)}$	Turn-Off Delay Time	(NOTE 2 \ 3)		26.9		113
T <sub>f</sub>	Fall Time			6.9		
C <sub>iss</sub>	Input Capacitance			420		
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ = -30V , $V_{GS}$ = 0V , F= 1MHz		50		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	]		35		

## **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current -			-3	Α
I <sub>SM</sub>	Pulsed Source Current			-	-6	Α
$V_{SD}$	Diode Forward Voltage	$V_{GS}$ =0V , $I_S$ = -1A , $T_J$ =25°C			-1	V

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 3. Essentially independent of operating temperature.





#### **Characteristics Curves**

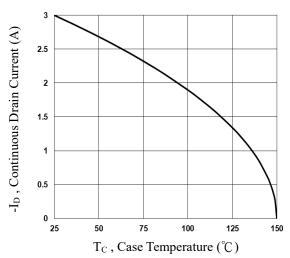


Fig.7 Continuous Drain Current vs. Tc

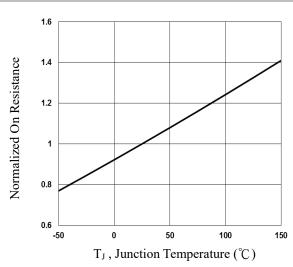


Fig.8 Normalized RDSON vs. TJ

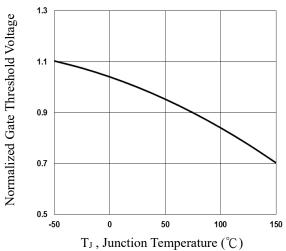


Fig.9 Normalized  $V_{\text{th}}$  vs.  $T_J$ 

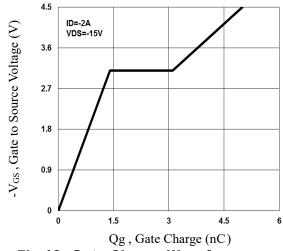


Fig.10 Gate Charge Waveform

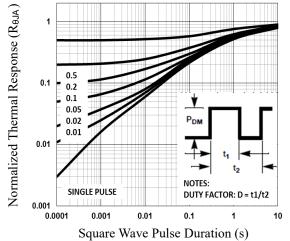


Fig.11 Normalized Transient Impedance

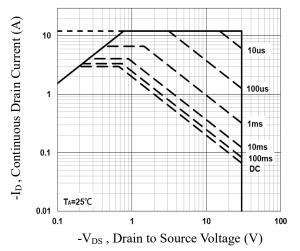
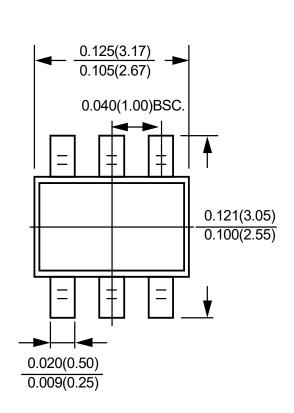


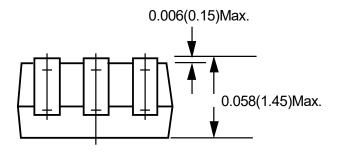
Fig.12 Maximum Safe Operation Area

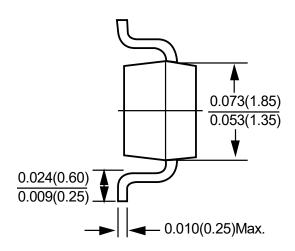




## **Package Outline Dimensions**







**SOT-23-6**Dimensions in inches and (millimeters)





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