



General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

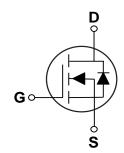
BV _{DSS}	R _{DS(ON)}	I _D
30 V	24 mΩ	6.5 A

Features

- 30V, 6.5A, $R_{DS(ON)}$ =24m $\Omega @V_{GS}$ =10V
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available

SOT-23S Pin Configuration





Applications

- MB / VGA / Vcore
- · Load Switch
- · Hand-Held Instrument

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±20	V
ı	Drain Current - Continuous (T _C =25°C)	6.5	Α
I _D	Drain Current - Continuous (T _C =100°C)	4.1	Α
I _{DM}	Drain Current - Pulsed (NOTE 1)	26	Α
EAS	Single Pulse Avalanche Energy (NOTE 2)	32	mJ
IAS	Single Pulse Avalanche Current (NOTE 2)	8	Α
P_{D}	Power Dissipation (T _C =25°C)	1.56	W
ı D	Power Dissipation - Derate above 25°C	0.012	W/°C
T_J	Operating Junction Temperature Range	-50 to 150	°C
T _{STG}	Storage Temperature Range	-50 to 150	°C
Marking Code		ı	

Thermal Characteristics					
Symbol	Parameter	Тур.	Max	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		80	°C/W	





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	30			V
I _{DSS}	Drain-Source Leakage Current	V_{DS} =30V , V_{GS} =0V , T_J =25°C			1	uA
		V_{DS} =24V , V_{GS} =0V , T_J =125 $^{\circ}$ C			10	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I Regions	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =6A		20	24	mΩ
	(NOTE 3)	V _{GS} =4.5V , I _D =4A		27	34	11122
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.2	1.6	2.5	V
gfs	Forward Transconductance	V _{DS} =10V , I _D =4A		6.5		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge (NOTE 3 \ 4)			4.1	8	
Q_{gs}	Gate-Source Charge (NOTE 3 · 4)	V_{DS} =15V , V_{GS} =4.5V , I_{D} =6A		1	2	nC
Q_{gd}	Gate-Drain Charge (NOTE 3 \ 4)			2.1	4	
$T_{d(on)}$	Turn-On Delay Time (NOTE 3 \ 4)			2.8	5	
T _r	Rise Time (NOTE 3 \ 4)	V_{DD} =15V , V_{GS} =10V , R_{G} =6 Ω ,	-	7.2	14	ns
$T_{d(off)}$	Turn-Off Delay Time (NOTE 3 \ 4)	I _D =1A		15.8	30	115
T_f	Fall Time (NOTE 3 \ 4)			4.6	9	
C _{iss}	Input Capacitance			345	500	
C _{oss}	Output Capacitance	V_{DS} =25V , V_{GS} =0V , F=1MHz		55	80	pF
C _{rss}	Reverse Transfer Capacitance			32	45	
Rg	Gate resistance	V_{GS} =0V , V_{DS} =0V , F=1MHz		3.2	6.4	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			6.5	Α
I _{SM}	Pulsed Source Current (NOTE 3)				26	Α
V_{SD}	Diode Forward Voltage (NOTE 3)	V _{GS} =0V , I _S =1A , T _J =25°C			1	V

NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. V_{DD} =25V, V_{GS} =10V, L=1mH, I_{AS} =8A, R_{G} =25 Ω , Starting T_{J} =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.





Characteristics Curves

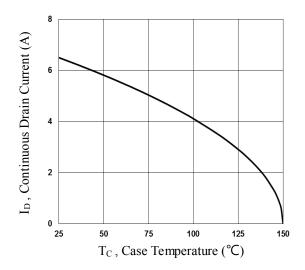


Fig.1 Continuous Drain Current vs. Tc

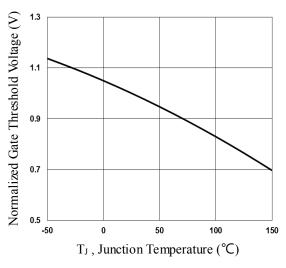


Fig.3 Normalized V_{th} vs. T_J

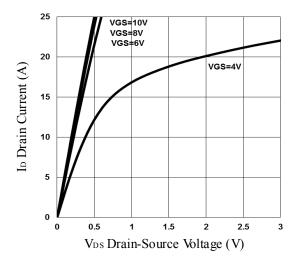


Fig.5 On Region Characteristics

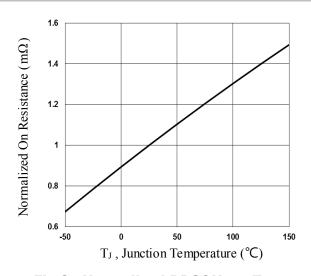


Fig.2 Normalized RDSON vs. T_J

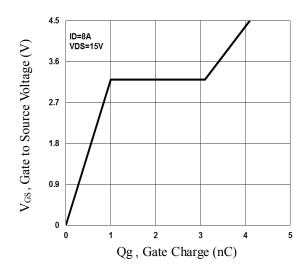


Fig.4 Gate Charge Waveform

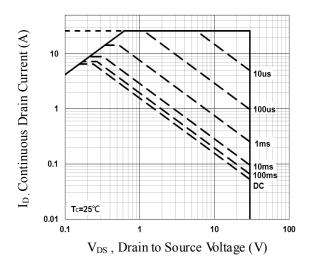
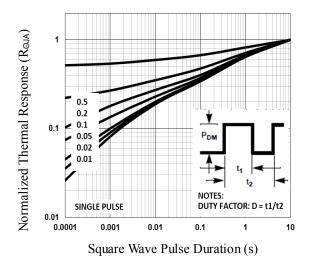


Fig.6 Maximum Safe Operation Area





Characteristics Curves



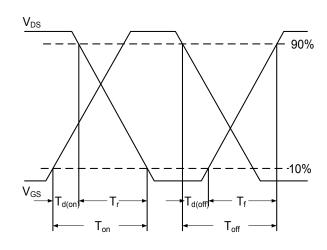


Fig.7 Normalized Transient Response

Fig.8 Switching Time Waveform

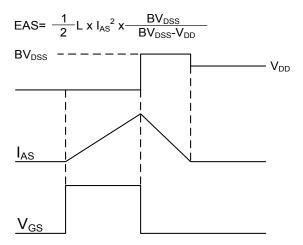
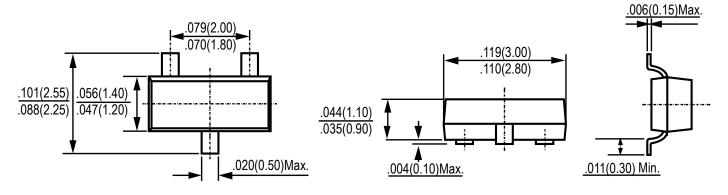


Fig.9 EAS Waveform

Package Outline Dimensions



SOT-23SDimensions in inches and (millimeters)





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