



# 20V N-Channel MOSFETs

### General Description

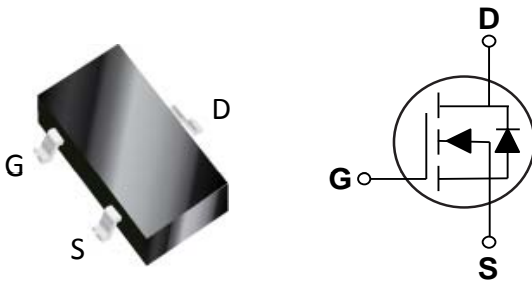
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
20 V	19 mΩ	6.7 A

### Features

- R<sub>DS(ON)</sub>=19mΩ@V<sub>GS</sub>=4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for 1.8V Gate Drive Applications

SOT-23S Pin Configuration



### Applications

- Notebook
- Load Switch
- Hand-Held Instrument

### Absolute Maximum Ratings T<sub>c</sub>=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	20	V
V <sub>GS</sub>	Gate-Source Voltage	±10	V
I <sub>D</sub>	Drain Current - Continuous (T <sub>c</sub> =25°C)	6.7	A
	Drain Current - Continuous (T <sub>c</sub> =100°C)	4.2	A
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	26.8	A
P <sub>D</sub>	Power Dissipation (T <sub>c</sub> =25°C)	1.56	W
	Power Dissipation - Derate above 25°C	0.012	W/°C
T <sub>J</sub>	Operating Junction Temperature Range	-50 to 150	°C
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C
Marking Code		T	

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to Ambient	---	80	°C/W



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Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	20	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =16V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	---	---	10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	---	15	19	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3A	---	18	24	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =2A	---	23	32	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	0.3	0.6	0.8	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>S</sub> =4A	---	9.5	---	S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A (NOTE 2、3)	---	5.8	8	nC
Q <sub>gs</sub>	Gate-Source Charge		---	0.6	1	
Q <sub>gd</sub>	Gate-Drain Charge		---	2	4	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =10V, V <sub>GS</sub> =4.5V, R <sub>G</sub> =25Ω , I <sub>D</sub> =1A (NOTE 2、3)	---	5	9	nS
T <sub>r</sub>	Rise Time		---	14.4	27	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	30	55	
T <sub>f</sub>	Fall Time		---	9.2	17	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, F=1MHz	---	600	870	pF
C <sub>OSS</sub>	Output Capacitance		---	70	100	
C <sub>rSS</sub>	Reverse Transfer Capacitance		---	45	65	

Drain-Source Diode Characteristics and Ratings

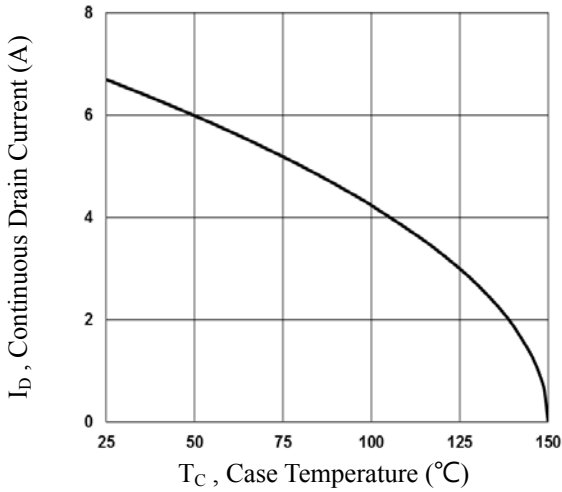
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	6.7	A
I <sub>SM</sub>	Pulsed Source Current		---	---	26.8	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V

NOTES :

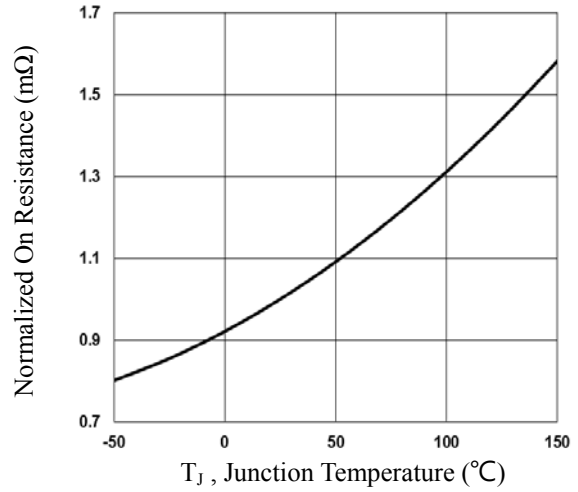
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
3. Essentially independent of operating temperature.



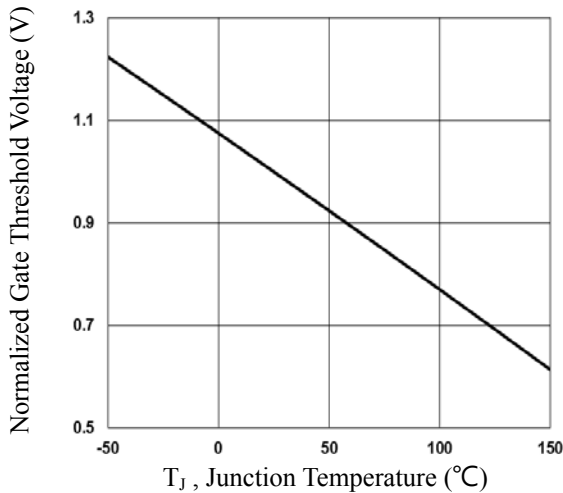
Characteristics Curves



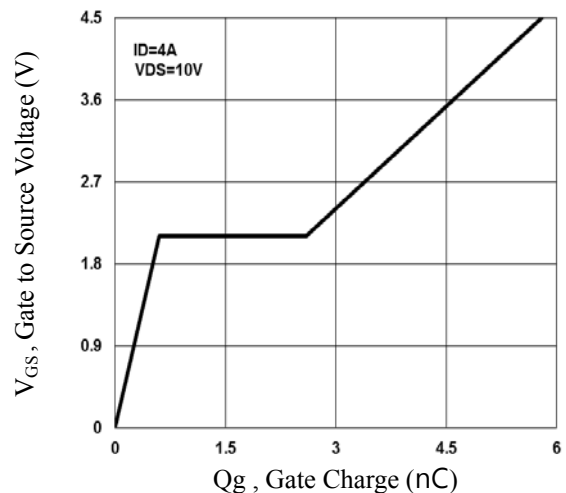
**Fig.1 Continuous Drain Current vs.  $T_c$**



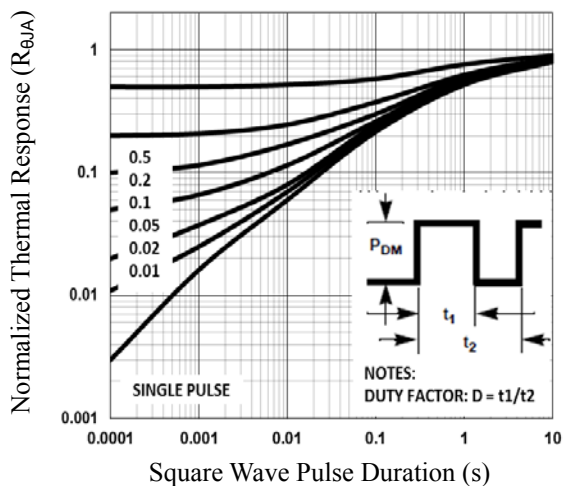
**Fig.2 Normalized  $R_{DSON}$  vs.  $T_j$**



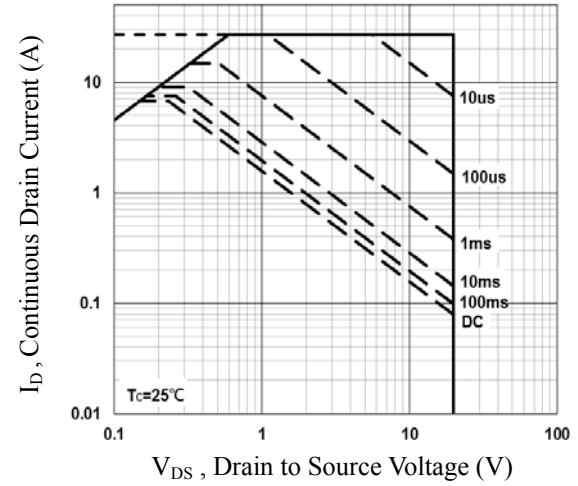
**Fig.3 Normalized  $V_{th}$  vs.  $T_j$**



**Fig.4 Gate Charge Waveform**



**Fig.5 Normalized Transient Impedance**



**Fig.6 Maximum Safe Operation Area**



Characteristics Curves

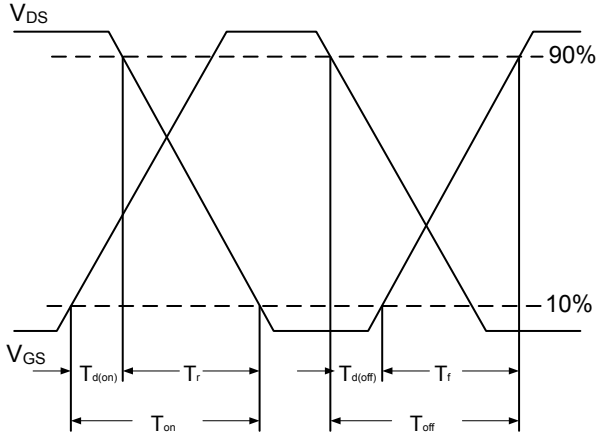


Fig.7 Switching Time Waveform

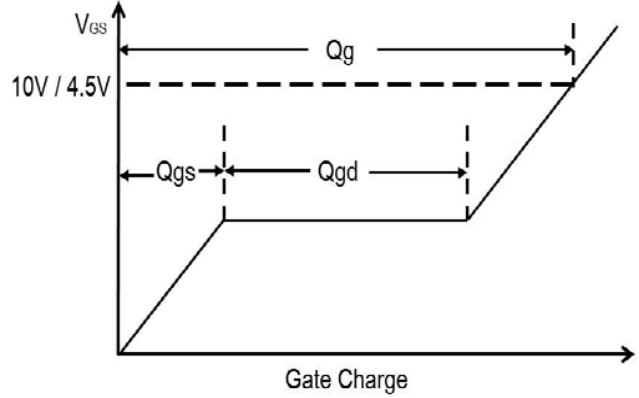
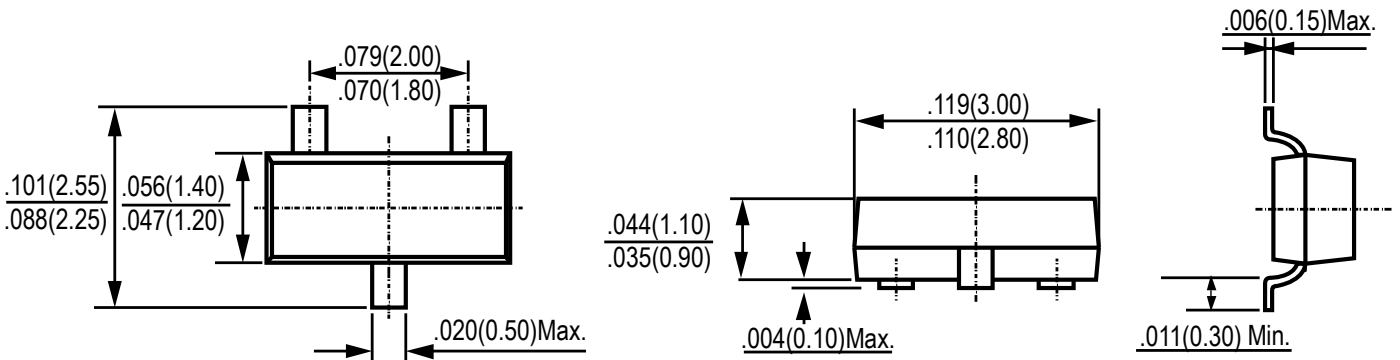


Fig.8 Gate Charge Waveform

Package Outline Dimensions



SOT-23S

Dimensions in inches and (millimeters)



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