



### **General Description**

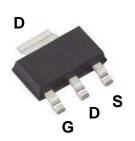
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

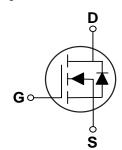
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
60 V	60 mΩ	6.8 A

### **Features**

- $\bullet \; R_{DS(ON)} {\leq} 60 m \Omega @V_{GS} {=} 10 V$
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available

## SOT-223 Pin Configuration





### **Applications**

- Motor Drive
- Power Tools
- · LED Lighting

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	60	V	
$V_{GS}$	Gate-Source Voltage	±20	V	
ı	Drain Current - Continuous (T <sub>C</sub> =25°C)	6.8	^	
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> =100°C)	4.3	Α	
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	27.2	Α	
E <sub>AS</sub>	Single Pulse Avalanche Energy (NOTE 2)	11	mJ	
I <sub>AS</sub>	Single Pulse Avalanche Current (NOTE 2)	15	Α	
$P_{D}$	Power Dissipation (T <sub>C</sub> =25°C)	5.4	W	
' D	Power Dissipation - Derate above 25°C	0.043	W/°C	
T <sub>J</sub>	Operating Junction Temperature Range	-50 to 150	°C	
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C	
Marking Code		NG060 , DL6910		

Thermal Characteristics					
Symbol Parameter Typ. Max.				Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		85	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction to Case		23	°C/W	





# Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

	Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
	$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	60			V
1	1	IDrain-Source Leakage Current	$V_{DS}$ =60V , $V_{GS}$ =0V , $T_{J}$ =25°C			1	uA
	I <sub>DSS</sub>		V <sub>DS</sub> =48V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			10	uA
	I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	IStatic Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =6A		50	60	- mΩ
		$V_{GS}$ =4.5V , $I_D$ =3A		56	70	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.2	1.8	2.5	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =4A		10		S

## **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	\/ -49\/ \/ -10\/   -9A		14	21	
$Q_gs$	Gate-Source Charge	$V_{DS}$ =48V , $V_{GS}$ =10V , $I_{D}$ =8A (NOTE 3 \ 4)		2.9	5	nC
$Q_{gd}$	Gate-Drain Charge	(NOTE 3 * 4)		2.4	4	
$T_{d(on)}$	Turn-On Delay Time			14	27	
T <sub>r</sub>	Rise Time	$V_{DD}$ =30V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$ ,		4	8	nS
$T_{d(off)}$	Turn-Off Delay Time	I <sub>D</sub> =8A (NOTE 3 × 4)		32	60	110
T <sub>f</sub>	Fall Time			2	4	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz		835	1300	
C <sub>oss</sub>	Output Capacitance			69	130	pF
$C_{rss}$	Reverse Transfer Capacitance			40	80	
$R_g$	Gate Resistance	$V_{DS}$ =0V , $V_{GS}$ =0V , f=1MHz		1.7	3.4	Ω

## **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			6.8	Α
I <sub>SM</sub>	Pulsed Source Current	v <sub>G</sub> -v <sub>D</sub> -ov , 1 orde Gurrent			13.6	Α
$V_{SD}$	Diode Forward Voltage	$V_{GS}$ =0V , $I_{S}$ =1A , $T_{J}$ =25 $^{\circ}$ C			1	V
t <sub>rr</sub>	Reverse Recovery Time	$V_{GS}$ =0V , $I_{S}$ =8A , di/dt=100A/ $\mu$ s ,		14.6		nS
$Q_{rr}$	Reverse Recovery Charge	T <sub>J</sub> =25°C		6.6		nC

#### NOTES:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =15A, Starting  $T_J$ =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- ${\bf 4.} \ Essentially \ independent \ of \ operating \ temperature.$





### **Characteristics Curves**

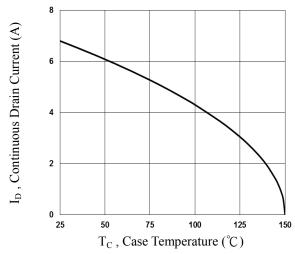


Fig.1 Continuous Drain Current vs. T<sub>c</sub>

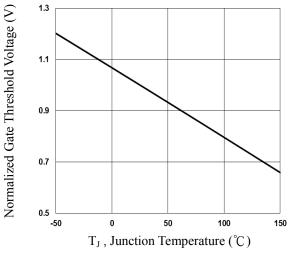


Fig.3 Normalized  $V_{th}$  vs.  $T_J$ 

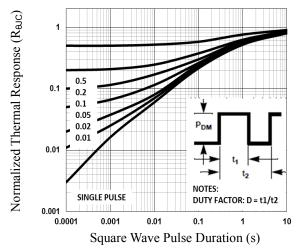


Fig.5 Normalized Transient Impedance

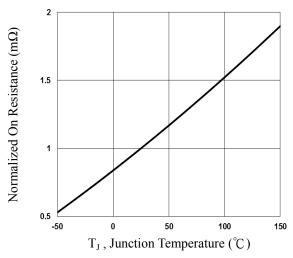


Fig.2 Normalized RDSON vs. T<sub>J</sub>

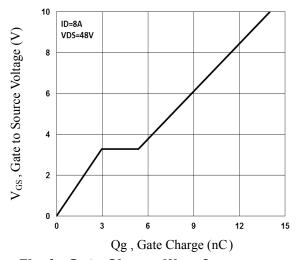


Fig.4 Gate Charge Waveform

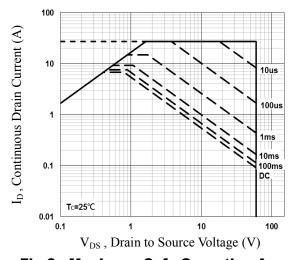


Fig.6 Maximum Safe Operation Area





### **Characteristics Curves**

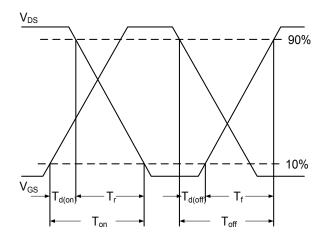


Fig.7 Switching Time Waveform

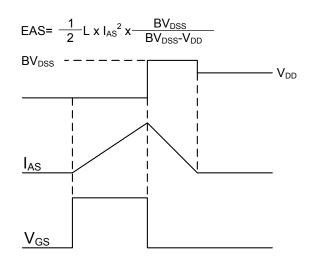
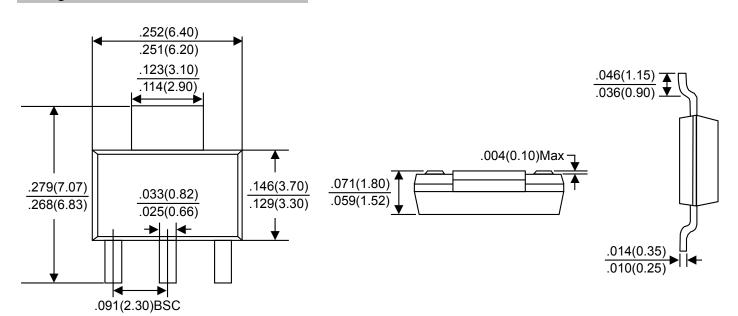


Fig.8 EAS Waveform

# **Package Outline Dimensions**



**SOT-223**Dimensions in inches and (millimeters)





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