

General Description

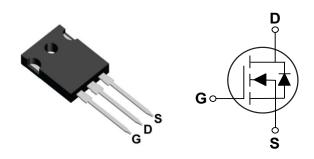
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV _{DSS}	R _{DS(ON)}	I _D
250 V	18 mΩ	120 A

Features

- $R_{DS(ON)} \le 18m\Omega@V_{GS} = 10V$
- · Improved dv/dt Capability
- Fast Switching
- · Green Device Available

TO-247-3L Pin Configuration



Applications

- ·UPS
- BLDC

Absolute Maximum Ratings T _c =25°C unless otherwise noted						
Symbol	Parameter	Rating	Units			
V_{DS}	Drain-Source Voltage	250	V			
V_{GS}	Gate-Source Voltage	±30	V			
I _D	Drain Current – Continuous (T _A =25°C)	120	Α			
I _{DM}	Drain Current – Pulsed (NOTE 1)	460	Α			
EAS	Single Pulse Avalanche Energy (NOTE 2)	600	mJ			
P_{D}	Power Dissipation (T _C =25°C)	568	W			
T_J	Operating Junction Temperature Range	-55 to 150	°C			
T _{STG}	Storage Temperature Range	-55 to 150	°C			
Marking Code		NT018				

Thermal Characteristics					
Symbol	Parameter	Rating	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	40	°C/W		
$R_{ heta JC}$	Thermal Resistance Junction to Case	0.22	°C/W		





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	250			V
I _{DSS}	Drain-Source Leakage Current	V_{DS} =250V , V_{GS} =0V			1	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =35A			18	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	3.6		5.0	V

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge			400		
Q_gs	Gate-Source Charge	V_{DD} =100V , V_{GS} =10V , I_{D} =35A	-	56		nC
Q_{gd}	Gate-Drain Charge		-	120		
$T_{d(on)}$	Turn-On Delay Time			90		
T_r	Rise Time	V_{DS} =50V , R_{G} =2.5 Ω , I_{D} =35A , V_{GS} =10V	-	140		nS
$T_{d(off)}$	Turn-Off Delay Time		-	220		113
T_f	Fall Time		-	180		
C _{iss}	Input Capacitance			14000		
C _{oss}	Output Capacitance	V_{DS} =25V , V_{GS} =0V , F=1MHz		960		pF
C_{rss}	Reverse Transfer Capacitance		-	420		
R_{g}	Gate Resistance	V_{GS} =0V , V_{DS} =0V , F=1MHz	-	1.5		Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Body Diode Current				120	Α
I _{SM}	Pulsed Diode Forward Current				460	Α
V_{SD}	Diode Forward Voltage	V_{GS} =0V , I_{S} =35A			1.2	V
t _{rr}	Reverse Recovery Time	V_{GS} =0V , I_S =30A , V_{DD} =50V ,		240		nS
Q_{rr}	Reverse Recovery Charge	dI _F /dt=100A/us		1.1		uC

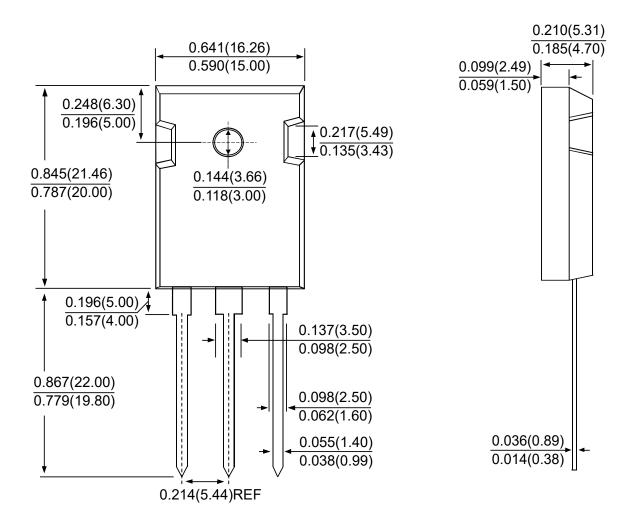
NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. I_{AS} =70A, V_{DD} =50V, R_{G} =25 Ω , V_{GS} =10V.
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.





Package Outline Dimensions



TO-247-3L
Dimensions in inches and (millimeters)





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