



#### **General Description**

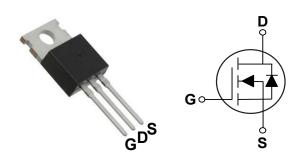
These N-Channel enhancement mode power field effect transistors are using trench MOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	Ι <sub>D</sub>
200 V	85 mΩ	28 A

#### **Features**

- $R_{DS(ON)} \leq 85 m\Omega@V_{GS} = \overline{10V}$
- · Fast Switching
- · Green Device Available

#### TO-220 Pin Configuration



#### **Applications**

- UPS
- · Synchronous Rectification
- Automotive
- Isolated DC/DC Converters in Telecom and Industrial

#### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted Symbol **Parameter** Rating Units $V_{DS}$ Drain-Source Voltage 200 $V_{GS}$ ٧ Gate-Source Voltage ±30 Drain Current - Continuous (T<sub>C</sub>=25°C) 28 $I_D$ Α Drain Current - Pulsed (NOTE 1) $I_{DM}$ 112 EAS Single Pulse Avalanche Energy (NOTE 2) 39.2 mJ $P_D$ Power Dissipation (T<sub>C</sub>=25°C) 208 W $T_J$ Operating Junction Temperature Range -55 to 150 °C Storage Temperature Range -55 to 150 $T_{STG}$ ٥С Marking Code NS085

Thermal Characteristics						
Symbol	Parameter	Rating	Unit			
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	62.5	°C/W			
$R_{\theta JC}$	Thermal Resistance Junction to Case	0.6	°C/W			





### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	200			V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =200V , V <sub>GS</sub> =0V			10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±30V , $V_{DS}$ =0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =28A			85	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_{D}=250uA$	3.5		5.5	V

#### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge			52		
$Q_{gs}$	Gate-Source Charge	$V_{DS}$ =100V , $V_{GS}$ =10V , $I_{D}$ =14A		22		nC
$Q_{gd}$	Gate-Drain Charge			19		
$T_{d(on)}$	Turn-On Delay Time			58		
T <sub>r</sub>	Rise Time	$V_{DD}$ =100V , $R_{G}$ =25 $\Omega$ , $I_{D}$ =14A ,		104		nS
$T_{d(off)}$	Turn-Off Delay Time	V <sub>GS</sub> =10V		74		113
$T_f$	Fall Time			58		
C <sub>iss</sub>	Input Capacitance			3000		
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ =25V , $V_{GS}$ =0V , F=1MHz		240		pF
$C_{rss}$	Reverse Transfer Capacitance			90		
$R_g$	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , F=1MHz		1.4		Ω

### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Body Diode Current				28	Α
I <sub>SM</sub>	Pulsed Diode Forward Current				112	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =28A			1.2	V

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The EAS data shows Max. rating .The test condition is  $V_{DD}$ =25V, L=0.1mH,  $R_{G}$ =25 $\Omega$ ,  $I_{AS}$ =28A.
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.



## **T2MNS085**



# 200V N-Channel MOSFETs

#### **Characteristics Curves**

FIG. 1- I<sub>D</sub> vs. T<sub>C</sub>

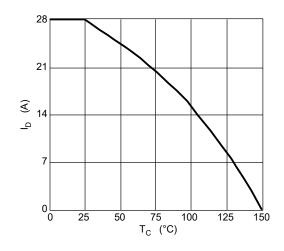


FIG. 2- Normalized  $R_{DS(ON)}$  vs.  $T_J$ 

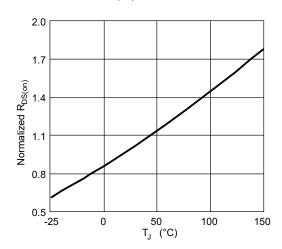


FIG. 3- Normalized  $V_{GS(th)}$  vs.  $T_J$ 

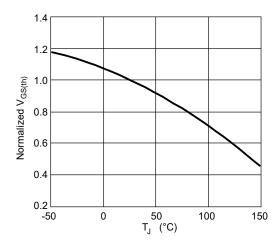


FIG. 4- Gate Charge Characteristics

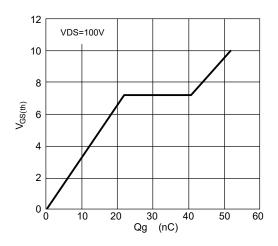


FIG. 5- Switching Time Waveform

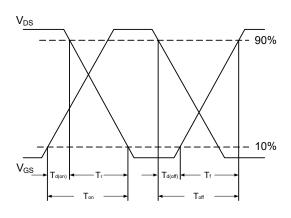
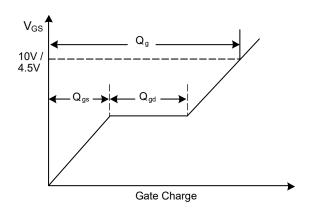


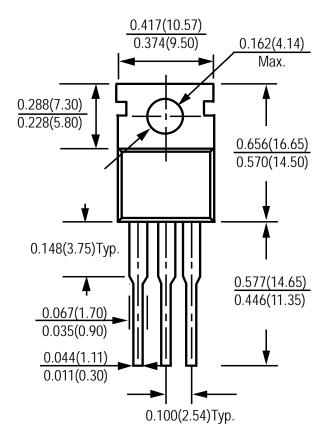
FIG. 6- Gate Charge Waveform

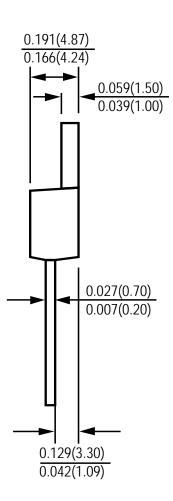






### **Package Outline Dimensions**





**TO-220** Dimensions in inches and (millimeters)



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