



### **General Description**

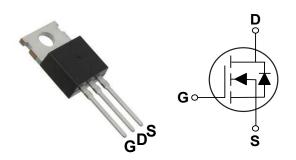
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	Ι <sub>D</sub>
100 V	7.6 mΩ	80 A

#### **Features**

- $R_{DS(ON)} \le 7.6 m\Omega @V_{GS} = 10V$
- · Improved dv/dt Capability
- Fast Switching
- · Green Device Available

### TO-220 Pin Configuration



#### **Applications**

- Networking
- · Load Switch
- · LED Applications
- Quick Charger

Absolute Maximum Ratings T <sub>c</sub> =25°C unless otherwise noted						
Symbol	Parameter	Rating	Units			
V <sub>DS</sub>	Drain-Source Voltage	100	V			
$V_{GS}$	Gate-Source Voltage	+20 / -12	V			
	Drain Current - Continuous (T <sub>C</sub> =25°C)	80	Α			
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> =100°C)	50.6	Α			
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	320	Α			
EAS	Single Pulse Avalanche Energy (NOTE 2)	231	mJ			
IAS	Single Pulse Avalanche Current (NOTE 2)	68	Α			
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> =25°C)	173	W			
ΓD	Power Dissipation - Derate above 25°C	1.39	W/°C			
T <sub>J</sub>	Operating Junction Temperature Range	-50 to 150	°C			
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C			
Marking Code		DP0978				

Thermal Characteristics					
Symbol	Symbol Parameter				
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction to Case		0.72	°C/W	





## Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	100			V
I <sub>DSS</sub>	IDrain-Source Leakage Current	$V_{DS}$ =80V , $V_{GS}$ =0V , $T_J$ =25°C			1	uA
		$V_{DS}$ =80V , $V_{GS}$ =0V , $T_{J}$ =125 $^{\circ}$ C			10	uA
$I_{GSS}$	Gate-Source Leakage Current	V <sub>GS</sub> =+20V , V <sub>DS</sub> =0V			100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =18A			7.6	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A			11.4	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.0	1.5	2.5	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =3A		15		S

### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	V 00V V 40V L 40A		53.5		
$Q_gs$	Gate-Source Charge	V <sub>DS</sub> =80V , V <sub>GS</sub> =10V , I <sub>D</sub> =10A (NOTE 3 \ 4)		7.5		nC
$Q_{gd}$	Gate-Drain Charge	(1012 3 + 4)		13.3		
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}$ =50V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$ , $I_{D}$ =1A (NOTE 3 $\cdot$ 4)		14.6		
$T_r$	Rise Time			32.8		ns
$T_{d(off)}$	Turn-Off Delay Time			62.2		115
$T_{f}$	Fall Time			28.4		
$C_{iss}$	Input Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , F=1MHz		3250		
C <sub>oss</sub>	Output Capacitance			867		pF
$C_{rss}$	Reverse Transfer Capacitance			58		
Rg	Gate resistance	$V_{GS}$ =0V , $V_{DS}$ =0V , F=1MHz		1.25		Ω

## **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V,Force Current			80	Α
I <sub>SM</sub>	Pulsed Source Current				160	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1	V

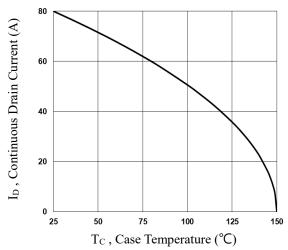
#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =50V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =68A,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$ =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 4. Essentially independent of operating temperature.

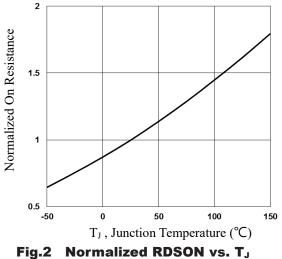


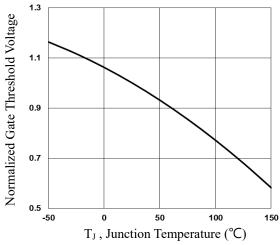


#### **Characteristics Curves**

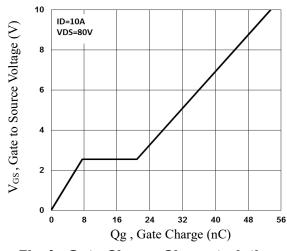


Continuous Drain Current vs. Tc

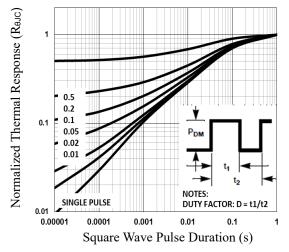




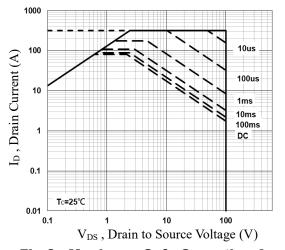
Normalized Vth vs. T<sub>J</sub>



**Gate Charge Characteristics** 



**Normalized Transient Impedance** 



**Maximum Safe Operation Area** 





#### **Characteristics Curves**

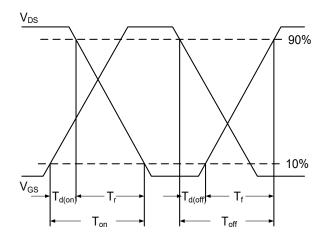


Fig.7 Switching Time Waveform

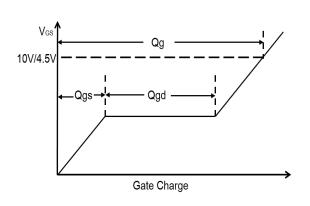
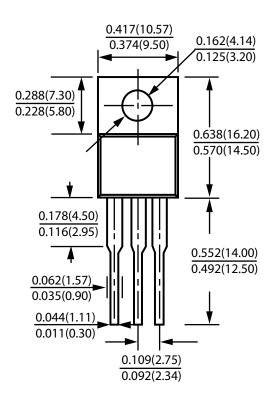
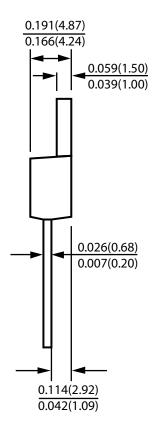


Fig.8 Gate Charge Waveform

### **Package Outline Dimensions**





**TO-220** Dimensions in inches and (millimeters)





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