



## 100V N-Channel MOSFETs

### General Description

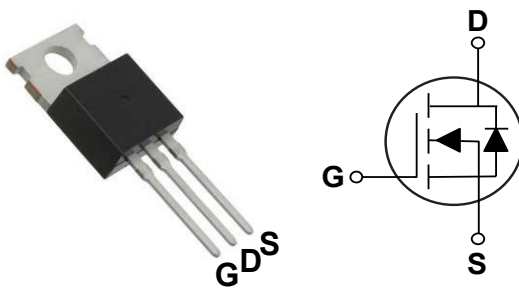
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$
100 V	11.5 m $\Omega$	80 A

### Features

- $R_{DS(ON)} \leq 11.5m\Omega @ V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

TO-220 Pin Configuration



### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	+20/-12	V
$I_D$	Drain Current - Continuous ( $T_c=25^\circ\text{C}$ )	80	A
	Drain Current - Continuous ( $T_c=100^\circ\text{C}$ )	50	A
$I_{DM}$	Drain Current - Pulsed (NOTE 1)	320	A
EAS	Single Pulse Avalanche Energy (NOTE 2)	192	mJ
IAS	Single Pulse Avalanche Current (NOTE 2)	62	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	156	W
	Power Dissipation - Derate above $25^\circ\text{C}$	0.8	W/ $^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.8	$^\circ\text{C}/\text{W}$



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Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

## Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu\text{A}$	100	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=80V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=80V, V_{GS}=0V, T_J=85^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=20V, V_{DS}=0V$	---	---	100	nA

## On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=15A$	---	---	11.5	m $\Omega$
		$V_{GS}=6V, I_D=8A$	---	---	16.5	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	2.0	2.5	4.0	V
gfs	Forward Transconductance	$V_{DS}=10V, I_D=3A$	---	10	---	S

## Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$Q_g$	Total Gate Charge	$V_{DS}=80V, V_{GS}=10V, I_D=8A$ (NOTE 3 · 4)	---	28.8	---	nC
$Q_{gs}$	Gate-Source Charge		---	5.8	---	
$Q_{gd}$	Gate-Drain Charge		---	9.2	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=50V, V_{GS}=10V, R_G=6\Omega,$ $I_D=1A$ (NOTE 3 · 4)	---	22	---	ns
$T_r$	Rise Time		---	18.7	---	
$T_{d(off)}$	Turn-Off Delay Time		---	42	---	
$T_f$	Fall Time		---	22	---	
$C_{iss}$	Input Capacitance	$V_{DS}=50V, V_{GS}=0V, F=1\text{MHz}$	---	1950	---	pF
$C_{oss}$	Output Capacitance		---	665	---	
$C_{rss}$	Reverse Transfer Capacitance		---	33	---	
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	---	1	---	$\Omega$

## Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V, \text{Force Current}$	---	---	100	A
$I_{SM}$	Pulsed Source Current		---	---	200	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1	V
$t_{rr}$	Reverse Recovery Time	$V_{GS}=0V, I_S=20A,$	---	50	---	ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt=100A/\mu\text{s}, T_J=25^\circ\text{C}$	---	75	---	nC

## NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=50V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=62A, R_G=25\Omega, \text{Starting } T_J=25^\circ\text{C}.$
3. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.



Characteristics Curves

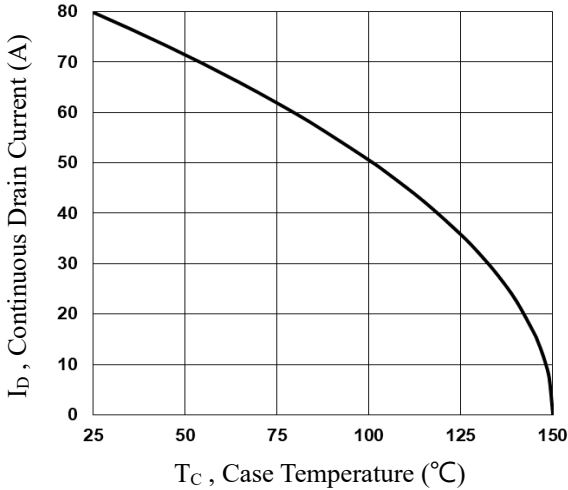


Fig.1 Continuous Drain Current vs.  $T_c$

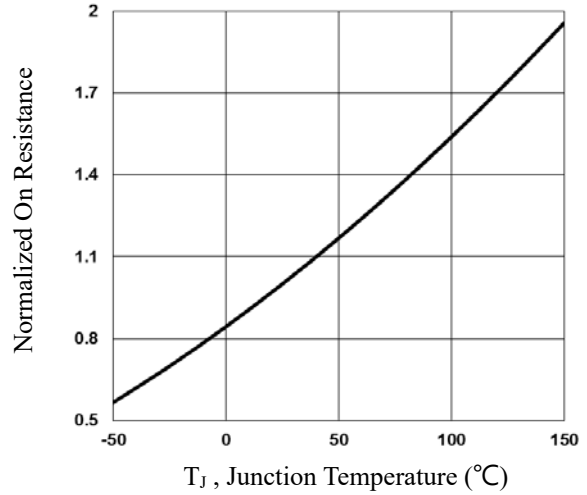


Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$

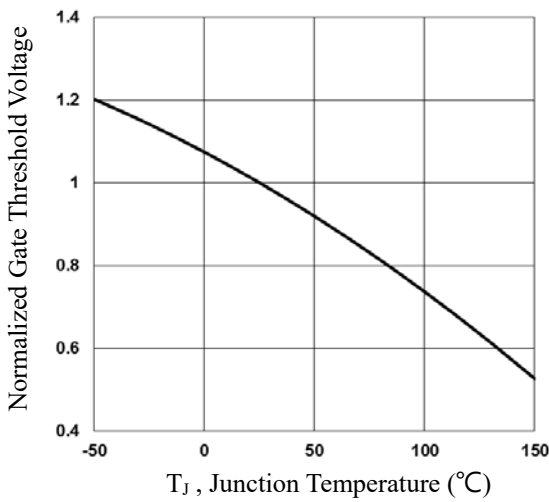


Fig.3 Normalized  $V_{th}$  vs.  $T_j$

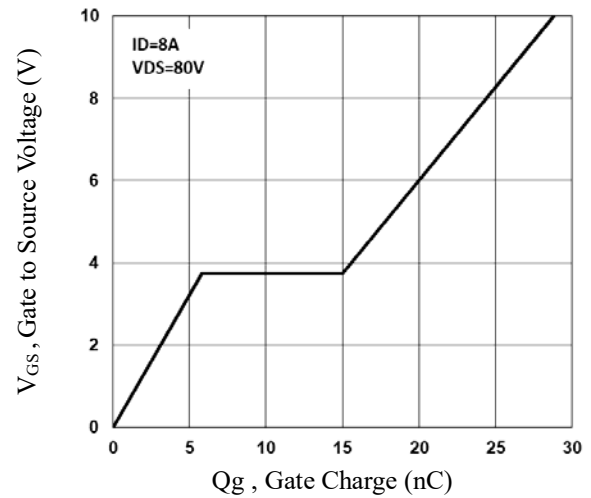


Fig.4 Gate Charge Characteristics

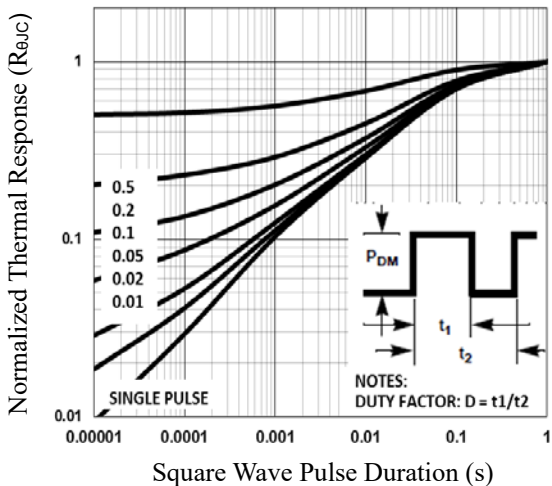


Fig.5 Normalized Transient Impedance

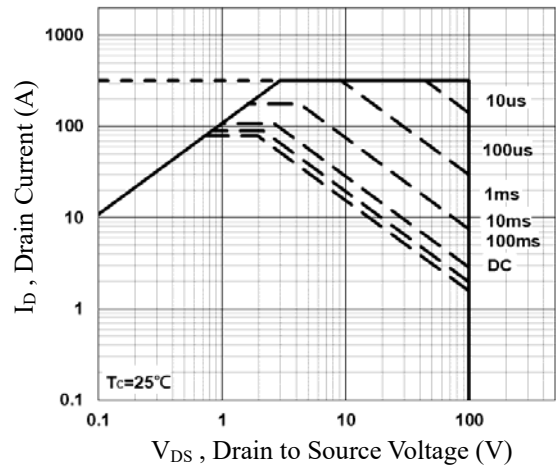


Fig.6 Maximum Safe Operation Area



Characteristics Curves

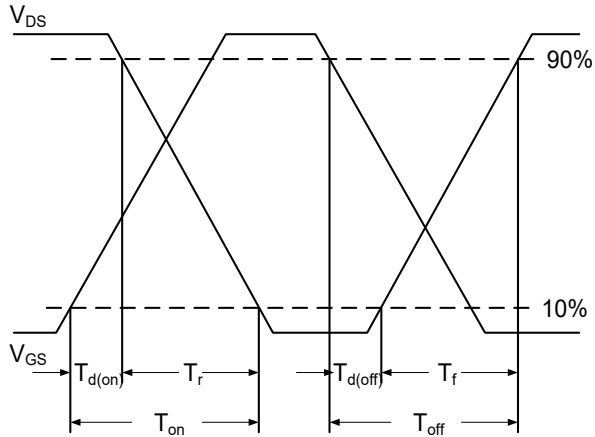
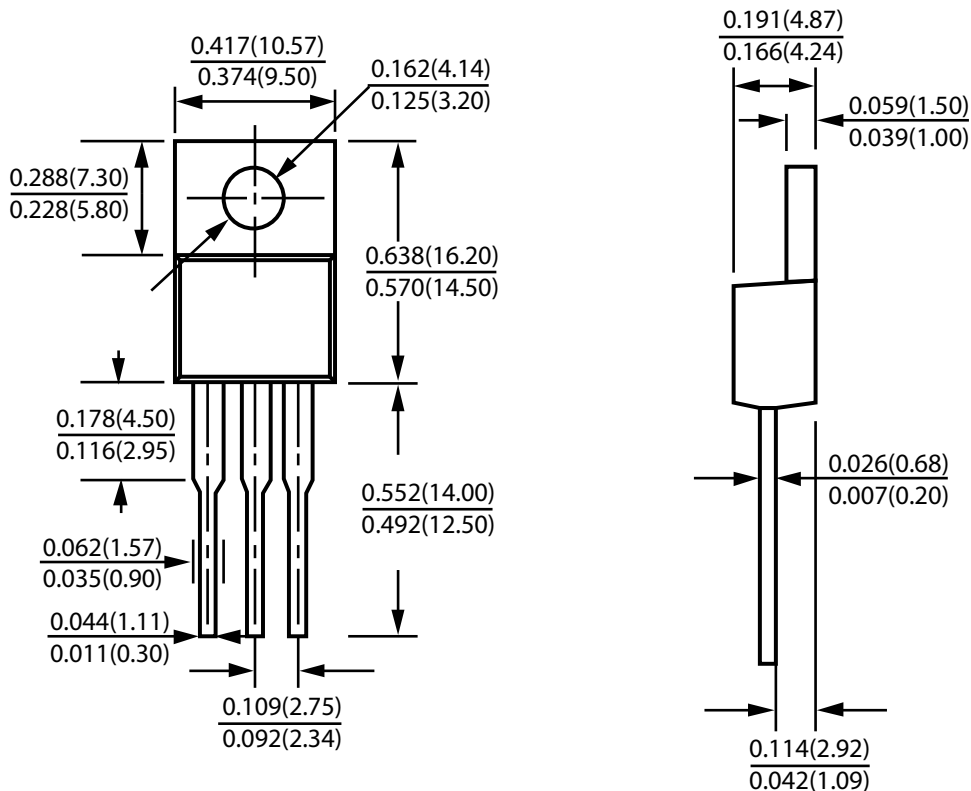


Fig.7 Switching Time Waveform

Package Outline Dimensions



TO-220

Dimensions in inches and (millimeters)



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