P5MNG8P5

60V N-Channel MOSFETs

BV_{DSS}	$R_{DS(ON)}$	I_D
60 V	8.5 mΩ	75 A

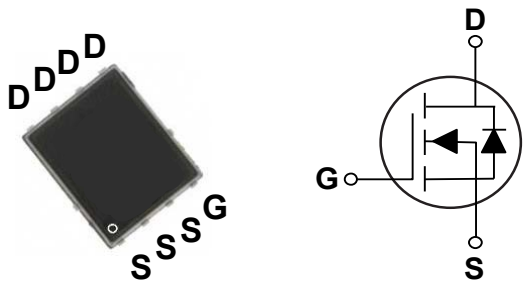
General Description

The P5MNG8P5 is the high cell density trenched N-ch MOSFETs, which provide excellent R_{DSON} and gate charge for most of the synchronous buck converter applications.

Features

- $R_{DS(ON)} \leqq 8.5m\Omega @ V_{GS}=10V$
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

PPAK5X6 Pin Configuration



Applications

- Networking
- Load Switch
- LED applications

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current – Continuous (T_C=25°C)	75	A
	Drain Current – Continuous (T_C=100°C)	47	A
I_{DM}	Drain Current – Pulsed (NOTE 1)	280	A
EAS	Single Pulse Avalanche Energy (NOTE 2)	22	mJ
IAS	Avalanche Current	21	A
P_D	Power Dissipation (T_C=25°C) (NOTE 3)	41	W
T_J	Operating Junction Temperature Range	-55 to 150	°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
Marking Code		NG8P5	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.4	°C/W

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	60	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=48V , V_{GS}=0V , T_J=25°C	---	---	1	uA
		V_{DS}=48V , V_{GS}=0V , T_J=55°C	---	---	5	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V , I_D=10A	---	7.1	8.5	mΩ
		V_{GS}=4.5V , I_D=5A	---	9.5	12	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D=250uA	1.2	---	3.0	V

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q_g	Total Gate Charge	V_{DS}=30V , V_{GS}=10V , I_D=18A	---	57	---	nC
Q_{gs}	Gate-Source Charge		---	8.7	---	
Q_{gd}	Gate-Drain Charge		---	14	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=30V , V_{GS}=10V , R_G=3.3Ω , I_D=20A	---	16.2	---	nS
T_r	Rise Time		---	41.2	---	
$T_{d(off)}$	Turn-Off Delay Time		---	56.4	---	
T_f	Fall Time		---	16.2	---	
C_{iss}	Input Capacitance	V_{DS}=30V , V_{GS}=0V , F=1MHz	---	3307	---	pF
C_{oss}	Output Capacitance		---	201	---	
C_{rss}	Reverse Transfer Capacitance		---	151	---	
R_g	Gate resistance	V_{GS}=0V , V_{DS}=0V , F=1MHz	---	1.2	---	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current (NOTE 4)	V_G=V_D=0V , Force Current	---	---	75	A
V_{SD}	Diode Forward Voltage (NOTE 1)	V_{GS}=0V , I_S=1A , T_J=25°C	---	---	1.2	V
t_{rr}	Reverse Recovery Time	I_F=20A , dI/dt=100A/us , T_J=25°C	---	22	---	nS
Q_{rr}	Reverse Recovery Charge		---	72	---	nC

NOTES：

1. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
2. The EAS data shows Max. rating. The test condition is V_{DD}=50V, V_{GS}=10V, L=0.1mH, I_{AS}=40A.
3. The power dissipation is limited by 150°C junction temperature.
4. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Characteristics Curves

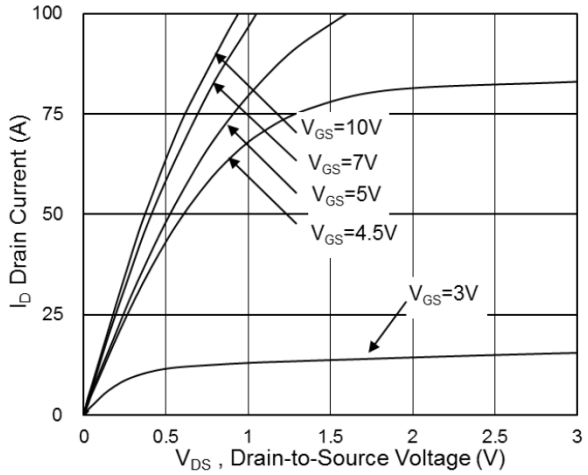


Fig.1 Typical Output Characteristics

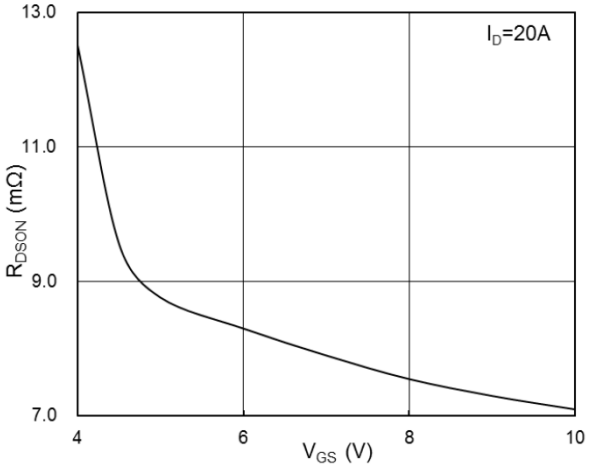


Fig.2 On-Resistance vs Gate-Source Voltage

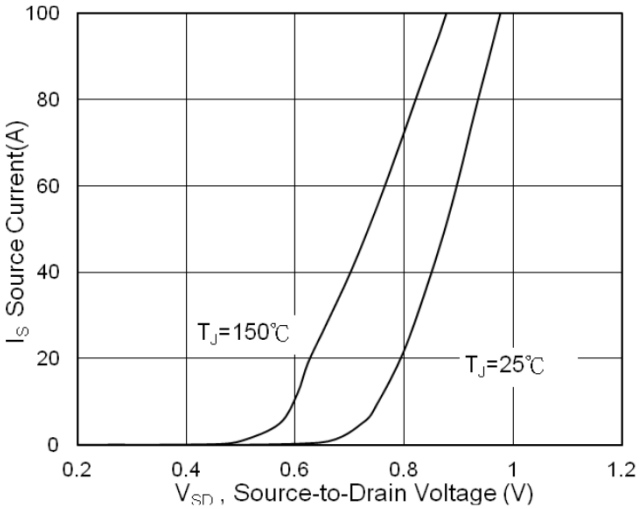


Fig.3 Forward Characteristics of Reverse

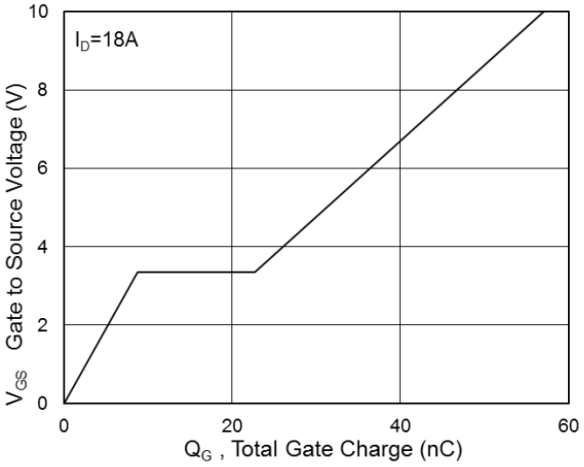


Fig.4 Gate-Charge Characteristics

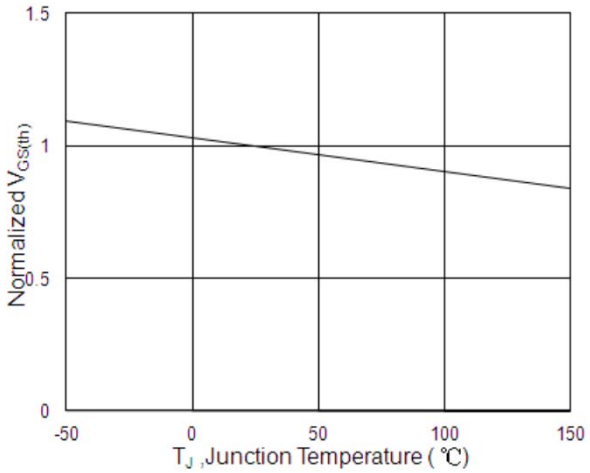


Fig.5 Normalized $V_{GS(th)}$ vs T_J

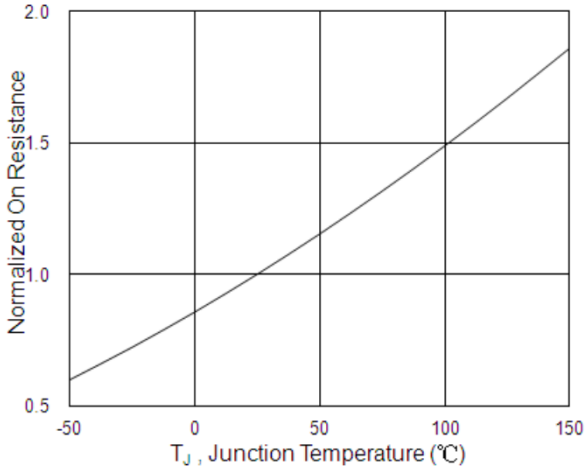


Fig.6 Normalized R_{DSON} vs T_J

Characteristics Curves

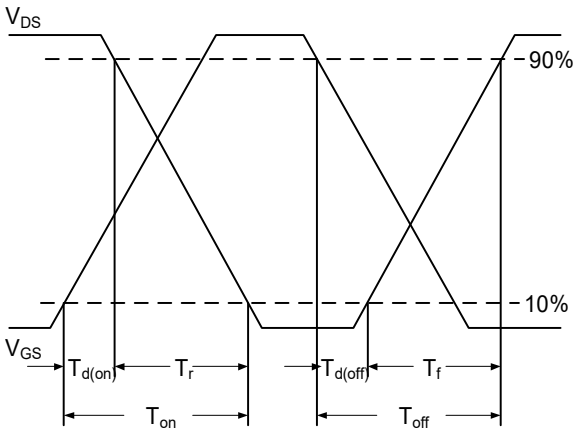
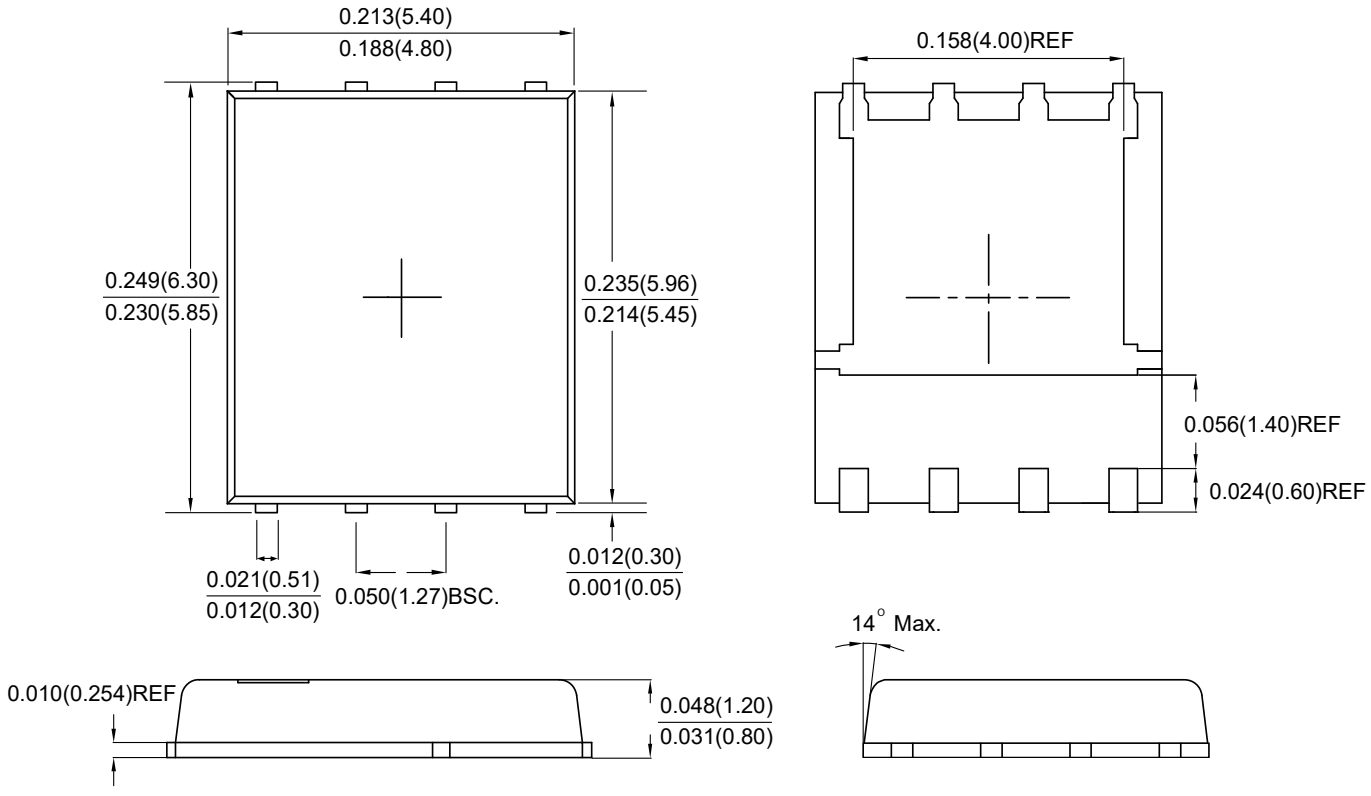


Fig.7 Switching Time Waveform

Package Outline Dimensions



PPAK5X6

Dimensions in inches and (millimeters)

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