P5MNG8P0

60V N-Channel MOSFETs

General Description

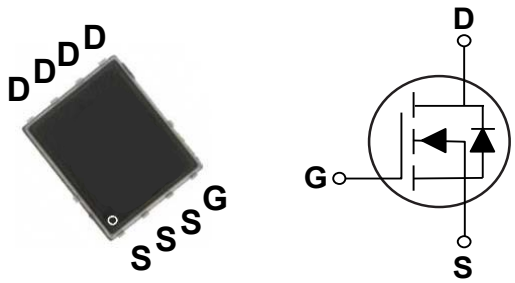
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV_{DSS}	$R_{DS(ON)}$	I_D
60 V	8 mΩ	62 A

Features

- $R_{DS(ON)} \leqq 8m\Omega @ V_{GS}=10V$
- Fast Switching
- Improved dv/dt Capability
- Green Device Available

PPAK5X6 Pin Configuration



Applications

- DC-DC Converter
- Power Management Switches

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current - Continuous (T_C=25°C)	62	A
I_{DM}	Drain Current - Pulsed (NOTE 1)	248	A
EAS	Single Pulse Avalanche Energy (NOTE 2)	105.8	mJ
P_D	Power Dissipation (T_C=25°C)	74.4	W
T_J	Operating Junction Temperature Range	-55 to 150	°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
Marking Code		NG8P0	

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	53	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	1.68	°C/W

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	60	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=60V , V_{GS}=0V	---	---	1	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V , I_D=20A	---	---	8	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D=250uA	2	---	4	V
gfs	Forward Transconductance	V_{DS}=10V , I_D=20A	---	47	---	S

Dynamic and switching Characteristics (NOTE 4)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q_g	Total Gate Charge	V_{DS}=30V , V_{GS}=10V , I_D=20A	---	67	---	nC
Q_{gs}	Gate-Source Charge		---	17.7	---	
Q_{gd}	Gate-Drain Charge		---	18	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=30V , V_{GS}=10V , R_G=3Ω , I_D=20A	---	18.4	---	nS
T_r	Rise Time		---	9.6	---	
$T_{d(off)}$	Turn-Off Delay Time		---	36	---	
T_f	Fall Time		---	11	---	
C_{iss}	Input Capacitance	V_{DS}=30V , V_{GS}=0V , F=1MHz	---	4160	---	pF
C_{oss}	Output Capacitance		---	235	---	
C_{rss}	Reverse Transfer Capacitance		---	175	---	
R_g	Gate resistance	V_{GS}=0V , V_{DS}=0V , f=1MHz	---	1	---	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current		---	---	62	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=20A	---	---	1.2	V
t_{rr}	Reverse Recovery Time	I_F=20A , dI_F/dt=100A/us	---	31	---	nS
Q_{rr}	Reverse Recovery Charge		---	43	---	nC

NOTES：

1. Repetitive Rating：Pulsed width limited by maximum junction temperature.
2. The EAS data shows Max. rating . The test condition is V_{DD}=35V, V_{GS}=10V, L=0.1mH, I_{AS}=46A.
3. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
4. This value is guaranteed by design hence it is not included in the production test.

Characteristics Curves

FIG. 1 - Output Characteristics

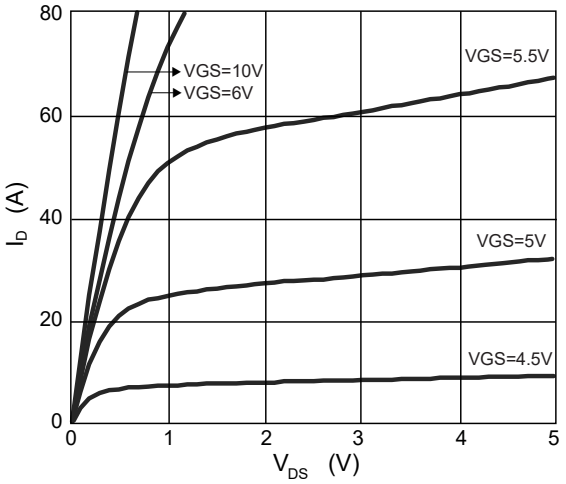


FIG. 2 - Transfer Characteristics

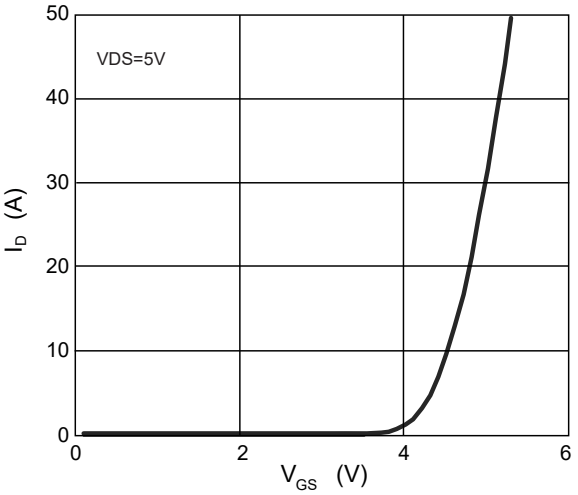


FIG. 3 - I_S vs. V_{SD}

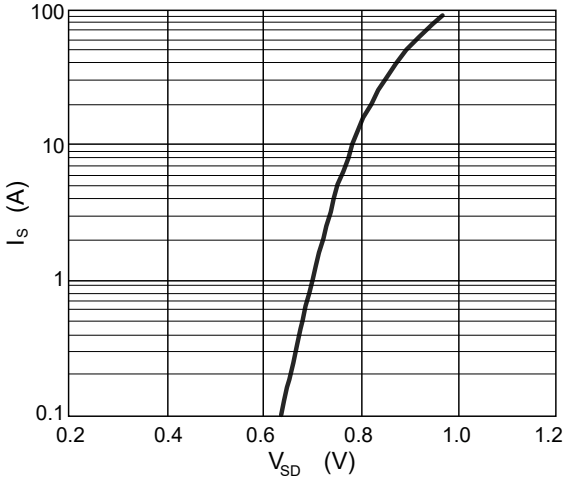


FIG. 4 - Gate Charge Characteristics

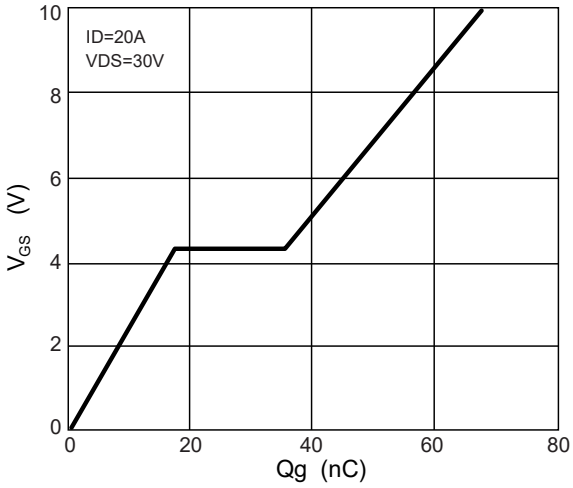


FIG. 5 - $R_{DS(ON)}$ vs. V_{GS}

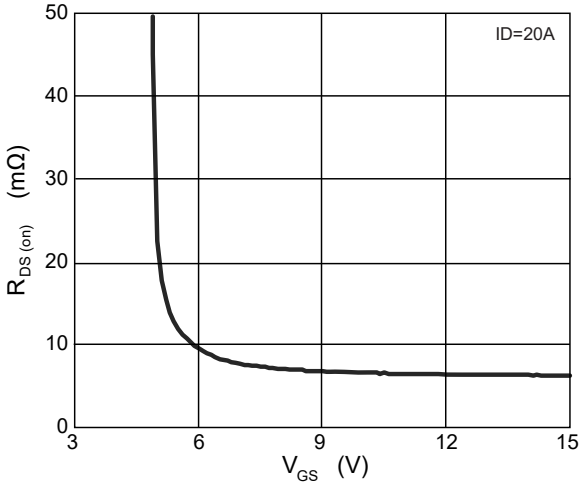
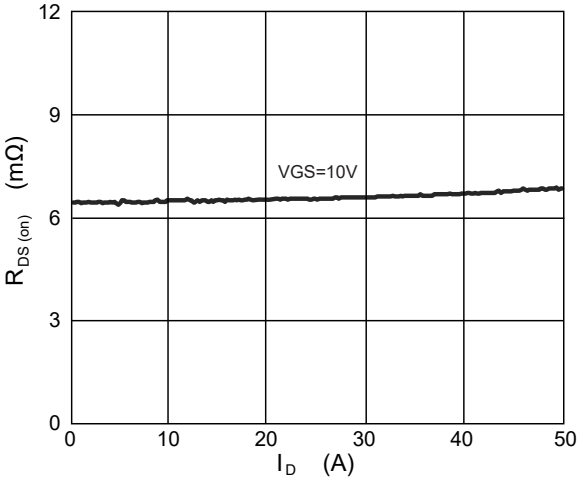


FIG. 6 - $R_{DS(ON)}$ vs. I_D



Characteristics Curves

FIG. 7 - Normalized $R_{DS(ON)}$ vs. T_J

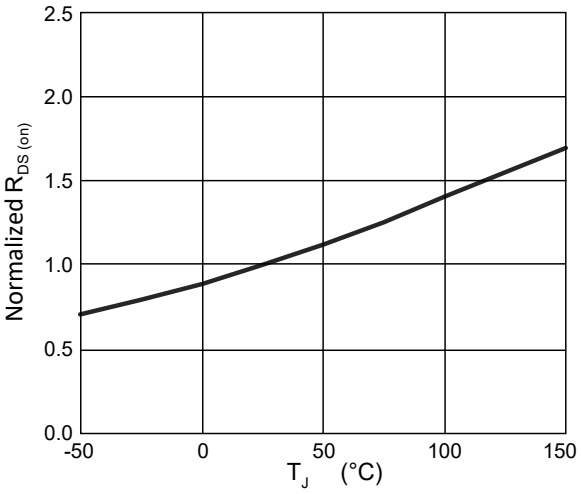
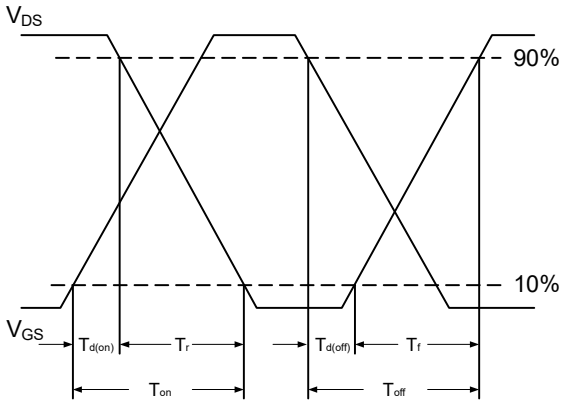
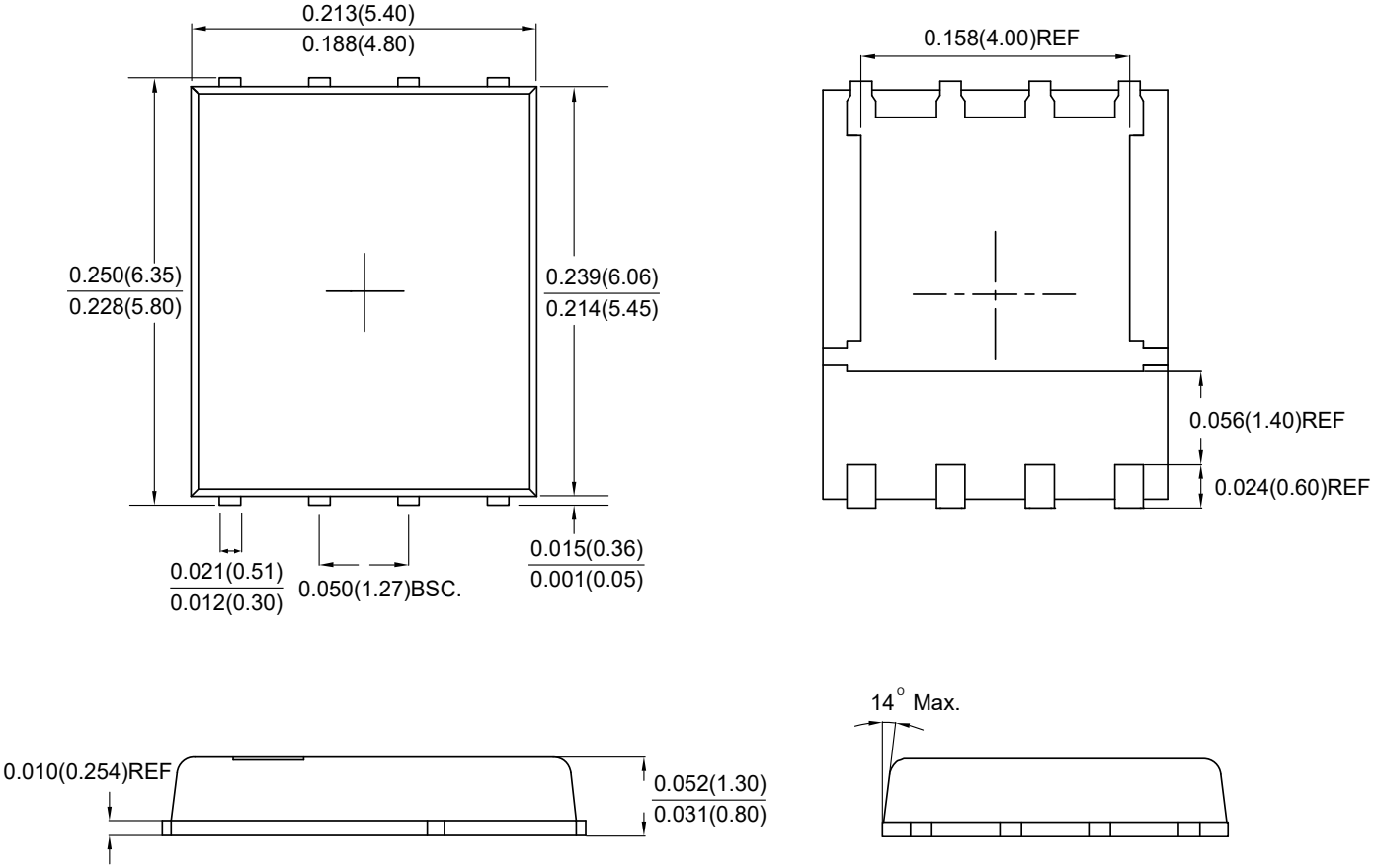


FIG. 8 - Switching Time Waveform



Package Outline Dimensions



PPAK5X6

Dimensions in inches and (millimeters)

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