



#### **General Description**

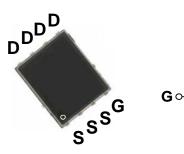
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

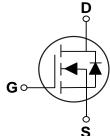
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
40V	$2.8 m\Omega$	100A

#### **Features**

- 40V, 100A,  $R_{DS(ON)}$ =2.8m  $\Omega$  @ $V_{GS}$ =10V
- · Fast switching
- · Improved dv/dt capability
- · Green Device Available

#### PPAK5X6 Pin Configuration





### **Applications**

- · MB / VGA / Vcore
- · POL Applications
- · SMPS 2<sup>nd</sup> SR

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	±20	V
ı	Drain Current - Continuous (T <sub>C</sub> =25°C)	100	Α
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> =100°C)	63	Α
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	400	Α
EAS	Single Pulse Avalanche Energy (NOTE 2)	312	mJ
IAS	Single Pulse Avalanche Current (NOTE 2)	79	Α
$P_{D}$	Power Dissipation (T <sub>C</sub> =25°C)	135	W
' D	Power Dissipation - Derate above 25°C	1.08	W/°(
T <sub>J</sub>	Operating Junction Temperature Range	-50 to 150	°C
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C
Marking Code		ND2P8	

Thermal Characteristics							
Symbol	Parameter	Тур.	Max	Unit			
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62	°C/W			
$R_{ heta JC}$	Thermal Resistance Junction to Case		0.92	°C/W			





## Electrical Characteristics (T<sub>i</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	40			V
1	Drain-Source Leakage Current	$V_{DS}$ =40V , $V_{GS}$ =0V , $T_{j}$ =25 $^{\circ}$ C			1	uA
DSS	Drain-Source Leakage Guirent	$V_{DS}$ =32V , $V_{GS}$ =0V , $T_j$ =125°C			10	uA
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

#### On Characteristics

R	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =25A		2.2	2.8	mΩ
$R_{DS(ON)}$	(NOTE 3)	$V_{GS}$ =4.5V , $I_D$ =12A		2.6	3.5	11122
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 250 uA$	1.2	1.6	2.5	V
gfs	Forward Transconductance	$V_{DS}$ =10V , $I_{D}$ =2A		16		S

#### **Dynamic and switching Characteristics**

$Q_g$	Total Gate Charge (NOTE 3 \ 4)		 44.4	80	
$Q_{gs}$	Gate-Source Charge (NOTE 3 · 4)	$V_{DS}$ =20V , $V_{GS}$ =4.5V , $I_{D}$ =10A	 9.6	18	nC
$Q_{gd}$	Gate-Drain Charge (NOTE 3 · 4)		 16	30	
$T_{d(on)}$	Turn-On Delay Time (NOTE 3 \ 4)		 28	50	
T <sub>r</sub>	Rise Time (NOTE 3 \ 4)	$V_{DD}$ =20V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$ ,	 3.2	6.5	no
$T_{d(off)}$	Turn-Off Delay Time (NOTE 3 \ 4)	I <sub>D</sub> =1A	 89	160	ns
$T_f$	Fall Time (NOTE 3 \ 4)		 14	28	
C <sub>iss</sub>	Input Capacitance		 4940	7800	
Coss	Output Capacitance	$V_{DS}$ =25V , $V_{GS}$ =0V , F=1MHz	 425	800	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		 170	330	
Rg	Gate resistance	$V_{GS}$ =0V , $V_{DS}$ =0V , F=1MHz	 1.4	2.8	Ω

#### **Drain-Source Diode Characteristics and Ratings**

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Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			100	Α
I <sub>SM</sub>	Pulsed Source Current (NOTE 3)	V <sub>G</sub> -V <sub>D</sub> -0V , 1 orde Current			200	Α
$V_{SD}$	Diode Forward Voltage (NOTE 3)	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1	V

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}\text{=}25V,\,V_{GS}\text{=}10V,\,L\text{=}0.1\text{mH},\,I_{AS}\text{=}79\text{A},\,Starting}\,\,T_{J}\text{=}25^{\circ}\!\!\,\text{C}\,.$
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- ${\bf 4.} \ {\bf Essentially \ independent \ of \ operating \ temperature}.$





#### **Characteristics Curves**

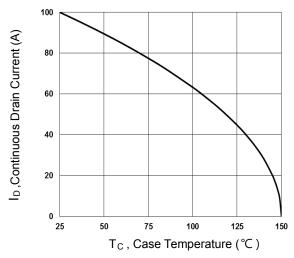


Fig.1 Continuous Drain Current vs.  $T_{
m c}$ 

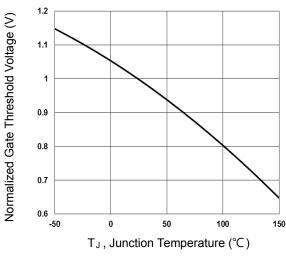


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

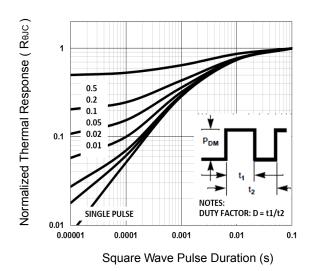


Fig.5 Normalized Transient Impedance

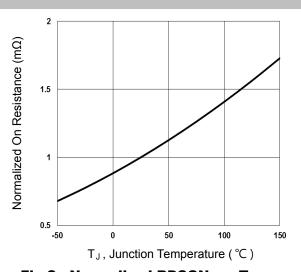


Fig.2 Normalized RDSON vs.  $T_J$ 

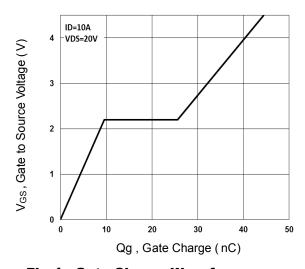


Fig.4 Gate Charge Waveform

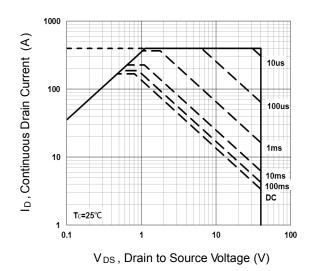
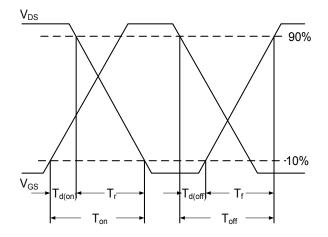


Fig.6 Maximum Safe Operation Area





#### **Characteristics Curves**



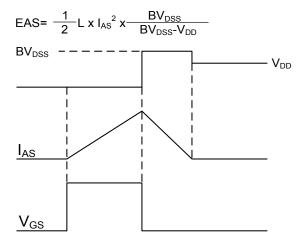


Fig.7 Switching Time Waveform

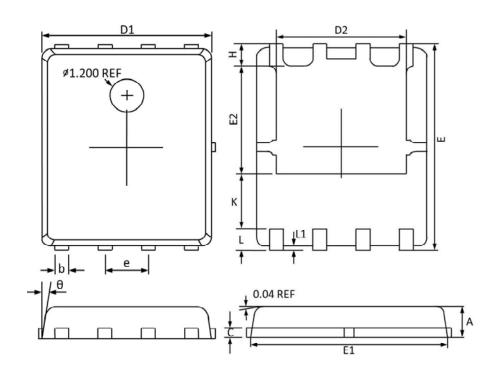
Fig.8 EAS Waveform





### **Package Outline Dimensions**

#### PPAK5X6



Symbol	Dimensions 1	n Millimeters	Dimension	s In Inches
	MAX	MIN	MAX	MIN
A	1.100	0.800	0.043	0.031
b	0.510	0.330	0.020	0.013
С	0.300	0.200	0.012	0.008
D1	5.100	4.800	0.201	0.189
D2	4.100	3.610	0.161	0.142
E	6.200	5.900	0.244	0.232
<b>E</b> 1	5.900	5.700	0.232	0.224
E2	3.780	3.350	0.149	0.132
e	1.2'	7BSC	0.05	BSC
Н	0.700	0.410	0.028	0.016
K	1.500	1.100	0.059	0.043
L	0.710	0.510	0.028	0.020
L1	0.200	0.060	0.008	0.002





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