



General Description

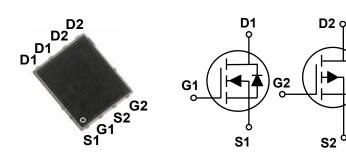
These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV _{DSS}	R _{DS(ON)}	Ι _D
60 V	40 mΩ	6.06 A
-60 V	70 mΩ	-4.58 A

Features

- Fast switching
- · Green Device Available
- · Suit for 4.5V Gate Drive Applications

PPAK5X6 Dual Pin Configuration



Applications

- Battery Protection
- · Load switch
- Uninterruptible Power Supply

Absolute Maximum Ratings T_C=25°C unless otherwise noted **Symbol Parameter** Rating Units V_{DS} Drain-Source Voltage 60 -60 V V_{GS} Gate-Source Voltage ٧ ±20 ±20 I_D Drain Current - Continuous (T_A=25°C) -4.58 6.06 Α I_{DM} Drain Current - Pulsed (NOTE 1) 24 -18 Α EAS Single Pulse Avalanche Energy (NOTE 2 . 6) 25.5 35.3 mJ Single Pulse Avalanche Current (NOTE 2) IAS 22.6 -26.6 Α ${\bf P}_{\rm D}$ Power Dissipation (T_A=25°C) W 1.47 T_J -55 to 150 Operating Junction Temperature Range ٥С Storage Temperature Range -55 to 150 T_{STG} ٥С BG040 Marking Code

Thermal Characteristics						
Symbol	Parameter	Rating	Unit			
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	85	°C/W			





N Channel Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	60			V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =48V , V _{GS} =0V			1	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	IStatic Drain-Source On-Resistance	V_{GS} =10V , I_D =4A			40	mΩ
		V _{GS} =4.5V , I _D =2A			45	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.2		2.5	V
gfs	Forward Transconductance	V_{DS} =5V , I_{D} =4A		21		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge			12.6		
Q_gs	Gate-Source Charge	V_{DS} =48V , V_{GS} =4.5V , I_{D} =4A		3.2		nC
Q_gd	Gate-Drain Charge			6.3		
$T_{d(on)}$	Turn-On Delay Time			8		
T _r	Rise Time	V_{DD} =30V , V_{GS} =10V , R_{G} =3.3 Ω		14.2		nS
$T_{d(off)}$	Turn-Off Delay Time	, I _D =4A		24.4		113
T_f	Fall Time			4.6		
C _{iss}	Input Capacitance			1378		
C_{oss}	Output Capacitance	V_{DS} =15V , V_{GS} =0V , F=1MHz		86		pF
C_{rss}	Reverse Transfer Capacitance			64		
R_{g}	Gate Resistance	V _{GS} =0V , V _{DS} =0V , F=1MHz		3.2		Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V,Force Current		-	4.8	Α
I _{SM}	Pulsed Source Current				9.6	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A			1.2	V

NOTES:

- ${\bf 1.}\ Repetitive\ Rating: Pulsed\ width\ limited\ by\ maximum\ junction\ temperature.$
- 2. V_{DD} =25V, V_{GS} =10V, L=0.1mH, I_{AS} =22.6A.
- 3. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 4. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.





Characteristics Curves

FIG. 1-Output Characteristics

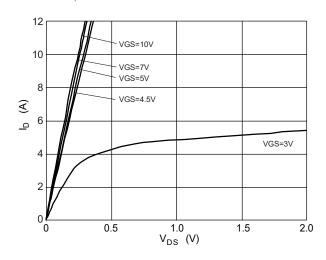


FIG. 2-R $_{DS(ON)}$ vs V_{GS}

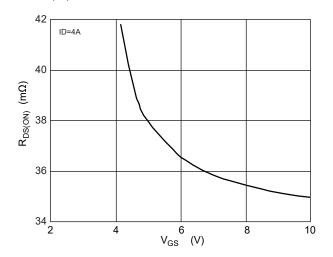


FIG. 3-Normalized V_{th} vs T_J

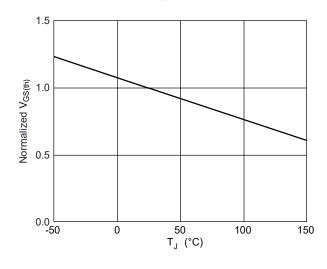


FIG. 4-Normalized R_{DS(ON)} vs T_J

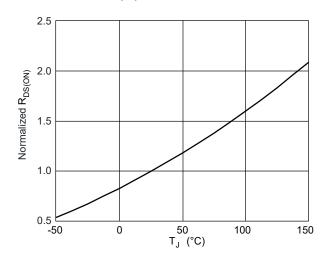


FIG. 5- I_S vs V_{SD}

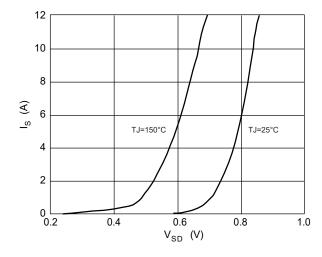
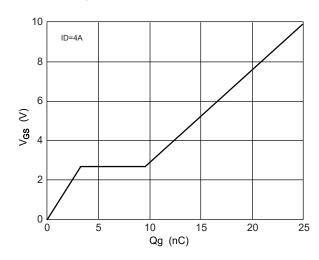


FIG. 6-Gate Charge Characteristics







P Channel Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V , I _D = -250uA	-60			V
I _{DSS}	Drain-Source Leakage Current	V_{DS} = -48V , V_{GS} = 0V			-1	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = ±20V , V_{DS} = 0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} = -10V , I_D = -3A			70	mΩ
		V_{GS} = -4.5V , I_{D} = -2A			105	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250uA$	-1.2		-2.5	V
gfs	Forward Transconductance	V_{DS} = -5V , I_D = -3A		15		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge	V _{DS} = -48V , V _{GS} = -4.5V ,		9.86		
Q_{gs}	Gate-Source Charge	I _D = -3A		3.1		nC
Q_{gd}	Gate-Drain Charge	.b		2.95		
$T_{d(on)}$	Turn-On Delay Time		-	28.8		
T _r	Rise Time	V_{DD} = -15V , V_{GS} = -10V ,	-	19.8		nS
$T_{d(off)}$	Turn-Off Delay Time	R_G = 3.3 Ω , I_D = -1 A		60.8		113
T_f	Fall Time			7.2		
C _{iss}	Input Capacitance			1447		
C _{oss}	Output Capacitance	V_{DS} = -15V , V_{GS} = 0V , F= 1MHz		97.3		pF
C _{rss}	Reverse Transfer Capacitance			70		
R_g	Gate Resistance	V_{GS} =0V , V_{DS} =0V , F=1MHz		13.5		Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V,Force Current			-3.7	Α
I _{SM}	Pulsed Source Current				-7.5	Α
V_{SD}	Diode Forward Voltage	V_{GS} =0V , I_{S} = -1A			-1.2	V

NOTES:

- 5. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 6. V_{DD} =-25V, V_{GS} =-10V, L=0.1mH, I_{AS} =-26.6A.
- 7. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 8. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.





Characteristics Curves

FIG. 7-Output Characteristics

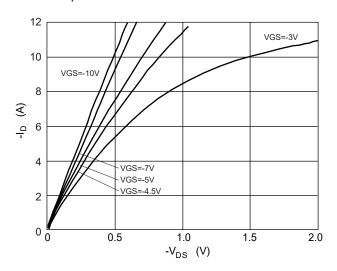


FIG. 8- $R_{DS(ON)}$ vs V_{GS}

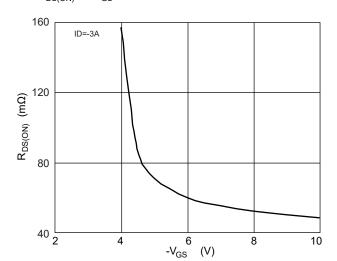


FIG. 9-Normalized V_{th} vs T_J

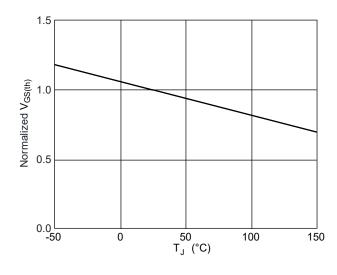


FIG. 10-Normalized R_{DS(ON)} vs T_J

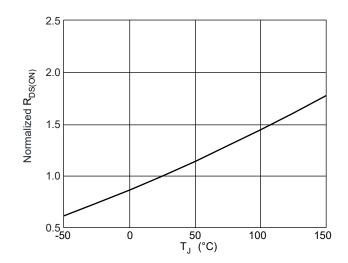


FIG. 11- I_S vs V_{SD}

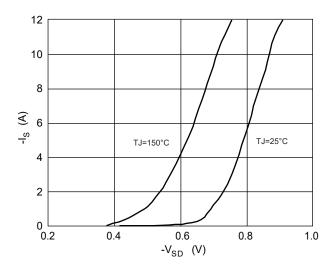
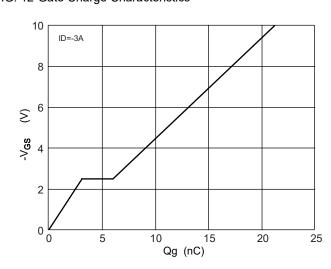


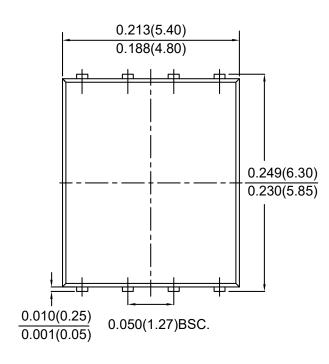
FIG. 12-Gate Charge Characteristics

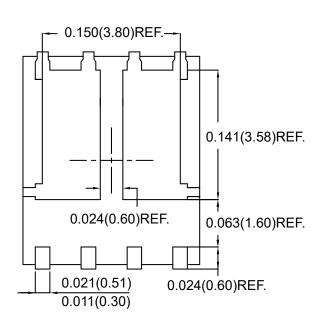


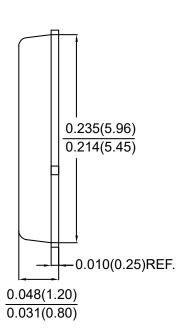




Package Outline Dimensions







PPAK5X6 Dual

Dimensions in inches and (millimeters)





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