



20V P-Channel MOSFETs

General Description

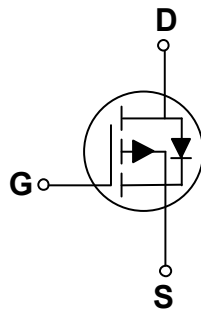
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV_{DSS}	R_{DS(ON)}	I_D
-20 V	9 mΩ	-50 A

Features

- $R_{DS(ON)} \leq 9m\Omega @ V_{GS} = -4.5V$
- Fast switching
- Green Device Available
- Improved dv/dt Capability

PPAK3X3 Pin Configuration



Applications

- Battery Protection
- Load Switch
- Uninterruptible Power Supply

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Drain Current - Continuous ($T_c=25^\circ C$)	-50	A
I_{DM}	Drain Current - Pulsed (NOTE 1)	-100	A
P_D	Power Dissipation ($T_c=25^\circ C$)	29	W
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
Marking Code		PB9P0	

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	75	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	4.2	$^\circ C/W$



Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = -250uA	-20	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} = -20V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±12V, V _{DS} =0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} = -4.5V, I _D = -15A	---	---	9	mΩ
		V _{GS} = -2.5V, I _D = -10A	---	---	11	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D = -250uA	-0.3	---	-1.0	V
g _{fs}	Forward Transconductance	V _{DS} = -5V, I _D = -10A	---	43	---	S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q _g	Total Gate Charge	V _{DS} = -15V, V _{GS} = -4.5V, I _D = -10A	---	63	---	nC
Q _{gs}	Gate-Source Charge		---	9.1	---	
Q _{gd}	Gate-Drain Charge		---	13	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} = -10V, V _{GS} = -4.5V, R _G = 3.3Ω, I _D = -10A	---	15.8	---	nS
T _r	Rise Time		---	76.8	---	
T _{d(off)}	Turn-Off Delay Time		---	193	---	
T _f	Fall Time		---	186.4	---	
C _{ISS}	Input Capacitance	V _{DS} = -15V, V _{GS} = 0V, F= 1MHz	---	5783	---	pF
C _{OSS}	Output Capacitance		---	509	---	
C _{rSS}	Reverse Transfer Capacitance		---	431	---	

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G = V _D = 0V, Force Current	---	---	-10.7	A
I _{SM}	Pulsed Source Current		---	---	-60	A
V _{SD}	Diode Forward Voltage	V _{GS} = 0V, I _S = -1A, T _J =25°C	---	---	-1.2	V

NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
3. Essentially independent of operating temperature.



Characteristics Curves

Fig.1 I_D vs. T_C

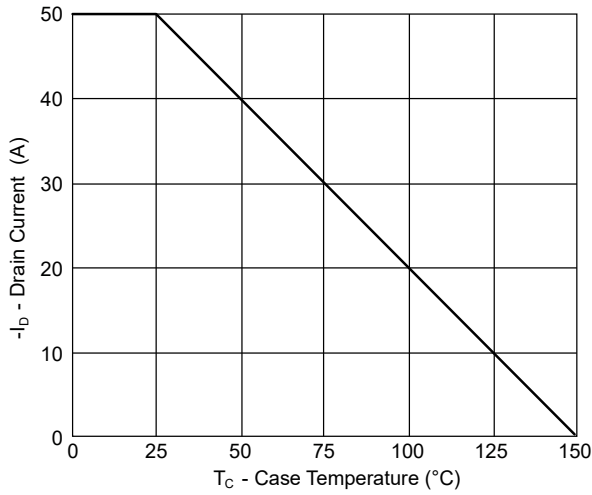


Fig.2 Normalized V_{th} vs. T_J

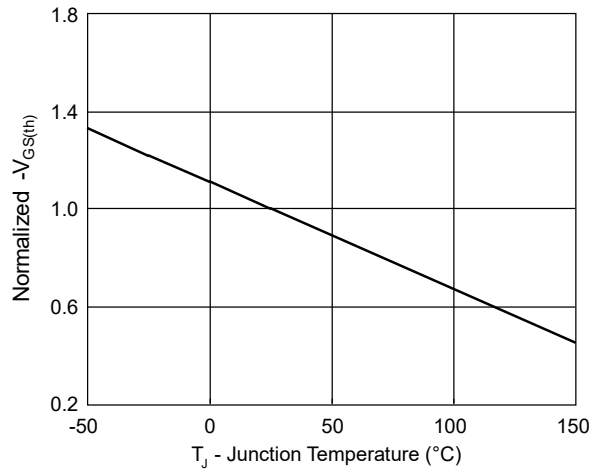


Fig.3 Normalized $R_{DS(on)}$ vs. T_J

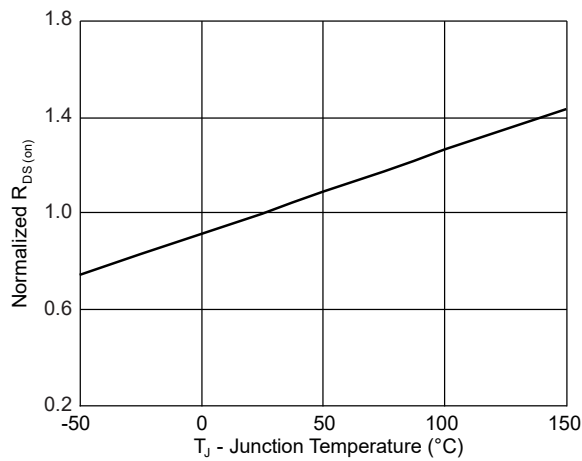


Fig.4 Gate Charge Waveform

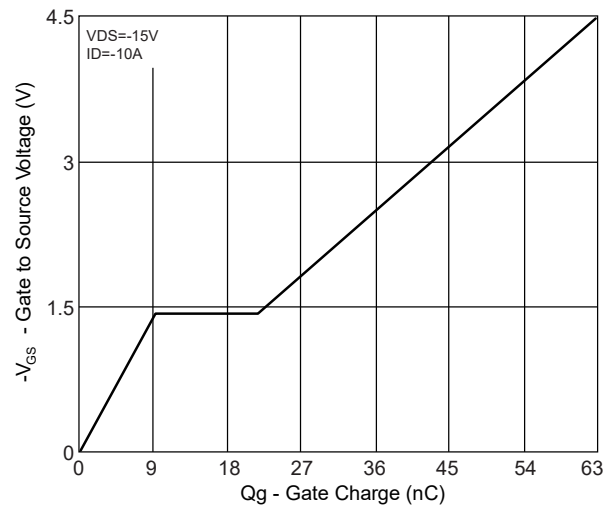


Fig.5 Safe Operation Area

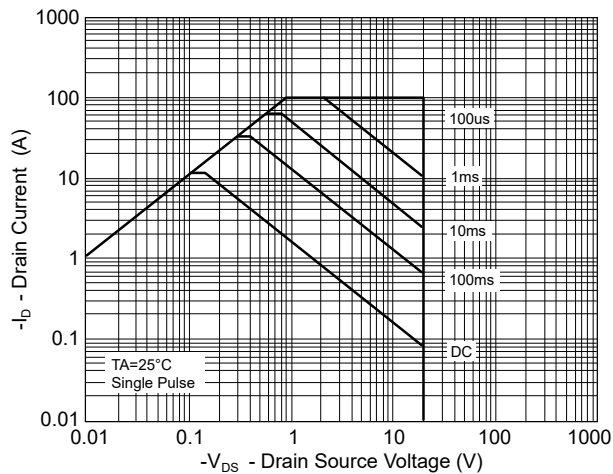
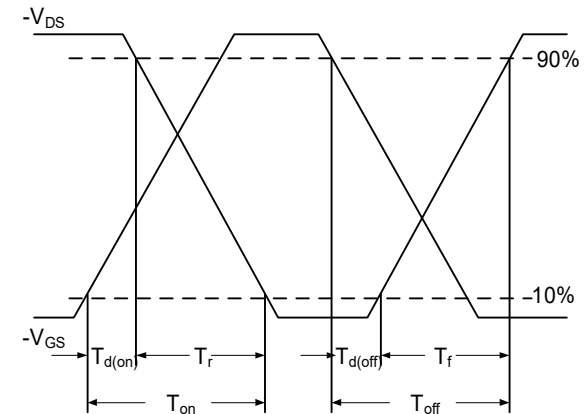


Fig.6 Switching Time Waveform



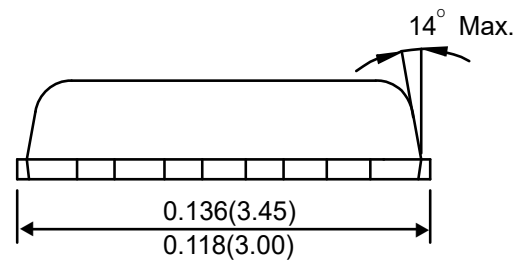
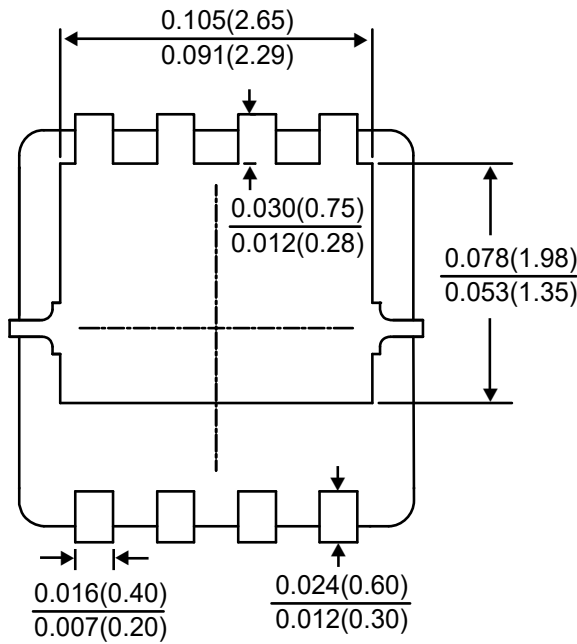
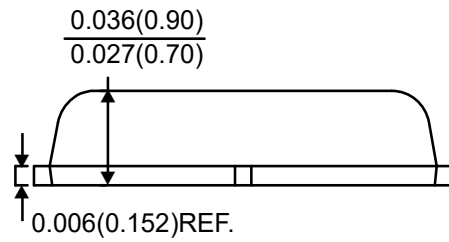
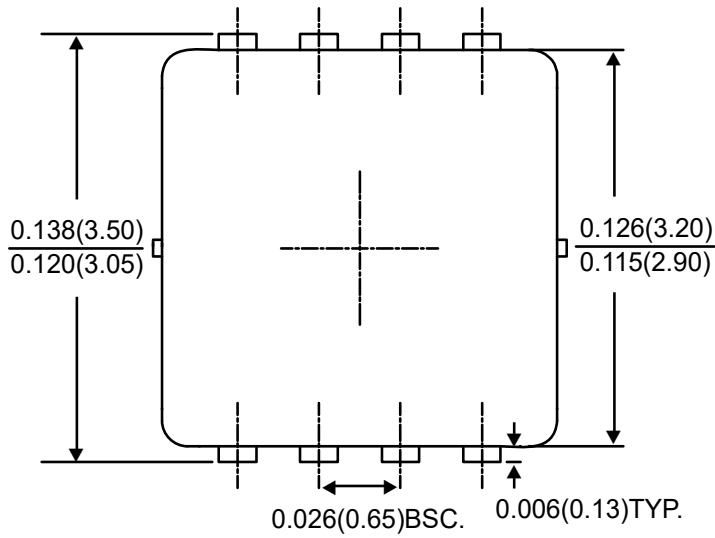


P3MPB9P0



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Package Outline Dimensions



PPAK3X3

Dimensions in inches and (millimeters)



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