



**60V N-Channel MOSFETs**

**General Description**

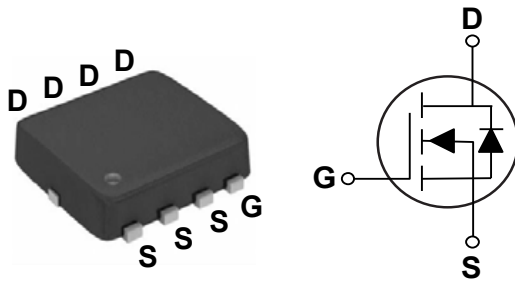
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

<b>BV<sub>DSS</sub></b>	<b>R<sub>DS(ON)</sub></b>	<b>I<sub>D</sub></b>
60 V	6 mΩ	66 A

**Features**

- $R_{DS(ON)} \leq 6m\Omega @ V_{GS}=10V$
- Improved dv/dt Capability
- Fast Switching
- Green Device Available

PPAK3X3 Pin Configuration



**Applications**

- Networking
- Load Switch
- LED Applications

**Absolute Maximum Ratings  $T_c=25^\circ\text{C}$  unless otherwise noted**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current - Continuous ( $T_c=25^\circ\text{C}$ )	66	A
$I_{DM}$	Drain Current - Pulsed (NOTE 1)	90	A
EAS	Single Pulse Avalanche Energy (L=0.1mH)	22	mJ
IAS	Single Pulse Avalanche Current (L=0.1mH)	21	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	31.25	W
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
Marking Code		NG6P0	

**Thermal Characteristics**

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	50	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	4	$^\circ\text{C/W}$



Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V	---	---	1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =15A	---	---	6	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =8A	---	---	10	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	---	3.0	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>DS</sub> =20A	---	31	---	S

Dynamic and switching Characteristics (NOTE 3)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	37.4	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	6.5	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	10	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, R <sub>GEN</sub> =3Ω, I <sub>D</sub> =1A	---	9.5	---	nS
T <sub>r</sub>	Rise Time		---	26	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	29	---	
T <sub>f</sub>	Fall Time		---	19.5	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, F=1MHz	---	2083	---	pF
C <sub>oss</sub>	Output Capacitance		---	793	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	16.5	---	
R <sub>g</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	---	2.1	---	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A	---	---	1.1	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =20A, V <sub>R</sub> =30V,	---	40	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/us	---	48.7	---	nC

NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
3. Guaranteed by design, not subject to production testing.



Characteristics Curves

FIG. 1- $I_D$  vs  $T_C$

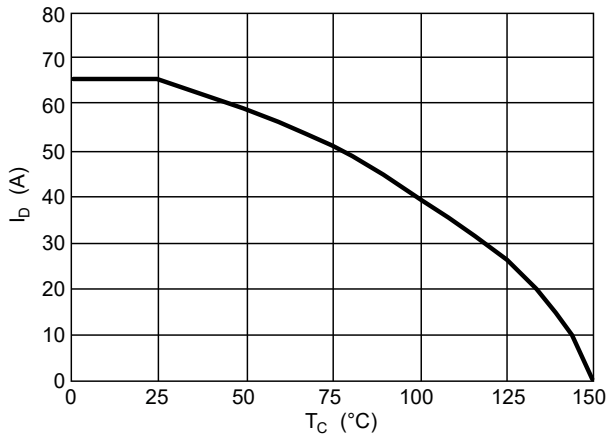


FIG. 2-Normalized  $V_{GS(th)}$  vs  $T_J$

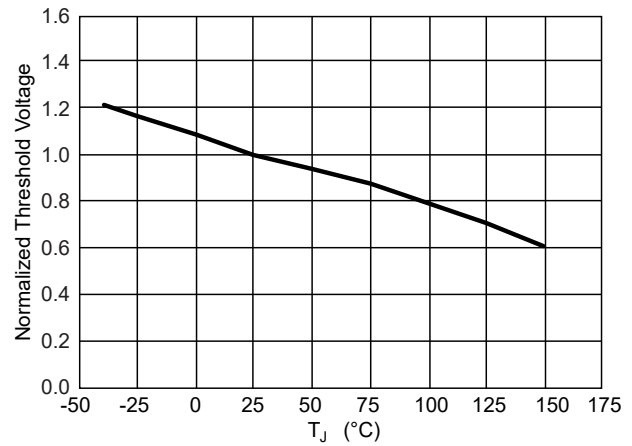


FIG. 3-Normalized  $R_{DS(on)}$  vs  $T_J$

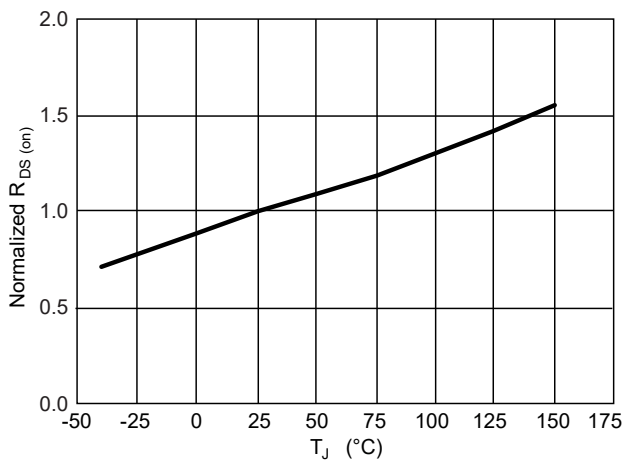


FIG. 4-Gate Charge Characteristics

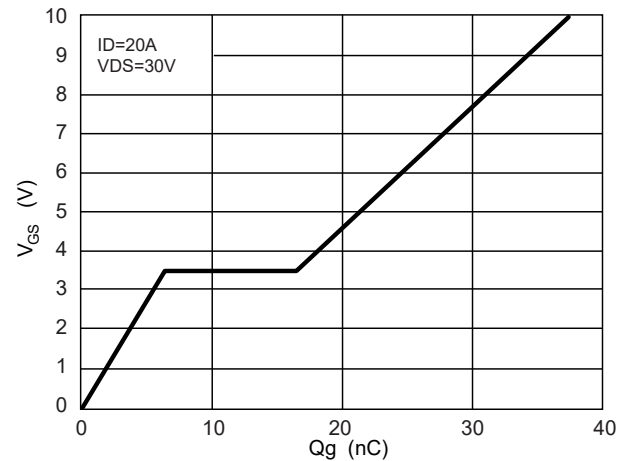


FIG. 5- $R_{DS(on)}$  vs  $I_D$

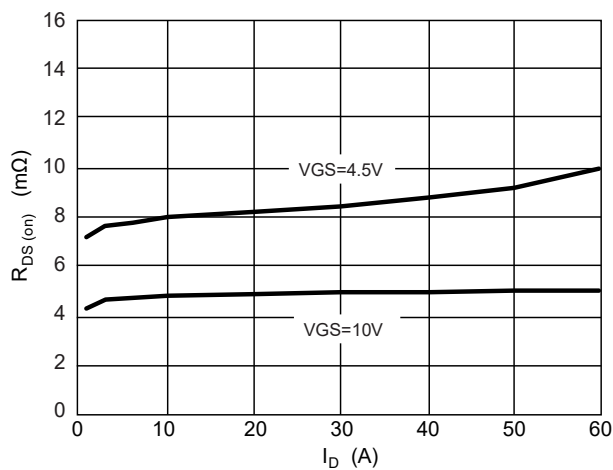
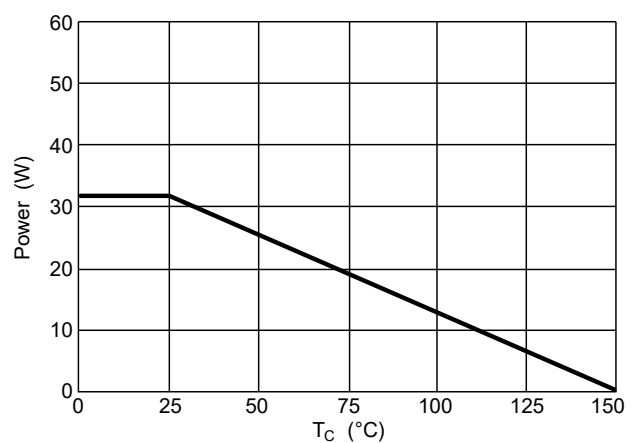


FIG. 6-Power Dissipation





Characteristics Curves

FIG. 7-Switching Time Waveform

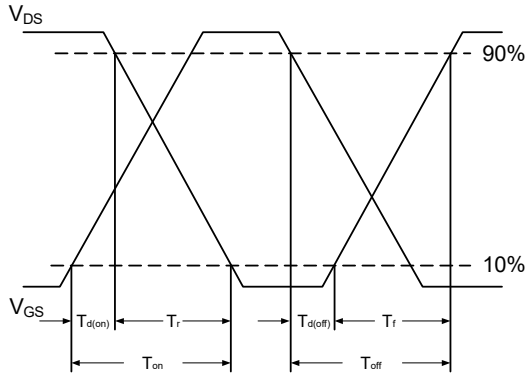
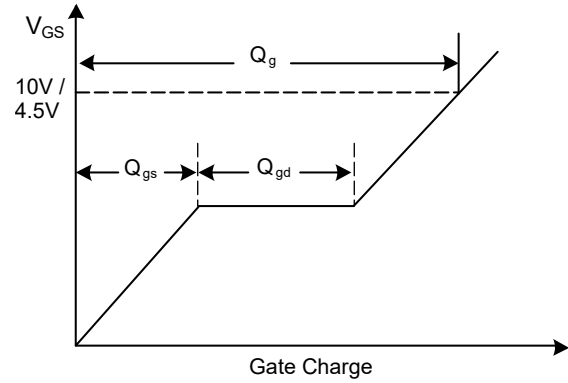
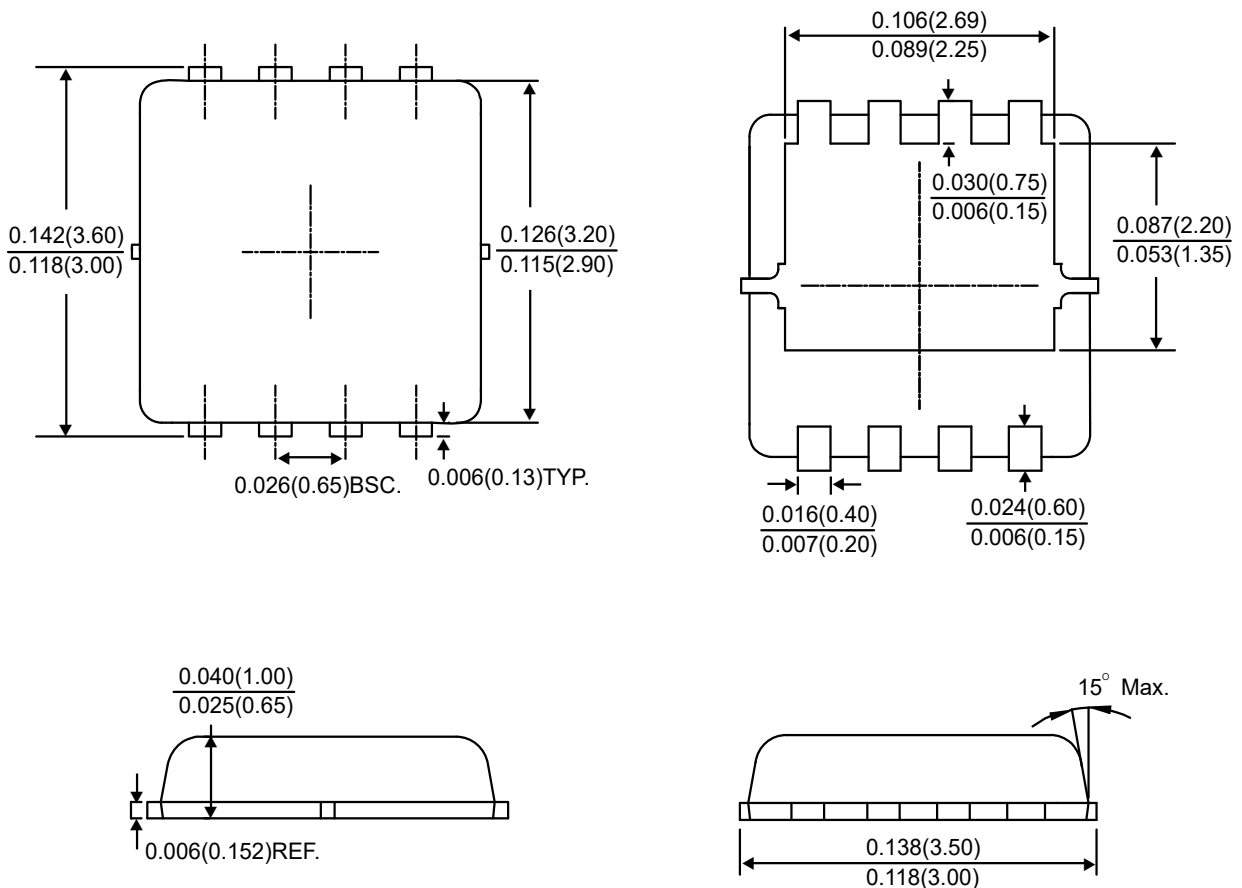


FIG. 8-Gate Charge Waveform



Package Outline Dimensions



PPAK3X3

Dimensions in inches and (millimeters)



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