



General Description

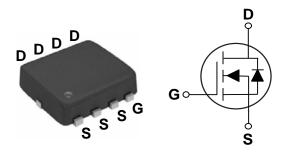
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV _{DSS}	R _{DS(ON)}	I _D
30 V	7.2 mΩ	54 A

Features

- 30V, 54A, $R_{DS(ON)} \le 7.2 m \Omega @V_{GS} = 10V$
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available

PPAK3X3 Pin Configuration



Applications

- · MB / VGA / Vcore
- POL Applications
- SMPS 2nd SR

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±20	V
1	Drain Current - Continuous (T _C =25°C)	54	Α
I _D	Drain Current - Continuous (T _C =100°C)	34	Α
I _{DM}	Drain Current - Pulsed (NOTE 1)	216	Α
E _{AS}	Single Pulse Avalanche Energy (NOTE 2)	45	mJ
I _{AS}	Single Pulse Avalanche Current (NOTE 2)	30	Α
P_D	Power Dissipation (T _C =25°C)	34.7	W
ГD	Power Dissipation - Derate above 25°C	0.28	W/°C
T_J	Operating Junction Temperature Range	-50 to 150	°C
T_{STG}	Storage Temperature Range	-50 to 150	°C
Marking Code		NC7P2	

Thermal Characteristics						
Symbol Parameter Ty		Тур.	Max.	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62	°C/W		
$R_{ heta JC}$			3.6	°C/W		





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	30			٧
I _{DSS}	IDrain-Source Leakage Current	V_{DS} =30V , V_{GS} =0V , T_J =25 $^{\circ}$ C			1	uA
		V_{DS} =24V , V_{GS} =0V , T_{J} =125 $^{\circ}$ C			10	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}		V _{GS} =10V , I _D =16A		5.7	7.2	mΩ
		V_{GS} =4.5V , I_D =8A		8.4	11.5	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	1.2	1.6	2.5	V
gfs	Forward Transconductance	V_{DS} =10V , I_{D} =3A		15		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge	\\ 45\\ \\ 40\\ \ \ 000		11.4	22	
Q_{gs}	Gate-Source Charge	-V _{DS} =15V , V _{GS} =10V , I _D =20A -(NOTE 3 \ 4)		2.1	4	nC
Q_{gd}	Gate-Drain Charge	(1012 3 + 4)		2.5	5	
$T_{d(on)}$	Turn-On Delay Time	V_{DD} =15V , V_{GS} =10V , R_{G} =3.3 Ω , $ I_{D}$ =15A		4.8	9	
T _r	Rise Time			12.5	24	nS
$T_{d(off)}$	Turn-Off Delay Time			27.6	52	110
T _f	Fall Time			8.2	16	
C _{iss}	Input Capacitance	V _{DS} =25V , V _{GS} =0V , F=1MHz		850	1700	
C _{oss}	Output Capacitance			133	260	pF
C _{rss}	Reverse Transfer Capacitance			78	160	
Rg	Gate resistance	V_{GS} =0V , V_{DS} =0V , F=1MHz		2.4		Ω

Guaranteed Avalanche Energy

Sy	mbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
	E _{AS}	Single Pulse Avalanche Energy	V _{DD} =25V , L=0.1mH , I _{AS} =15A	12			mJ

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current			54	Α
I _{SM}	Pulsed Source Current (NOTE 3)				108	Α
V_{SD}	Diode Forward Voltage (NOTE 3)	V_{GS} =0V , I_{S} =1A , T_{J} =25 $^{\circ}$ C			1	V
trr	Reverse Recovery Time	V_{GS} =0V , I_S =10A , di/dt=100A/us		126		nS
Qrr	Reverse Recovery Charge	, T _J =25°C		165		nC

NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. $\rm V_{DD}$ =25V, $\rm V_{GS}$ =10V, L=0.1mH, $\rm I_{AS}$ =30A, $\rm R_{G}$ =25 Ω , Starting $\rm T_{J}$ =25 $^{\circ}\!\rm C$.
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.





Characteristics Curves

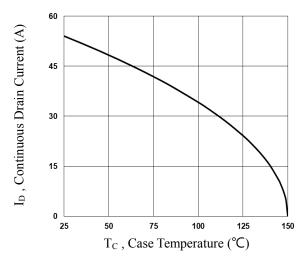


Fig.1 Continuous Drain Current vs. Tc

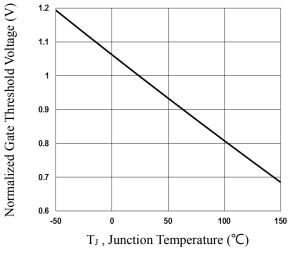


Fig.3 Normalized V_{th} vs. T_J

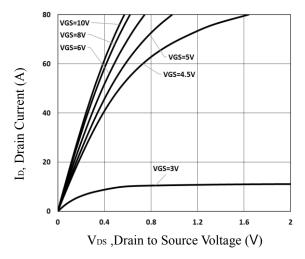


Fig.5 Typical Output Characteristics

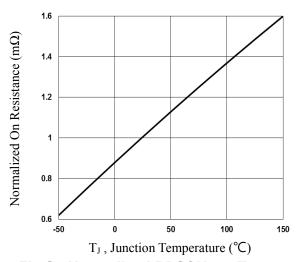


Fig.2 Normalized RDSON vs. T_J

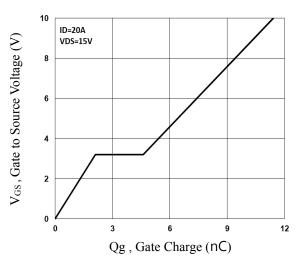


Fig.4 Gate Charge Waveform

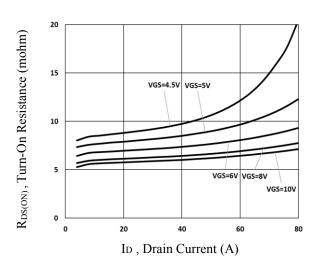


Fig.6 Turn-On Resistance vs. ID





Characteristics Curves

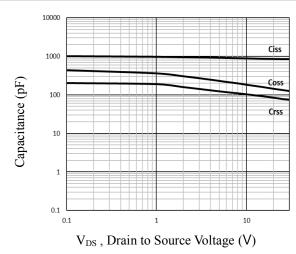


Fig.7 Capacitance Characteristics

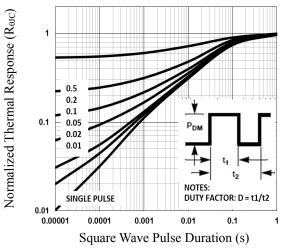


Fig.9 Normalized Transient Impedance

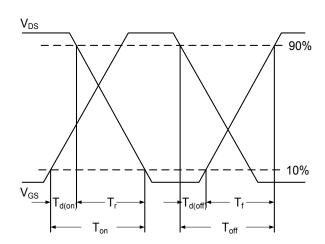


Fig.11 Switching Time Waveform

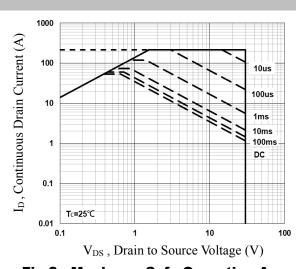


Fig.8 Maximum Safe Operation Area

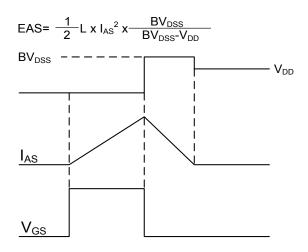
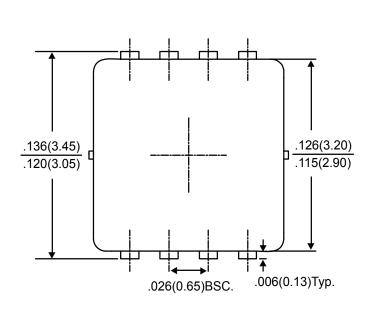


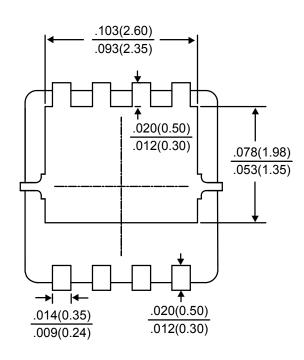
Fig.10 EAS Waveform

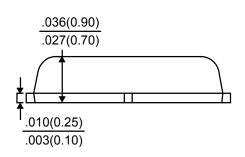


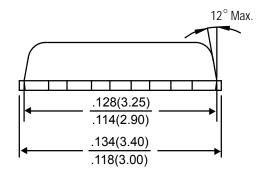


Package Outline Dimensions









PPAK3X3Dimensions in inches and (millimeters)





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