## **SPDT SWITCH GaAs MMIC**

### ■ GENERAL DESCRIPTION

NJG1635AHB6 is a GaAs SPDT switch IC suited for mobile handset, WiBro and WiMAX devices. This switch features high power handling, low insertion loss, high isolation.

This switch includes logic decoder function, and can be operated by single bit control signal from 1.3V of logical high voltage. In addition, this switch includes ESD protection circuits.

The ultra-small & ultra-thin USB8-B6 package is adopted.

### ■ APPLICATIONS

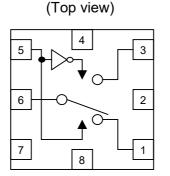
LTE, UMTS, CDMA and WiMAX applications Mobile phone, Data card, Tablet PC and Femtocell applications Antenna switching, Bands switching and Post PA switching applications General Purpose Switching application

+1.3V~+4.5V

#### **■** FEATURES

- Single bit low voltage control
- Operation supply voltage +2.5~+4.5V
- Low insertion loss
- High isolation
- High power handling
- ESD protection circuit
- Small & thin package

## ■ PIN CONFIGURATION



0.30dB typ. @f=0.9GHz, PIN=30dBm, VDD=2.7V 0.35dB typ. @f=1.9GHz, PIN=30dBm, VDD=2.7V 0.40dB typ. @f=2.7GHz, PIN=30dBm, VDD=2.7V 35dB typ. @f=0.9/1.9GHz, P<sub>IN</sub>=30dBm, V<sub>DD</sub>=2.7V 33dB typ. @f=2.7GHz, P<sub>IN</sub>=30dBm, V<sub>DD</sub>=2.7V P-0.1dB=32dBm min. @f=2.7GHz, VDD=2.7V

USB8-B6 (Package size: 1.5 x 1.5 x 0.55mm)

Pin connection 1. P1 2. GND 3. P2 4. GND 5. VCTL 6. PC 7. VDD 8. GND

P2-PC

## ■ TRUTH TABLE

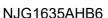
| Control Voltage: "H"=V <sub>CTL (H)</sub> , "L"=V <sub>CTL (L)</sub> |       |  |  |
|--|-------|--|--|
| VCTL   | PATH  |  |  |
| Н  | P1-PC |  |  |
| L  |       |  |  |

NOTE: Please note that any information on this datasheet will be subject to change

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## PACKAGE OUTLINE





## ■ ABSOLUTE MAXIMUM RATINGS

|                   |                  |   | (T <sub>a</sub> =+25°C, Z <sub>s</sub> = | Z <sub>I</sub> =50Ω)) |
|-------------------|------------------|---|--|-----------------------|
| PARAMETER         | SYMBOL           | CONDITIONS                                      | CONDITIONS                               | UNITS                 |
| RF Input Power    | P <sub>IN</sub>  | V <sub>DD</sub> =2.7V, V <sub>CTL</sub> =0/1.8V | 35                                       | dBm                   |
| Supply Voltage    | $V_{\text{DD}}$  | VDD terminal                                    | 5.0                                      | V                     |
| Control Voltage   | V <sub>CTL</sub> | VCTL terminal                                   | 5.0                                      | V                     |
| Power Dissipation | PD               | on PCB board                                    | 160                                      | mW                    |
| Operating Temp.   | T <sub>opr</sub> |   | -40~+95                                  | °C                    |
| Storage Temp.     | T <sub>stg</sub> |   | -55~+150                                 | °C                    |

## ■ ELECTRICAL CHARACTERISTICS

(General conditions:  $V_{DD}=2.7V$ ,  $V_{CTL(L)}=0V$ ,  $V_{CTL(H)}=1.8V$ ,  $Z_S=Z_I=50\Omega$ ,  $T_a=+25^{\circ}C$ , with application circuit)

| PARAMETERS                           | SYMBOL              |  | MIN | TYP  | MAX  | UNITS |
|--------------------------------------|---------------------|--|-----|------|------|-------|
| Operating Voltage                    | V <sub>DD</sub>     |  | 2.5 | 2.7  | 4.5  | V     |
| Operating Current                    | I <sub>DD</sub>     | P <sub>IN</sub> =30dBm                           | -   | 25   | 50   | μA    |
| Control Voltage (LOW)                | V <sub>CTL(L)</sub> |  | 0   | -    | 0.4  | V     |
| Control Voltage (HIGH)               | $V_{\text{CTL}(H)}$ |  | 1.3 | 1.8  | 4.5  | V     |
| Control Current                      | ICTL                |  | -   | 5    | 10   | μA    |
| Insertion Loss 1                     | LOSS1               | f=0.9GHz, P <sub>IN</sub> =30dBm                 | -   | 0.30 | 0.45 | dB    |
| Insertion Loss 2                     | LOSS2               | f=1.9GHz, P <sub>IN</sub> =30dBm                 | -   | 0.35 | 0.50 | dB    |
| Insertion Loss 3                     | LOSS3               | f=2.7GHz, P <sub>IN</sub> =30dBm                 | -   | 0.40 | 0.60 | dB    |
| Isolation 1                          | ISL1                | f=0.9GHz, P <sub>IN</sub> =30dBm                 | 32  | 35   | -    | dB    |
| Isolation 2                          | ISL2                | f=1.9GHz, P <sub>IN</sub> =30dBm                 | 30  | 35   | -    | dB    |
| Isolation 3                          | ISL3                | f=2.7GHz, P <sub>IN</sub> =30dBm                 | 25  | 33   | -    | dB    |
| Input Power at<br>0.1dB Compression  | P <sub>-0.1dB</sub> | f=2.7GHz   | 32  | -    | -    | dBm   |
| 2nd Harmonics 1                      | 2f <sub>o</sub> (1) | f=0.9GHz, P <sub>IN</sub> =26dBm                 | -   | -75  | -65  | dBc   |
| 2nd Harmonics 2                      | 2f <sub>o</sub> (2) | f=1.9GHz, P <sub>IN</sub> =26dBm                 | -   | -75  | -65  | dBc   |
| 3rd Harmonics 1                      | 3f <sub>o</sub> (1) | f=0.9GHz, P <sub>IN</sub> =26dBm                 | -   | -80  | -65  | dBc   |
| 3rd Harmonics 2                      | 3f <sub>o</sub> (2) | f=1.9GHz, P <sub>IN</sub> =26dBm                 | -   | -80  | -65  | dBc   |
| Input 3rd Order<br>Intercept Point 1 | IIP3(1)             | f=0.9GHz+0.901GHz<br>P <sub>IN</sub> =25dBm each | 58  | 64   | -    | dBm   |
| Input 3rd Order<br>Intercept Point 2 | IIP3(2)             | f=1.9GHz+1.901GHz<br>P <sub>IN</sub> =25dBm each | 58  | 62   | -    | dBm   |
| VSWR (PC, P1, P2)                    | VSWR                | f=2.7GHz, ON State                               | -   | 1.2  | 1.4  |       |
| Switching time                       | T <sub>sw</sub>     | PC-P1, PC-P2 port switching time                 | -   | 2    | 5    | μs    |

The input 3rd order intercept point is defined as following equation, IIP3 = (3 x Pout - IM3)/2 + LOSS

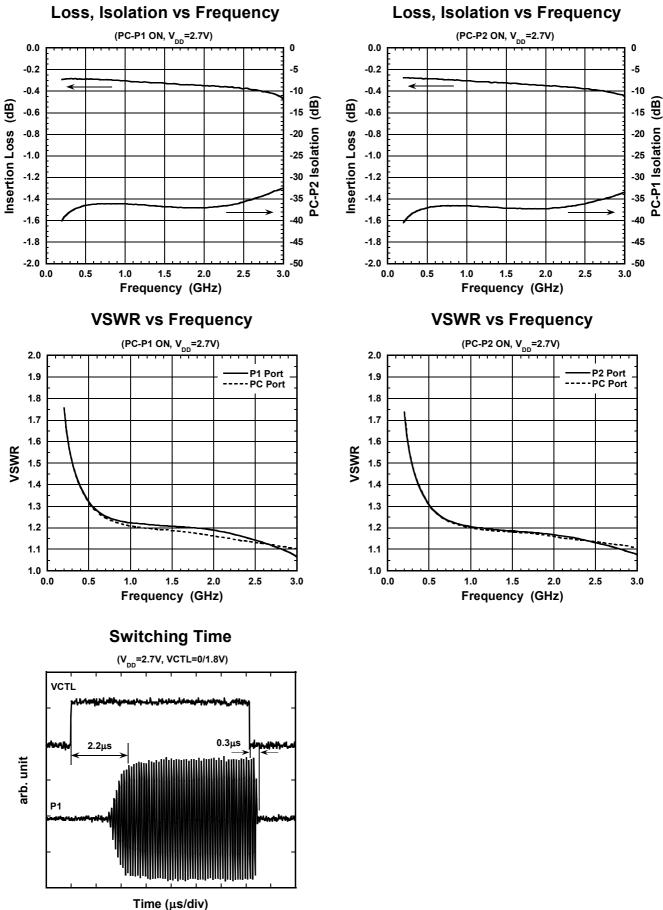
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#### ■ TERMINAL INFORMATION

| No. | SYMBOL | DESCRIPTION  |
|-----|--------|--|
| 1   | P1     | This port is connected to PC port by control voltage of $+1.3 \sim 4.5 V(V_{CTL(H)})$ to 5th pin. An external capacitor is required to block the DC bias voltage of internal circuit.              |
| 2   | GND    | Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.   |
| 3   | P2     | This port is connected to PC port by control voltage of $+0.0\sim0.4V(V_{CTL(L)})$ to 5th pin. An external capacitor is required to block the DC bias voltage of internal circuit.                 |
| 4   | GND    | Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.   |
| 5   | VCTL   | Control port. This terminal is set to +1.3V~4.5V of logical high level for ON state between PC and P1 RF ports, and set to +0.0~0.4V of logical low level for ON state between PC and P2 RF ports. |
| 6   | PC     | Common RF port. This PC port is connected to P1 or P2 by logical control voltage of VCTL.<br>In order to block DC bias voltage of internal circuit, an external capacitor is required.             |
| 7   | VDD    | A supply voltage terminal (+2.5~+4.5V). Please place a bypass capacitor between this and GND for avoiding RF noise from outside.   |
| 8   | GND    | Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.   |

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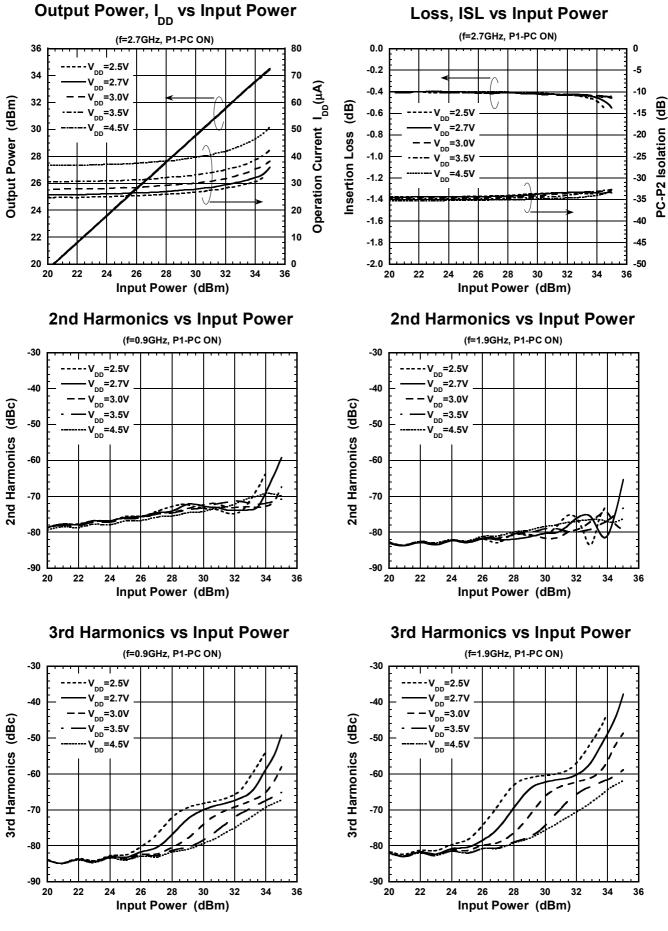
■ ELECTRICAL CHARACTERISTICS (with Application circuit, Loss of external circuit are excluded)



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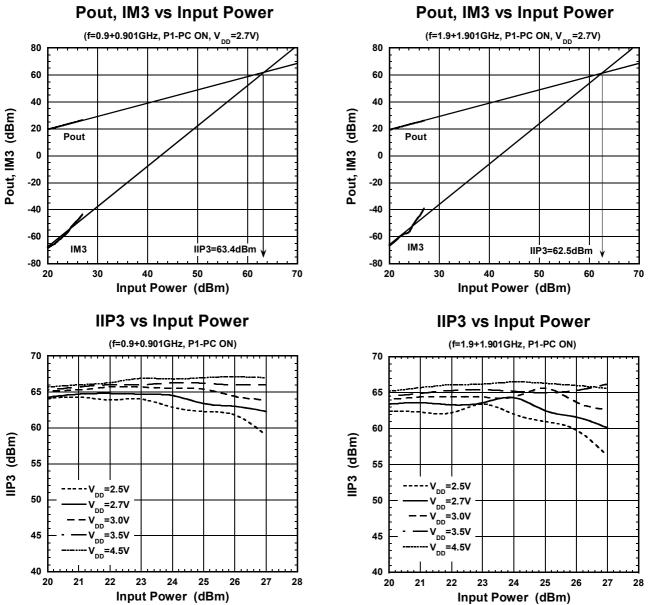
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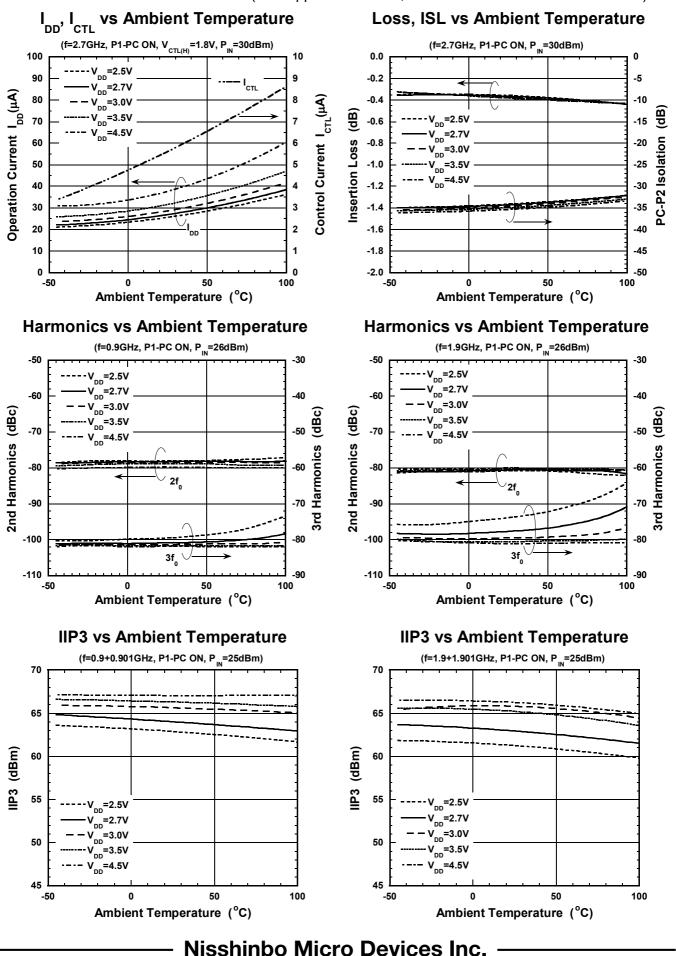




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■ ELECTRICAL CHARACTERISTICS (with Application circuit, Loss of external circuit are excluded)



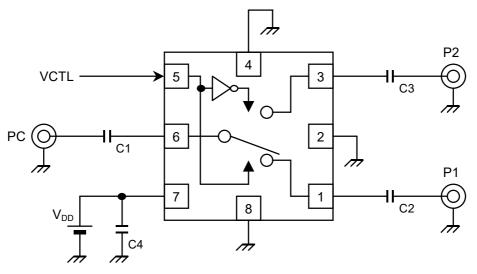


■ ELECTRICAL CHARACTERISTICS (with Application circuit, Loss of external circuit are excluded)

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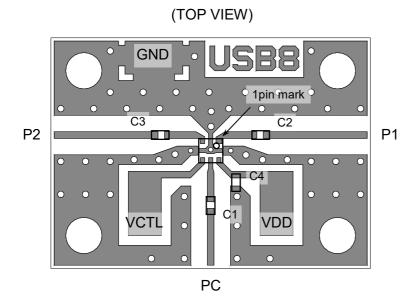
# NJG1635AHB6

## APPLICATION CIRCUIT



| PART ID | Value  | COMMENT |
|---------|--------|---------|
| C1~C3   | 56pF   | MURATA  |
| C4      | 1000pF | (GRM15) |

## ■ TEST PCB LAYOUT



PCB SIZE=19.4x14.0mm PCB: FR-4, t=0.2mm CAPACITOR: size 1005 STLIPLINE=0.4mm

Losses of PCB, capacitors and connectors

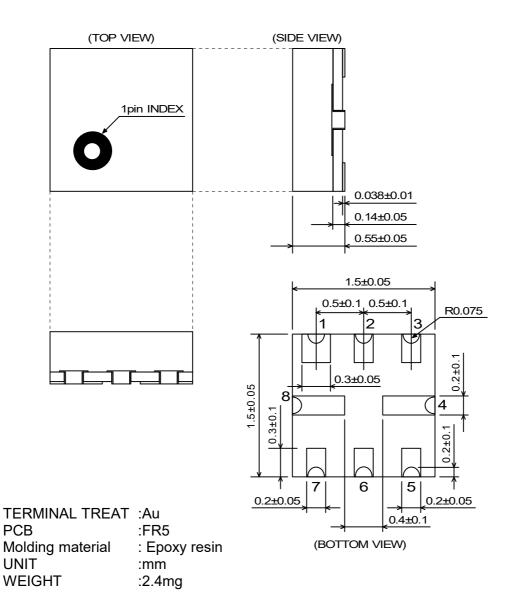
| Frequency (GHz) | Loss (dB) |
|-----------------|-----------|
| 0.9             | 0.21      |
| 1.9             | 0.30      |
| 2.7             | 0.38      |

## PRECAUTIONS

[1]The DC blocking capacitors have to be placed at RF terminal of P1, P2 and PC. Please choose appropriate capacitance values to the application frequency.

- [2]To reduce strip line influence on RF characteristics, please locate bypass capacitors(C4) as close as possible to each terminal.
- [3]For good isolation, the GND terminal (2nd pin) must be connected to the ground plane of substrate, and through-holes for GND should be placed near by the pin connection.

### ■ PACKAGE OUTLINE (USB8-B6)



#### Cautions on using this product

- This product contains Gallium-Arsenide (GaAs) which is a harmful material.
- Do NOT eat or put into mouth.
- Do NOT dispose in fire or break up this product.
- Do NOT chemically make gas or powder with this product.
- To waste this product, please obey the relating law of your country.

This product may be damaged with electric static discharge (ESD) or spike voltage. Please handle with care to avoid these damages.

[CAUTION]

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- 8. Quality Warranty
  - 8-1. Quality Warranty Period

In the case of a product purchased through an authorized distributor or directly from us, the warranty period for this product shall be one (1) year after delivery to your company. For defective products that occurred during this period, we will take the quality warranty measures described in section 8-2. However, if there is an agreement on the warranty period in the basic transaction agreement, quality assurance agreement, delivery specifications, etc., it shall be followed.

8-2. Quality Warranty Remedies

When it has been proved defective due to manufacturing factors as a result of defect analysis by us, we will either deliver a substitute for the defective product or refund the purchase price of the defective product.

- Note that such delivery or refund is sole and exclusive remedies to your company for the defective product.
- 8-3. Remedies after Quality Warranty Period

With respect to any defect of this product found after the quality warranty period, the defect will be analyzed by us. On the basis of the defect analysis results, the scope and amounts of damage shall be determined by mutual agreement of both parties. Then we will deal with upper limit in Section 8-2. This provision is not intended to limit any legal rights of your company.

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- 12. Warning for handling Gallium and Arsenic (GaAs) products (Applying to GaAs MMIC, Photo Reflector). These products use Gallium (Ga) and Arsenic (As) which are specified as poisonous chemicals by law. For the prevention of a hazard, do not burn, destroy, or process chemically to make them as gas or power. When the product is disposed of, please follow the related regulation and do not mix this with general industrial waste or household waste.
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