GNSS LOW NOISE AMPLIFIER

■ GENERAL DESCRIPTION

The NJG1150UA2 is a low noise amplifier GaAs MMIC designed for GNSS (Global Navigation Satellite Systems).

The NJG1150UA2 is featured low noise figure and low distortion, and operates from 1.5V to 3.3V single voltage. The NJG1150UA2 has stand-by mode to save the supply current, has the on-chip ESD protection devices.

The NJG1150UA2 achieves very small mounting area by only two external components and very small package that is lead-free and halogen-free 6-pin EPFFP6-A2 package.

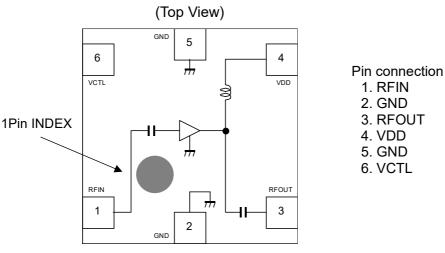
APPLICATIONS

GNSS applications, like GPS, Galileo, GLONASS and COMPASS.

■ FEATURES

- Low supply voltage
- Low current consumption
- High gain
- Low noise figure
- High input IP3
- High out of band input IP3
- Small package
- RoHS compliant and Halogen Free
- MSL1

■ PIN CONFIGURATION



1.8/ 2.8V typ.

■ TRUTH TABLE

"H"=V _{СТL} (H), "L"=V _{СТL} (L)			
VCTL	LNA Mode		
Н	Active mode		
L	Stand-by mode		

Note: Specifications and description listed in this datasheet are subject to change without notice.

Nisshinbo Micro Devices Inc.

Ver.2014-06-30

PACKAGE OUTLINE



NJG1150UA2

4.2 / 4.9mA typ. @V_{DD}=1.8/ 2.8V, V_{CTL}=1.8V 0.1µA typ. $@V_{DD}=1.8/2.8V, V_{CTL}=0V$ (Stand-by mode) 16.0dB typ. @V_{DD}=1.8/ 2.8V, V_{CTL}=1.8V, f=1575MHz 0.6dB typ. @V_{DD}=1.8/ 2.8V, V_{CTL}=1.8V, f=1575MHz -1 / +1dBm typ. @V_{DD}=1.8/ 2.8V, V_{CTL}=1.8V, f=1575+1575.1MHz +5 / +6dBm typ. @V_{DD}=1.8/ 2.8V, V_{CTL}=1.8V, f=1712.7+1850MHz EPFFP6-A2 (Package size: 1.0mmx1.0mmx0.37mm typ.)

■ ABSOLUTE MAXIMUM RATINGS

 T_a =+25°C, Z_s = Z_l =50 Ω

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PARAMETERS	SYMBOL	CONDITIONS	RATINGS	UNITS
Supply voltage	V _{DD}		5.0	V
Control voltage	V _{CTL}		5.0	V
Input power	P _{IN}	V _{DD} =2.8V	+15	dBm
Power dissipation	PD	4-layer FR4 PCB with through-hole (101.5mmx114.5mm), T _j =150°C	590	mW
Operating temperature	T _{opr}		-40~+85	°C
Storage temperature	T _{stg}		-55~+150	°C

■ ELECTRICAL CHARACTERISTICS 1 (DC)

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				General	conditions	$I_a = +25$ °C
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage	V _{DD}		1.5	-	3.3	V
Control Voltage (High)	V _{CTL(H)}		1.5	1.8	3.3	V
Control Voltage (Low)	V _{CTL(L)}		0	0	0.3	V
Supply Current1	I _{DD1}	Active mode, V _{DD} =2.8V, V _{CTL} =1.8V	-	4.9	8.0	mA
Supply Current2	I _{DD2}	Active mode, V _{DD} =1.8V, V _{CTL} =1.8V	-	4.2	7.0	mA
Supply Current3	I _{DD3}	Stand-by mode, V _{DD} =2.8V, V _{CTL} =0V	-	0.1	3.0	μA
Supply Current4	I _{DD4}	Stand-by mode, V _{DD} =1.8V, V _{CTL} =0V	-	0.1	3.0	μA
Control Current	I _{CTL}	V _{CTL} =1.8V	-	5.0	12.0	μA

General conditions: T_a=+25°C

■ ELECTRICAL CHARACTERISTICS 2 (RF)

General conditions: $V_{DD}=2.8V$, $V_{CTL}=1.8V$, $f_{RF}=1555\sim1610$ MHz, $T_a=+25^{\circ}$ C, $Z_s=Z_l=50\Omega$, with application circuit 1						
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Small Signal Gain1	Gain1	Exclude PCB and connector Losses (0.17dB)	14.5	16.0	17.0	dB
Noise Figure1	NF1	Exclude PCB and connector Losses (0.08dB)	-	0.6	0.9	dB
Input Power at 1dB Gain Compression Point1	P- _{1dB} (IN)1		-12.0	-7.0	-	dBm
Input 3rd Order Intercept Point1	IIP3_1	f1=f _{RF,} f2=f1+/- 1MHz, Pin=-30dBm	-1.0	+1.0	-	dBm
Out of Band Input 3rd Order Intercept Point1	IIP3_OB1	$f_1 = 1712.7$ MHz Pin =-20dBm, $f_2 = 1850$ MHz Pin =-65dBm	+2.0	+6.0	-	dBm
700MHz Harmonic1	2fo1	Input jammer tone: 787.76MHz at –25dBm Measure the harmonic tone at 1575.52MHz	-	-45.0	-	dBm
RF IN VSWR1	VSWRi1		-	1.9	2.4	-
RF OUT VSWR1	VSWRo1		-	1.8	2.3	-

ELECTRICAL CHARACTERISTICS 3 (RF)

General conditions: V_{DD}=1.8V, V_{CTL=}1.8V, f_{RF}=1555~1610MHz, T_a=+25°C, Z_s=Z_I=50Ω, with application circuit2

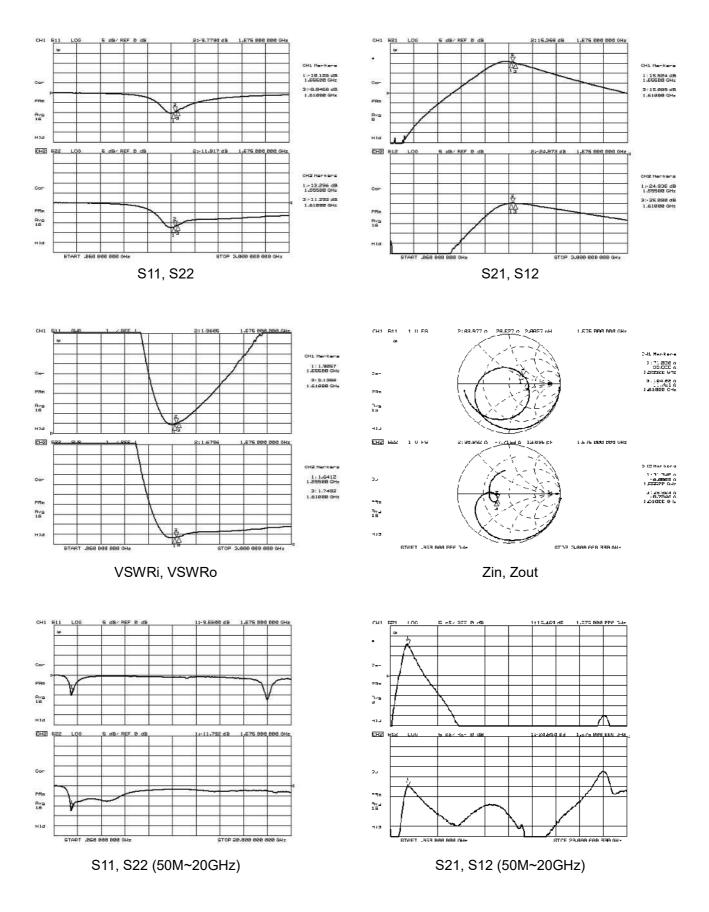
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PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Small Signal Gain2	Gain2	Exclude PCB and connector Losses (0.17dB)	14.0	16.0	17.0	dB
Noise Figure2	NF2	Exclude PCB and connector Losses (0.08dB)	-	0.6	0.95	dB
Input Power at 1dB Gain Compression Point2	P- _{1dB} (IN)2		-14.0	-9.0	-	dBm
Input 3rd Order Intercept Point2	IIP3_2	f1=f _{RF,} f2=f1+/- 1MHz, Pin=-30dBm	-6.0	-1.0	-	dBm
Out of Band Input 3rd Order Intercept Point2	IIP3_OB2	$f_1 = 1712.7$ MHz Pin =-20dBm, $f_2 = 1850$ MHz Pin =-65dBm	-1.0	+5.0	-	dBm
700MHz Harmonic2	2fo2	Input jammer tone: 787.76MHz at –25dBm Measure the harmonic tone at 1575.52MHz	-	-45.0	-	dBm
RF IN VSWR2	VSWRi2		-	1.9	2.6	-
RF OUT VSWR2	VSWRo2		-	1.8	2.5	-

■ TERMINAL INFORMATION

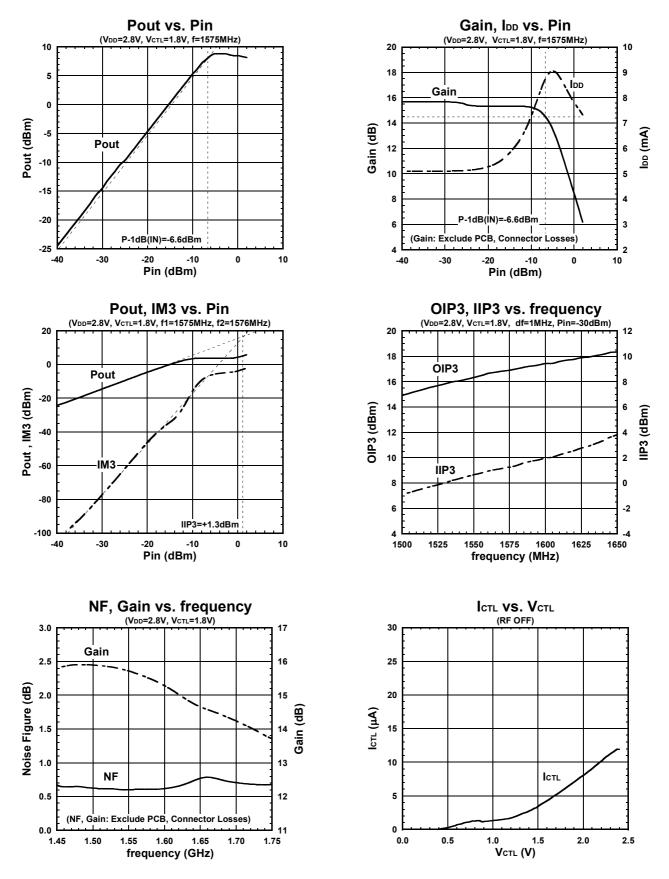
No.	SYMBOL	DESCRIPTION
1	RFIN	RF input terminal. This terminal requires only a matching inductor L1, and does not require DC blocking capacitor.
2	GND	Ground terminal. This terminal should be connected to the ground plane as close as possible for excellent RF performance.
3	RFOUT	RF output terminal. This terminal requires no DC blocking capacitor since this IC has internal output matching circuit including DC blocking capacitor.
4	VDD	Supply voltage terminal. Please connect bypass capacitor C1 with ground as close as possible.
5	GND	Ground terminal. This terminal should be connected to the ground plane as close as possible for excellent RF performance.
6	VCTL	Control voltage terminal.

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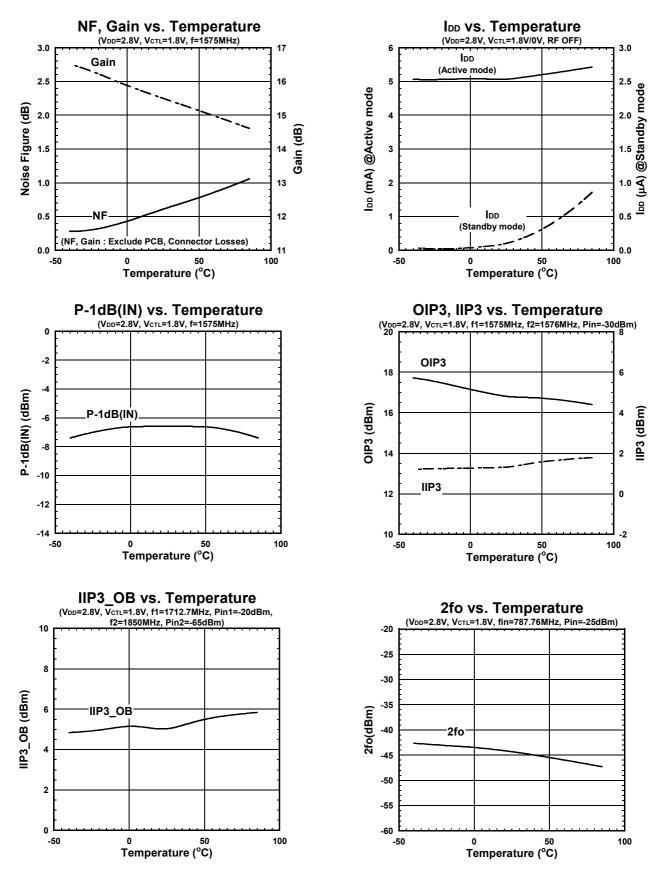
Conditions: V_{DD} =2.8V, V_{CTL} =1.8V, Ta=25°C, Z_s = Z_l =50 Ω , with application circuit1



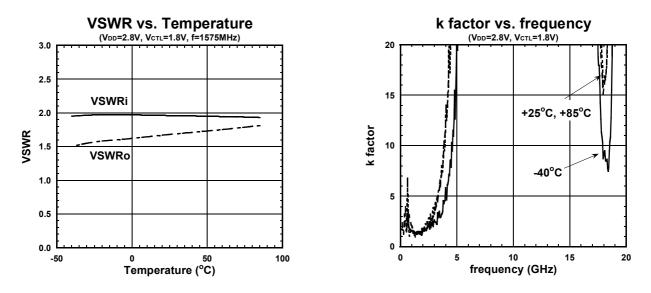
Conditions: V_{DD} =2.8V, V_{CTL} =1.8V, Ta=25°C, Z_s = Z_l =50 Ω , with application circuit1



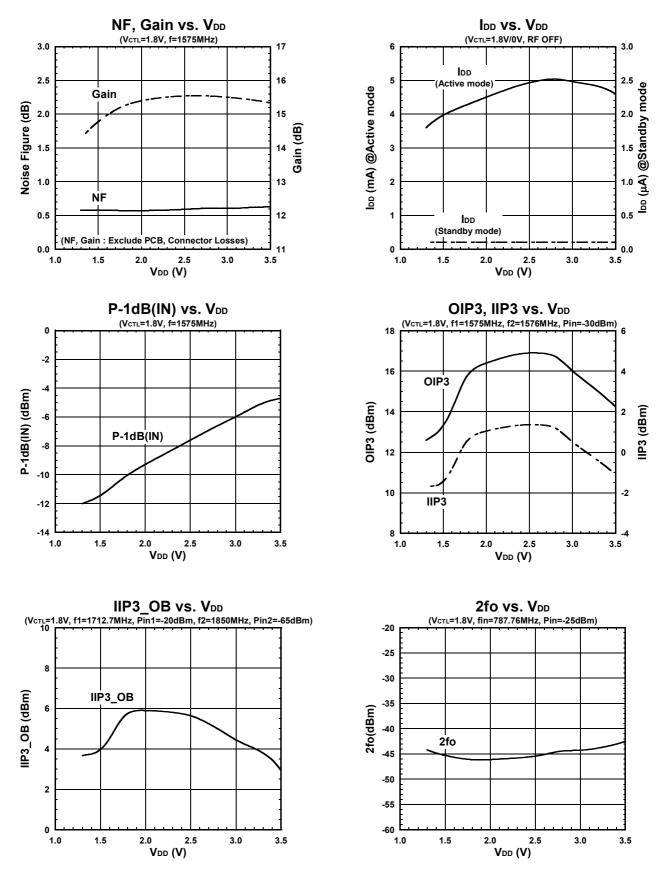
Conditions: V_{DD} =2.8V, V_{CTL} =1.8V, Z_s = Z_l =50 Ω , with application circuit1



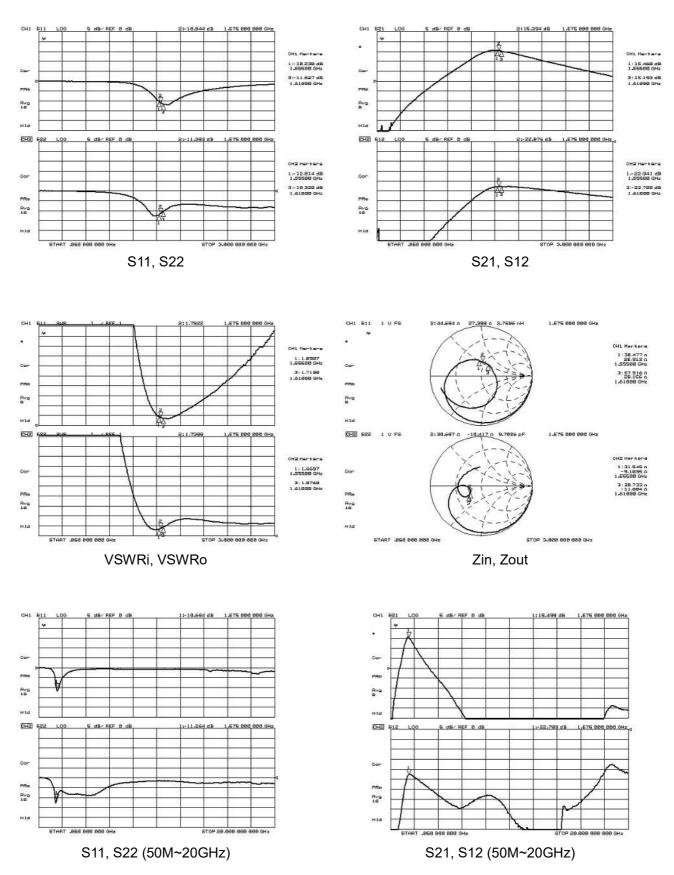
Conditions: V_{DD} =2.8V, V_{CTL} =1.8V, Z_s = Z_l =50 Ω , with application circuit1



Conditions: V_{CTL} =1.8V, Ta=25°C, Z_s = Z_l =50 Ω , with application circuit1



Conditions: V_{DD} =1.8V, V_{CTL} =1.8V, Ta=25°C, Z_s = Z_l =50 Ω , with application circuit2



-2

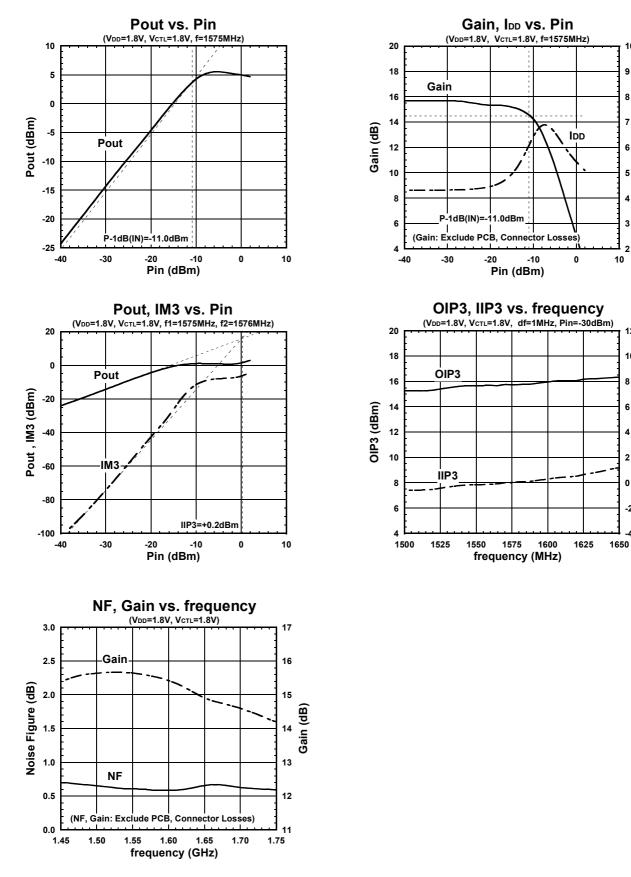
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IIP3 (dBm)

IbD (mA)

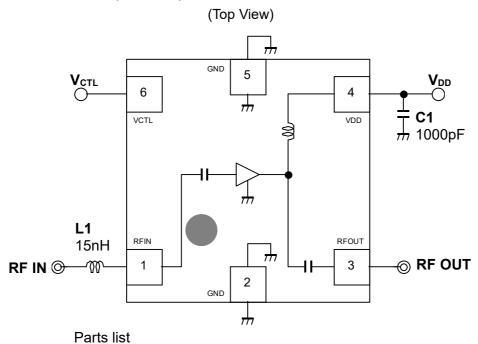
ELECTRICAL CHARACTERISTICS

Conditions: V_{DD} =1.8V, V_{CTL} =1.8V, Ta=25°C, Z_s = Z_l =50 Ω , with application circuit2



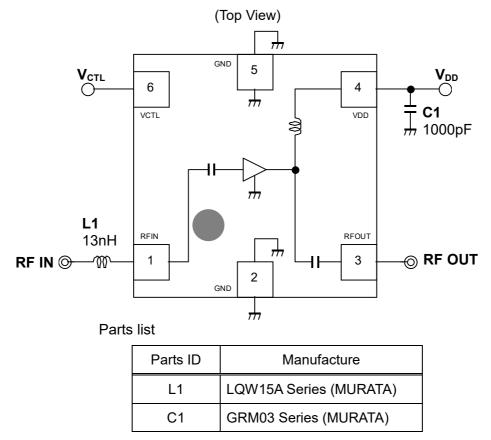
NJG1150UA2

■ APPLICATION CIRCUIT1 (V_{DD}=2.8V)

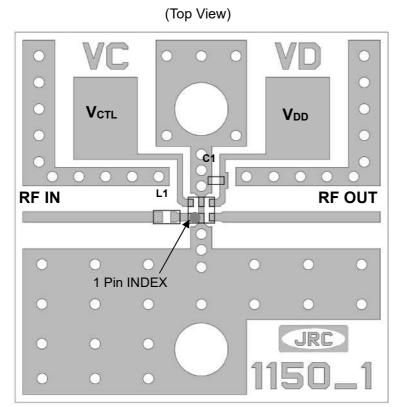


Parts ID	Manufacture
L1	LQW15A Series (MURATA)
C1	GRM03 Series (MURATA)

■ APPLICATION CIRCUIT2 (V_{DD}=1.8V)

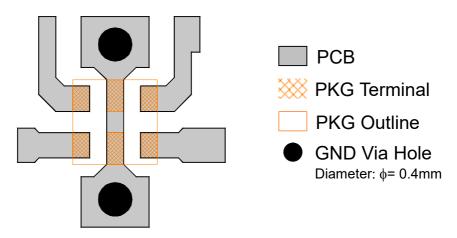


EVALUATION BOARD



PCB (FR-4): t=0.2mm MICROSTRIP LINE WIDTH =0.4mm (Z_0 =50 Ω) PCB SIZE =14.0mm x 14.0mm

<PCB LAYOUT GUIDELINE>



PRECAUTIONS

- Please layout ground pattern under this IC in order not to couple with terminal RFIN and RFOUT.
- All external parts should be placed as close as possible to the IC.
- For good RF performance, all GND terminals must be connected to PCB ground plane of substrate, and via-holes for GND should be placed near the IC.

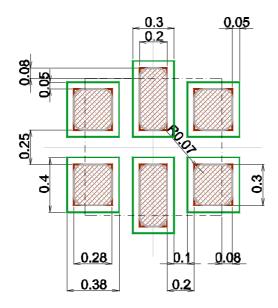
■ RECOMMENDED FOOTPRINT PATTERN (EPFFP6-A2 PACKAGE Reference)

: Land

- Package: 1.0 x 1.0mm
- : Mask (Open area) *Metal mask thickness: 100um

: Resist (Open area)

Pin pitch: 0.4mm



APPLICATION NOTE FOR SMALLER MOUNTING AREA (using MLG0603P Series inductor)

This application note shows an example in order to achieve smaller mounting area using smaller size external inductor L1. MLG0603P (TDK-EPC) Series inductor is used for this application. The example of electrical characteristics are shown as follows:

ELECTRICAL CHARACTERISTICS 4 (DC)

		()	General conditions	: T _a =+25°C
PARAMETER	SYMBOL	CONDITIONS	MEASURED DATA	UNITS
Supply Voltage	V _{DD}		2.8 / 1.8	V
Control Voltage (High)	V _{CTL(H)}		1.8	V
Control Voltage (Low)	V _{CTL(L)}		0	V
Supply Current1	I _{DD1}	Active mode, V _{DD} =2.8V, V _{CTL} =1.8V	5.04	mA
Supply Current2	I _{DD2}	Active mode, V _{DD} =1.8V, V _{CTL} =1.8V	4.33	mA
Supply Current3	I _{DD3}	Stand-by mode, V _{DD} =2.8V, V _{CTL} =0V	0.1	μA
Supply Current4	I _{DD4}	Stand-by mode, V _{DD} =1.8V, V _{CTL} =0V	0.1	μA
Control Current	I _{CTL}	V _{CTL} =1.8V	6.0	μA

ELECTRICAL CHARACTERISTICS 5 (RF)

General conditions: V_{DD} =2.8V, $V_{CTL=}$ 1.8V, f_{RF} =1555~1610MHz, T_a =+25°C, Z_s = Z_I =50 Ω , with application circuit3

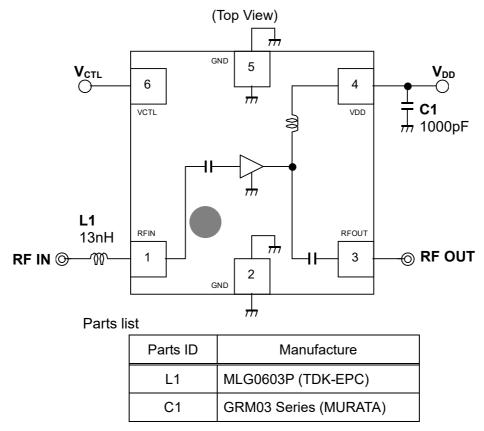
PARAMETER	SYMBOL	CONDITIONS	MEASURED DATA	UNITS
Small Signal Gain5	Gain5	Exclude PCB and connector Losses	15.3~15.7	dB
Noise Figure5	NF5	Exclude PCB and connector Losses	0.80~0.84	dB
Input Power at 1dB Gain Compression Point5	P-1dB (IN)5		-8.6~-6.6	dBm
Input 3rd Order Intercept Point5	IIP3_5	f1=f _{RF} , f2=f1+/- 1MHz, Pin=-30dBm	+0.2~+1.5	dBm
Out of Band Input 3rd Order Intercept Point5	IIP3_OB5	f ₁ =1712.7MHz Pin =-20dBm, f ₂ =1850MHz Pin =-65dBm	+4.3	dBm
700MHz Harmonic5	2fo5	Input jammer tone: 787.76MHz at –25dBm Measure the harmonic tone at 1575.52MHz	-44.8	dBm
RF IN VSWR5	VSWRi5		1.54~1.60	-
RF OUT VSWR5	VSWRo5		1.54~1.69	-

■ ELECTRICAL CHARACTERISTICS 6 (RF)

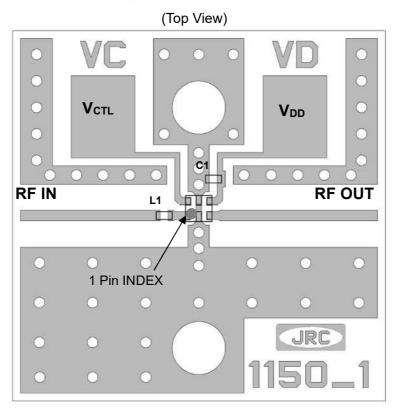
General conditions: V_{DD} =1.8V, V_{CTL} =1.8V, f_{RF} =1555~1610MHz, T_a =+25°C, Z_s = Z_I =50 Ω , with application circuit3

PARAMETER	SYMBOL	CONDITIONS	MEASURED DATA	UNITS
Small Signal Gain6	Gain6	Exclude PCB and connector Losses	14.9~15.4	dB
Noise Figure6	NF6	Exclude PCB and connector Losses	0.77~0.83	dB
Input Power at 1dB Gain Compression Point6	P- _{1dB} (IN)6		-10.5 ~ -10.0	dBm
Input 3rd Order Intercept Point6	IIP3_6	f1=f _{RF,} f2=f1+/- 1MHz, Pin=-30dBm	+0.5 ~ +1.2	dBm
Out of Band Input 3rd Order Intercept Point6	IIP3_OB6	$f_1 = 1712.7$ MHz Pin =-20dBm, $f_2 = 1850$ MHz Pin =-65dBm	+5.8	dBm
700MHz Harmonic6	2fo6	Input jammer tone: 787.76MHz at –25dBm Measure the harmonic tone at 1575.52MHz	-45.5	dBm
RF IN VSWR6	VSWRi6		1.67~1.78	-
RF OUT VSWR6	VSWRo6		1.87~2.11	-

■ APPLICATION CIRCUIT3 (Using MLG0603P Series inductor)

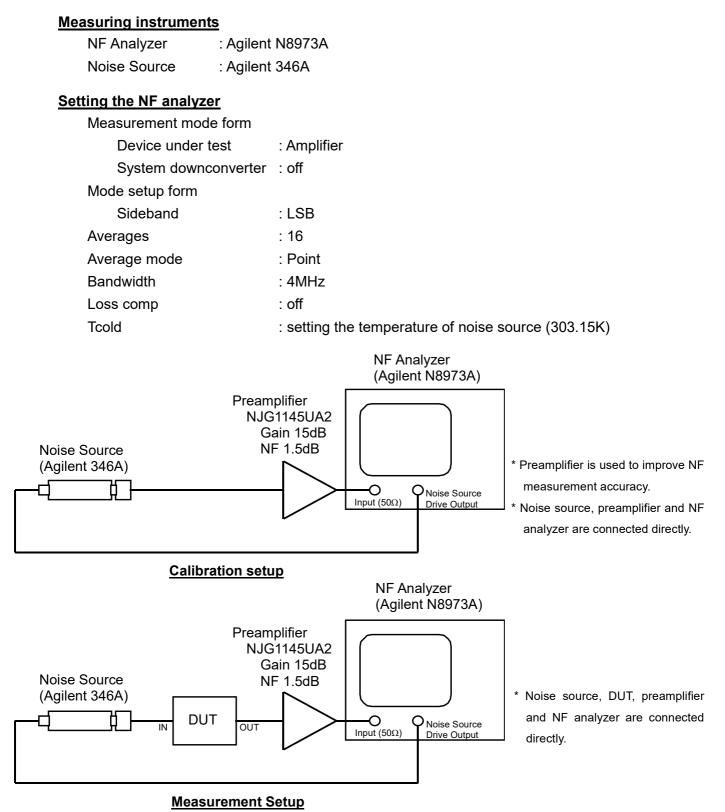


■ EVALUATION BOARD (Using MLG0603P Series)

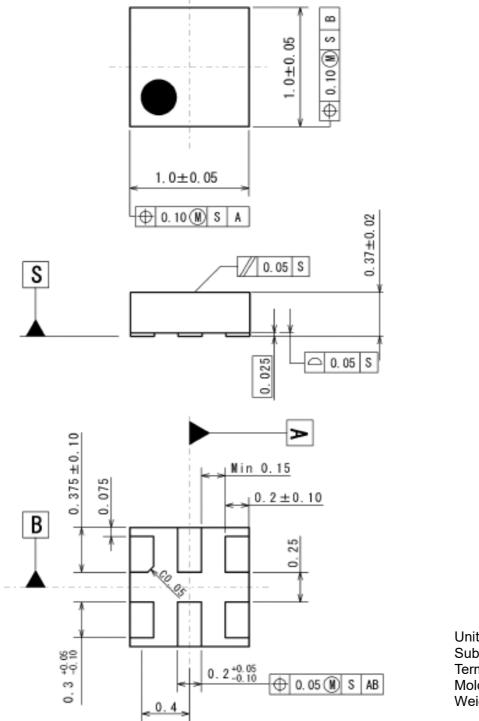


PCB (FR-4): t=0.2mm MICROSTRIP LINE WIDTH =0.4mm (Z_0 =50 Ω) PCB SIZE =14.0mm x 14.0mm

■ NOISE FIGURE MEASUREMENT BLOCK DIAGRAM



■ PACKAGE OUTLINE (EPFFP6-A2)



: mm
: FR4
: Au
: Epoxy resin
: 0.855mg

Cautions on using this product

This product contains Gallium-Arsenide (GaAs) which is a harmful material.

- Do NOT eat or put into mouth.
- Do NOT dispose in fire or break up this product.
- Do NOT chemically make gas or powder with this product.
- To waste this product, please obey the relating law of your country.

This product may be damaged with electric static discharge (ESD) or spike voltage. Please handle with care to avoid these damages.

[CAUTION] The specifications on this databook are only given for information, without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or the product and not intended for the guarantee or permission of any right including the industrial rights.

- 1. The products and the product specifications described in this document are subject to change or discontinuation of production without notice for reasons such as improvement. Therefore, before deciding to use the products, please refer to our sales representatives for the latest information thereon.
- 2. The materials in this document may not be copied or otherwise reproduced in whole or in part without the prior written consent of us.
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 - Aerospace Equipment
 - Equipment Used in the Deep Sea
 - Power Generator Control Equipment (nuclear, steam, hydraulic, etc.)
 - Life Maintenance Medical Equipment
 - Fire Alarms / Intruder Detectors
 - Vehicle Control Equipment (automotive, airplane, railroad, ship, etc.)
 - Various Safety Devices
 - Traffic control system
 - Combustion equipment

In case your company desires to use this product for any applications other than general electronic equipment mentioned above, make sure to contact our company in advance. Note that the important requirements mentioned in this section are not applicable to cases where operation requirements such as application conditions are confirmed by our company in writing after consultation with your company.

- 6. We are making our continuous effort to improve the quality and reliability of our products, but semiconductor products are likely to fail with certain probability. In order to prevent any injury to persons or damages to property resulting from such failure, customers should be careful enough to incorporate safety measures in their design, such as redundancy feature, fire containment feature and fail-safe feature. We do not assume any liability or responsibility for any loss or damage arising from misuse or inappropriate use of the products.
- 7. The products have been designed and tested to function within controlled environmental conditions. Do not use products under conditions that deviate from methods or applications specified in this datasheet. Failure to employ the products in the proper applications can lead to deterioration, destruction or failure of the products. We shall not be responsible for any bodily injury, fires or accident, property damage or any consequential damages resulting from misuse or misapplication of the products.
- 8. Quality Warranty
 - 8-1. Quality Warranty Period

In the case of a product purchased through an authorized distributor or directly from us, the warranty period for this product shall be one (1) year after delivery to your company. For defective products that occurred during this period, we will take the quality warranty measures described in section 8-2. However, if there is an agreement on the warranty period in the basic transaction agreement, quality assurance agreement, delivery specifications, etc., it shall be followed.

8-2. Quality Warranty Remedies

When it has been proved defective due to manufacturing factors as a result of defect analysis by us, we will either deliver a substitute for the defective product or refund the purchase price of the defective product.

- Note that such delivery or refund is sole and exclusive remedies to your company for the defective product.
- 8-3. Remedies after Quality Warranty Period

With respect to any defect of this product found after the quality warranty period, the defect will be analyzed by us. On the basis of the defect analysis results, the scope and amounts of damage shall be determined by mutual agreement of both parties. Then we will deal with upper limit in Section 8-2. This provision is not intended to limit any legal rights of your company.

- 9. Anti-radiation design is not implemented in the products described in this document.
- 10. The X-ray exposure can influence functions and characteristics of the products. Confirm the product functions and characteristics in the evaluation stage.
- 11. WLCSP products should be used in light shielded environments. The light exposure can influence functions and characteristics of the products under operation or storage.
- 12. Warning for handling Gallium and Arsenic (GaAs) products (Applying to GaAs MMIC, Photo Reflector). These products use Gallium (Ga) and Arsenic (As) which are specified as poisonous chemicals by law. For the prevention of a hazard, do not burn, destroy, or process chemically to make them as gas or power. When the product is disposed of, please follow the related regulation and do not mix this with general industrial waste or household waste.
- 13. Please contact our sales representatives should you have any questions or comments concerning the products or the technical information.



Nisshinbo Micro Devices Inc.

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