



#### **General Description**

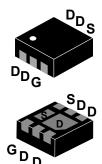
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

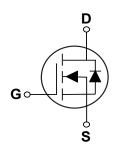
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
30 V	19 mΩ	8 A

#### **Features**

- $R_{DS(ON)} \le 19 m\Omega @V_{GS} = 10V$
- · Improved dv/dt capability
- · Fast switching
- · Green Device Available

#### DFN2x2-6L Pin Configuration





### **Applications**

- · MB / VGA / Vcore
- · POL Applications
- · SMPS 2<sup>nd</sup> SR

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	±20	V
ı	Drain Current - Continuous (T <sub>A</sub> =25°C)	8	Α
I <sub>D</sub>	Drain Current - Continuous (T <sub>A</sub> =70°C)	6.4	Α
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	32	Α
$P_D$	Power Dissipation (T <sub>A</sub> =25°C)	2	W
г	Power Dissipation - Derate above 25°C	16.1	mW/°0
$T_J$	Operating Junction Temperature Range	-55 to 150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C

Thermal Characteristics						
Symbol	Parameter	Тур.	Max.	Unit		
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62	°C/W		





### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	30			V
-	Drain-Source Leakage Current	$V_{DS}$ =30V , $V_{GS}$ =0V , $T_J$ =25°C			1	uA
I <sub>DSS</sub>	Dialii-Source Leakage Guirein	$V_{DS}$ =24V , $V_{GS}$ =0V , $T_{J}$ =125°C			10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS}$ =10V , $I_D$ =4A			19	mΩ
	Static Diam-Source On-Nesistance	$V_{GS}$ =4.5V , $I_D$ =3A			27	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.2	1.6	2.5	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>S</sub> =2A		4		S

#### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =10V , I <sub>D</sub> =4A		5.2		
$Q_gs$	Gate-Source Charge	(NOTE 2 \ 3)		0.6		nC
$Q_{gd}$	Gate-Drain Charge	(NOTE 2 × 3)		2		
$T_{d(on)}$	Turn-On Delay Time			2.8		
T <sub>r</sub>	Rise Time	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$ ,		7.2		ns
$T_{d(off)}$	Turn-Off Delay Time	I <sub>D</sub> =4A (NOTE 2 \ 3)		15.8		115
$T_f$	Fall Time			4.6		
C <sub>iss</sub>	Input Capacitance			490		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , F=1MHz		80		pF
$C_{rss}$	Reverse Transfer Capacitance			55		
$R_g$	Gate resistance	$V_{GS}$ =0V , $V_{DS}$ =0V , F=1MHz		2.2		Ω

#### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			8	Α
I <sub>SM</sub>	Pulsed Source Current	v <sub>G</sub> -v <sub>D</sub> -ov , i orde duitent			16	Α
$V_{SD}$	Diode Forward Voltage	$V_{GS}$ =0V , $I_{S}$ =1A , $T_{J}$ =25 $^{\circ}$ C			1	V
t <sub>rr</sub>	Reverse Recovery Time	$V_R$ =30V , $I_S$ =8A , di/dt=100A/us		130		ns
$Q_{rr}$	Reverse Recovery Charge	, T <sub>J</sub> =25°C		200		nC

#### NOTES:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 3. Essentially independent of operating temperature.





#### **Characteristics Curves**

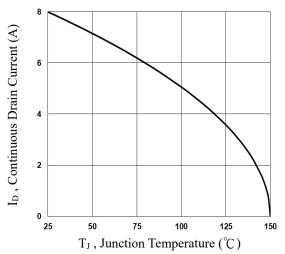


Fig.1 Continuous Drain Current vs. TJ

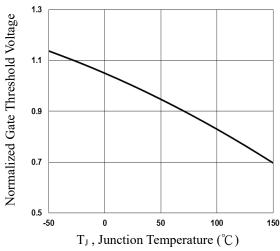


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

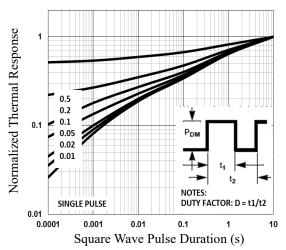


Fig.5 Normalized Transient Response

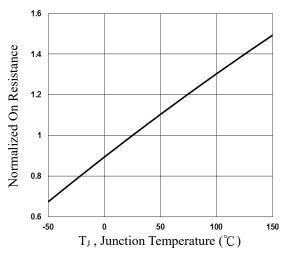


Fig.2 Normalized RDSON vs. T<sub>J</sub>

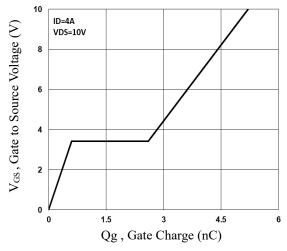


Fig.4 Gate Charge Waveform

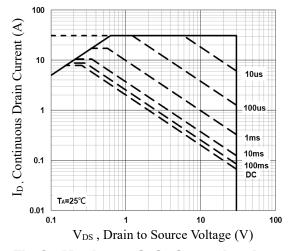


Fig.6 Maximum Safe Operation Area





## **Characteristics Curves**

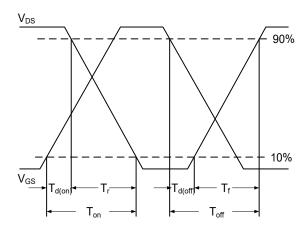
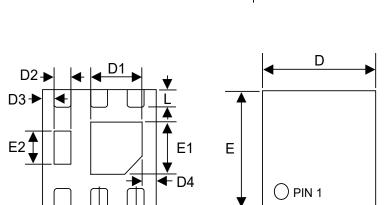


Fig.7 Switching Time Waveform

## **Package Outline Dimensions**



A2

	Dimens	sions in	Dimensions in		
Symbol	m	m	inc	hes	
	Min.	Max.	Min.	Max.	
Α	0.50	0.80	0.019	0.032	
A2	0.152	REF	0.006	REF	
b	0.25	0.35	0.009	0.014	
D	1.90	2.10	0.074	0.083	
D1	0.80	1.00	0.031	0.040	
D2	0.25	0.35	0.009	0.014	
D3	0.20	BSC	0.008	BSC	
D4	0.25 BSC		0.010	BSC	
Е	1.90	2.10	0.074	0.083	
E1	0.80	1.10	0.031	0.044	
E2	0.46	0.66	0.018	0.260	
е	0.65 BSC		0.026 BSC		
L	0.25	0.35	0.009	0.014	

## DFN2x2-6L





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