



# N3MNB5P8



## 20V Dual N-Channel MOSFETs

### General Description

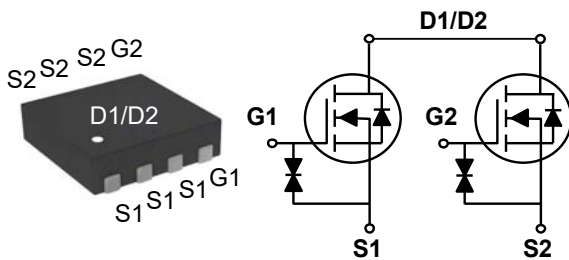
The N3MNB5P8 is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the small power switching and load switch applications.

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$
20 V	5.8 m $\Omega$	56 A

### Features

- $R_{DS(ON)} \leq 5.8m\Omega @ V_{GS}=4.5V$
- Super Low Gate Charge
- Green Device Available
- Excellent CdV/dt effect decline

DFN3x3 Dual Pin Configuration



### Applications

- Handheld Instruments
- POL Applications
- Battery Protection Applications

### Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 8$	V
$I_D$	Drain Current - Continuous ( $T_C=25^\circ\text{C}$ )	56	A
	Drain Current - Continuous ( $T_C=100^\circ\text{C}$ )	35.6	A
$I_{DM}$	Drain Current - Pulsed (NOTE 1)	100	A
$P_D$	Power Dissipation ( $T_C=25^\circ\text{C}$ )	31	W
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
Marking Code		NB5P8 , A2030	

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	35	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	4	$^\circ\text{C}/\text{W}$



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### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	20	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =16V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =16V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V	---	---	±10	uA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance (NOTE 1)	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	---	4.3	5.8	mΩ
		V <sub>GS</sub> =3.9V, I <sub>D</sub> =3A	---	4.5	6.5	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3A	---	5	7	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =3A	---	7	11	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	0.4	---	1.0	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =3A	---	42	---	S

#### Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q <sub>g</sub>	Total Gate Charge (4.5V)	V <sub>DS</sub> =10V, I <sub>D</sub> =3A	---	38	---	nC
	Total Gate Charge (3.9V)		---	33	---	
Q <sub>gs</sub>	Gate-Source Charge		---	4.5	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	12	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =16V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A, R <sub>G</sub> =6Ω	---	22	---	nS
T <sub>r</sub>	Rise Time		---	41	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	77	---	
T <sub>f</sub>	Fall Time		---	21	---	
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, F=1MHz	---	3165	---	pF
C <sub>OSS</sub>	Output Capacitance		---	380	---	
C <sub>RSS</sub>	Reverse Transfer Capacitance		---	325	---	

#### Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	30	A
I <sub>SM</sub>	Pulsed Source Current (NOTE 1)		---	---	100	A
V <sub>SD</sub>	Diode Forward Voltage (NOTE 1)	V <sub>GS</sub> =0V, I <sub>S</sub> =3A, T <sub>J</sub> =25°C	---	---	1.2	V

#### NOTES :

1. The data tested by pulsed, pulse width ≤ 10us, duty cycle ≤ 1%.



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## Characteristics Curves

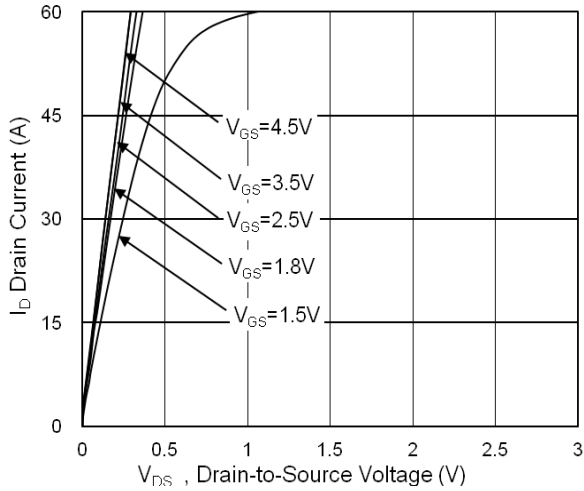


Fig.1 Typical Output Characteristics

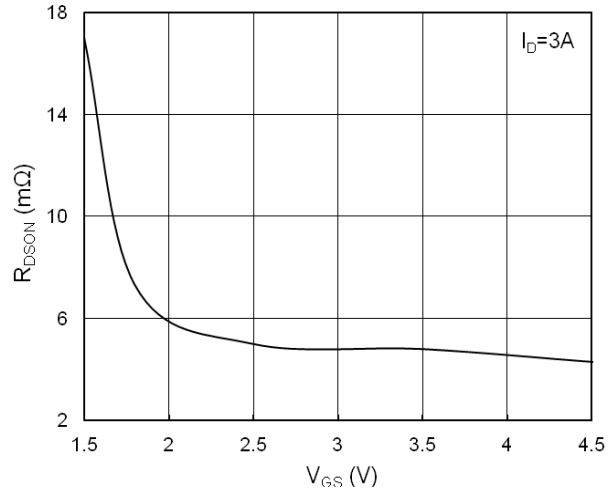


Fig.2 On-Resistance vs. Gate-Source

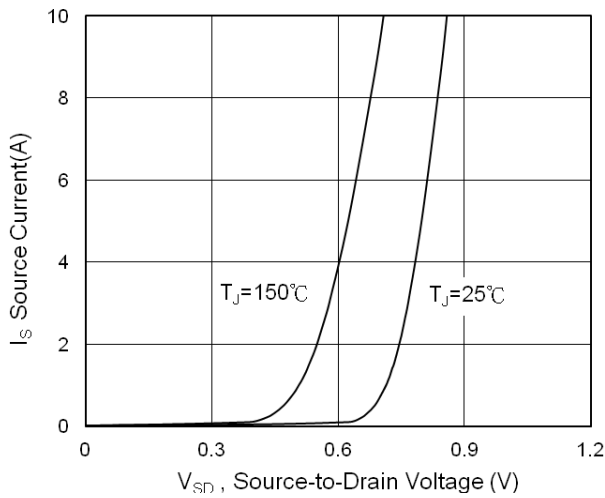


Fig.3 Forward Characteristics

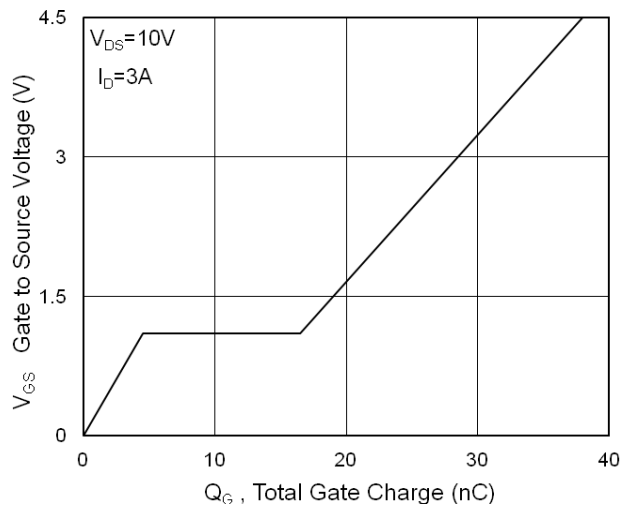


Fig.4 Gate-Charge Characteristics

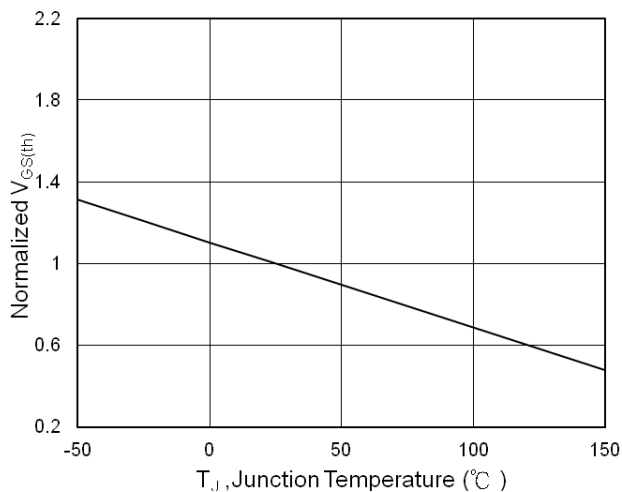


Fig.5  $V_{GS(th)}$  vs.  $T_J$

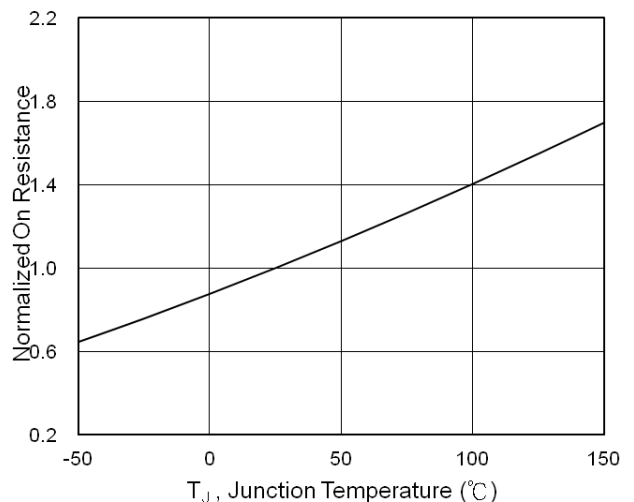


Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$



Characteristics Curves

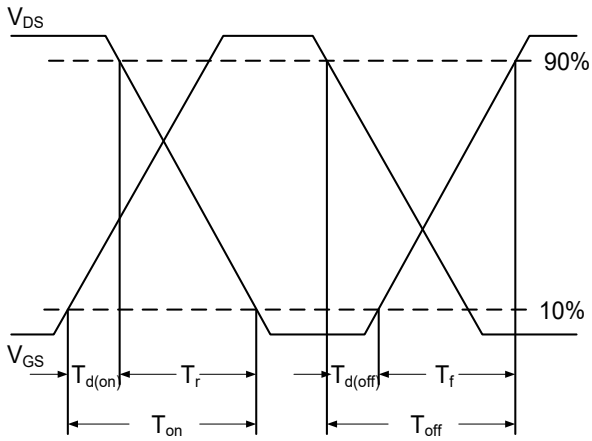


Fig. 7 Switching Time Waveform

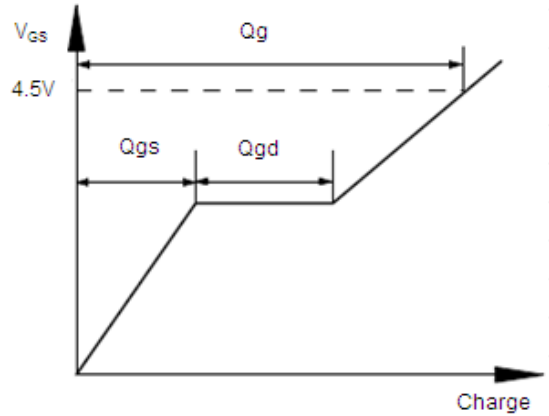
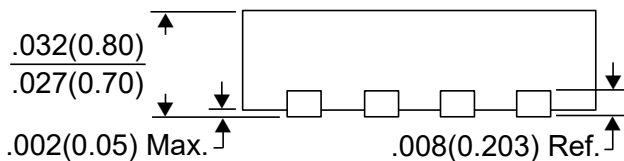
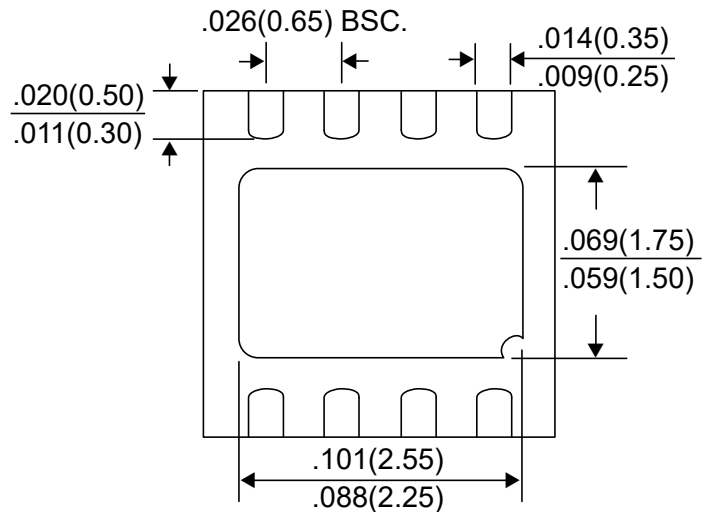
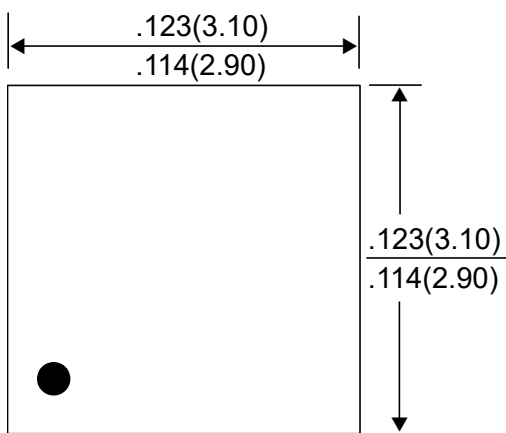


Fig. 8 Gate Charge Waveform

Package Outline Dimensions



DFN3x3

Dimensions in inches and (millimeters)



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