

DATASHEET

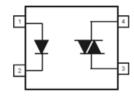
4 PIN DIP RANDOM-PHASE TRIAC DRIVER PHOTOCOUPLER ELT302X, ELT305X Series







<u>Schematic</u>



Features:

- Compliance Halogens Free (Br < 900 ppm, Cl < 900 ppm, Br+Cl < 1500 ppm)
- Peak breakdown voltage
- 400V: ELT302X
- 600V: ELT305X
- High isolation voltage between input and output (Viso=5000 V rms)
- Compact dual-in-line package
- •The product itself will remain within RoHS compliant version
- Compliance with EU REACH
- UL and cUL approved(No. E214129)
- VDE approved (No. 40028391)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved
- CQC approved

Pin Configuration

- 1. Anode
- 2. Cathode
- 3. Terminal
- 4. Terminal

Description

The ELT302X and ELT305X series of devices each consist of a GaAs infrared emitting diode optically coupled to a monolithic silicon random phase photo Triac.

They are designed for interfacing between electronic controls and power triacs to control resistive and inductive loads for 115 to 240 VAC operations.

Applications

- Solenoid/valve controls
- Lamp ballasts
- Static AC power switch
- Interfacing microprocessors to 115 to 240Vac peripherals
- Incandescent lamp dimmers
- Temperature controls
- Motor controls



Absolute Maximum Ratings (Ta=25℃)

	Parameter		Symbol	Rating	Unit
Input	Forward current		I _F	60	mA
	Reverse voltage		V_{R}	6	V
Power dissipation		D	100	mW	
	Derating factor (above	$T_a = 85^{\circ}C$	P _D -	3.8	mW /°C
Output	Off-state Output Terminal Voltage	ELT302X	\ /	400	
		ELT305X	– V _{DRM} –	600	V
	Peak Repetitive Surge	Current	Ітѕм	1	А
	Power dissipation		Б	300	mW
Derating factor (above T _a = 85°C)			P _C -	7.4	mW/°C
Total power dissipation			Ртот	330	mW
Isolation voltage *1			V _{ISO}	5000	Vrms
Operatin	g temperature		T _{OPR}	-55 to 100	$^{\circ}$ C
Storage	temperature		T _{STG}	-55 to 125	°C
Soldering Temperature*2			T _{SOL}	260	°C

Notes:

^{*1} AC for 1 minute, R.H.= $40 \sim 60\%$ R.H. In this test, pins 1 & 2 are shorted together, and pins 3 & 4 are shorted together.

^{*2} For 10 seconds



Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input

Parameter	Symbol	Min.	Тур.*	Max.	Unit	Condition
Forward Voltage	VF	-	1.18	1.5	V	I _F = 10mA
Reverse Leakage current	I_R	-	-	10	μΑ	$V_R = 6V$

Output

Parameter		Symbol	Min.	Тур.*	Max.	Unit	Condition
Peak Blocking Current		I _{DRM}	-	-	100	nA	V_{DRM} = Rated V_{DRM} I _F = 0mA
Peak On-state Voltage		V_{TM}	-	-	2.5	V	I _{TM} =100mA peak, I _F =Rated I _{FT}
Critical Rate of Rise off-state Voltage	ELT302X	-l. //-l#	-	100	-	V/µs	V _{PEAK} =Rated V _{DRM} , I _F =0 (Fig. 8)
	ELT305X	dv/dt -	1000	-	-		V _{PEAK} =400V, I _F =0 (Fig. 8)

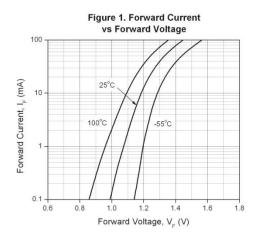
Transfer Characteristics

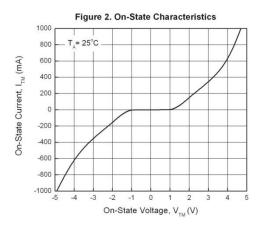
Parameter		Symbol	Min.	Typ.*	Max.	Unit	Condition
	ELT3021 ELT3051		15		15		
LED Trigger Current	ELT3022 ELT3052	l _{FT}	-	-	10	mA	Main terminal Voltage=3V
	ELT3023 ELT3053		-	-	5		
Holding Current		lΗ	-	250	-	μΑ	

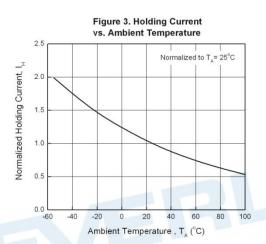
^{*} Typical values at T_a = 25°C

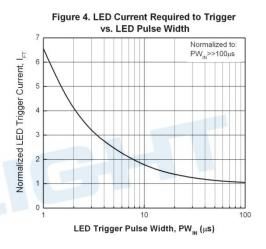


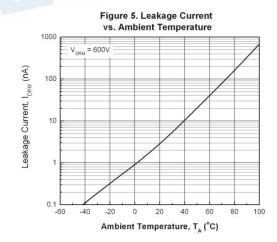
Typical Electro-Optical Characteristics Curves

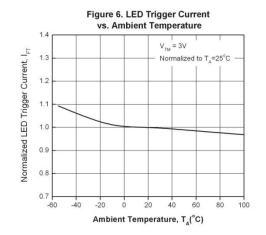














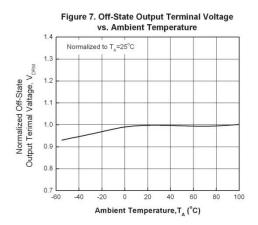
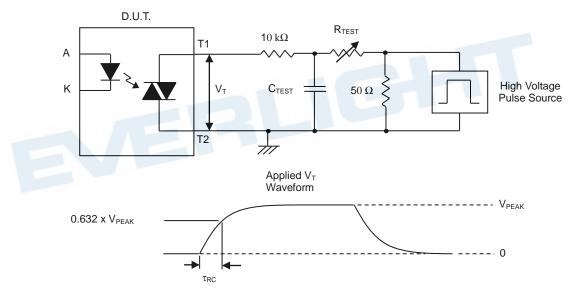


Figure 8. Static dv/dt Test Circuit & Waveform



Measurement Method

The high voltage pulse is set to the required V_{PEAK} value and applied to the D.U.T. output side through the RC circuit above. LED current is not applied. The waveform V_T is monitored using a x100 scope probe. By varying R_{TEST} , the dv/dt (slope) is increased, until the D.U.T. is observed to trigger (waveform collapses). The dv/dt is then decreased until the D.U.T. stops triggering. At this point, τ_{RC} is recorded and the dv/dt calculated.

$$dv/dt = \frac{0.632 \times V_{PEAK}}{\tau_{RC}}$$

For example, V_{PEAK} = 400V for EL302X series. The dv/dt value is calculated as follows:

$$dv/dt = \frac{0.63 \times 400}{\tau_{RC}} = \frac{252}{\tau_{RC}}$$



Order Information

Part Number

ELT302XY(Z)-V ELT305XY(Z)-V

Note

X = Part No. (1, 2 or 3)

Y = Lead form option (S1, M or none)

Z = Tape and reel option (TU, TD or none).

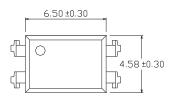
V = VDE safety approved (optional)

Option	Description	Packing quantity
None	Standard DIP-4	100 units per tube
М	Wide lead bend (0.4 inch spacing)	100 units per tube
S1 (TU)	Surface mount lead form (low profile) + TU tape & reel option	1500 units per reel
S1 (TD)	Surface mount lead form (low profile) + TD tape & reel option	1500 units per reel

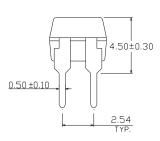


Package Dimension (Dimensions in mm)

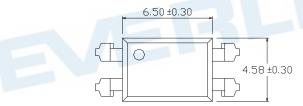
Standard DIP Type

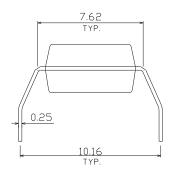


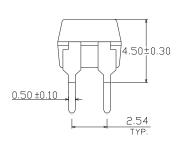




Option M Type

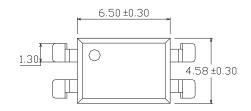


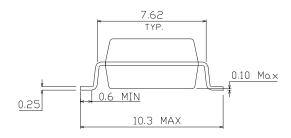


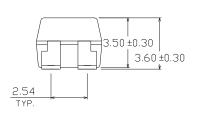




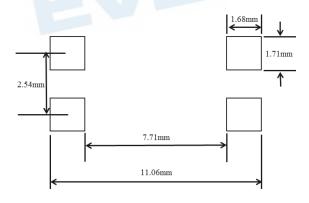
Option S1 Type







Recommended pad layout for surface mount leadform





Device Marking



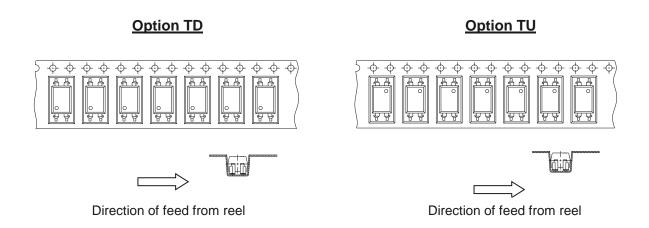
Notes

EL denotes Everlight
T3053 denotes Device Number
Y denotes 1 digit Year code
WW denotes 2 digit Week code
V denotes VDE option

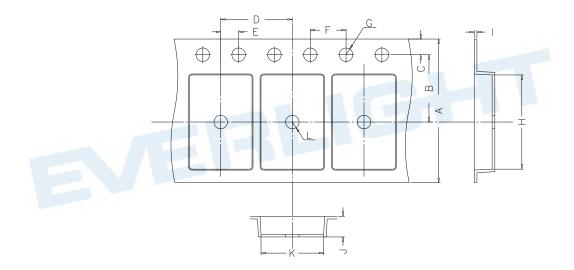




Tape & Reel Packing Specifications



Tape dimensions



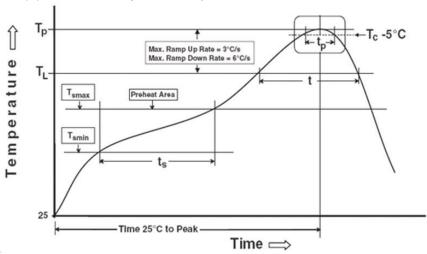
Dimension No.	Α	В	С	D	E	F
Dimension(mm)	16.00±0.3	7.5±0.1	1.75±0.1	8.0±0.1	2.0±0.1	4.0±0.1
Dimension No.	G	н	I	J	К	L



Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

Preheat

Temperature min (T_{smin})

Temperature max (T_{smax})

Time $(T_{smin} \text{ to } T_{smax})$ (t_s)

Average ramp-up rate (T_{smax} to T_p)

Other

Liquidus Temperature (T_L)

Time above Liquidus Temperature (t L)

Peak Temperature (T_P)

Time within 5 °C of Actual Peak Temperature: TP - 5°C

Ramp- Down Rate from Peak Temperature

Time 25°C to peak temperature

Reflow times

Reference: IPC/JEDEC J-STD-020D

150 °C

200°C

60-120 seconds

3 °C/second max

217 °C

60-100 sec

260°C

30 s

6°C /second max.

8 minutes max.

3 times

DATASHEET 4PIN DIP RANDOM-PHASE TRIAC DRIVER PHOTO COUPLER ELT302X, EL305X Series



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