



### General Description

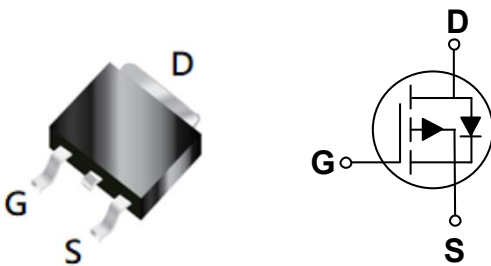
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$
-40 V	5.8 m $\Omega$	-90 A

### Features

- $R_{DS(ON)} \leq 5.8m\Omega @ V_{GS} = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

TO-252 Pin Configuration



### Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

### Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current - Continuous ( $T_C=25^\circ\text{C}$ )	-90	A
	Drain Current - Continuous ( $T_C=100^\circ\text{C}$ )	-57	A
$I_{DM}$	Drain Current - Pulsed (NOTE 1)	-360	A
EAS	Single Pulse Avalanche Energy (NOTE 2)	174	mJ
IAS	Single Pulse Avalanche Current (NOTE 2)	-59	A
$P_D$	Power Dissipation ( $T_C=25^\circ\text{C}$ )	101	W
	Power Dissipation - Derate above $25^\circ\text{C}$	0.81	W/ $^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.23	$^\circ\text{C}/\text{W}$



### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V , I <sub>D</sub> = -250uA	-40	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = -40V , V <sub>GS</sub> = 0V , T <sub>J</sub> =25°C	---	---	-1	uA
		V <sub>DS</sub> = -32V , V <sub>GS</sub> = 0V , T <sub>J</sub> =125°C	---	---	-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V , V <sub>DS</sub> = 0V	---	---	±100	nA

#### On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = -10V , I <sub>D</sub> = -25A	---	---	5.8	mΩ
		V <sub>GS</sub> = -4.5V , I <sub>D</sub> = -15A	---	---	8.3	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> = -250uA	-1.2	-1.6	-2.5	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> = -10V , I <sub>D</sub> = -3A	---	15	---	S

#### Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = -20V , V <sub>GS</sub> = -10V , I <sub>D</sub> = -45A (NOTE 3 · 4)	---	115	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	16	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	25	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = -20V , V <sub>GS</sub> = -10V , R <sub>G</sub> = 6Ω , I <sub>D</sub> = -45A (NOTE 3 · 4)	---	41.6	---	ns
T <sub>r</sub>	Rise Time (NOTE 3 · 4)		---	12.7	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	308	---	
T <sub>f</sub>	Fall Time		---	70	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = -20V , V <sub>GS</sub> = 0V , F= 1MHz	---	6100	---	pF
C <sub>oss</sub>	Output Capacitance		---	600	---	
C <sub>riss</sub>	Reverse Transfer Capacitance		---	540	---	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> = 0V , V <sub>DS</sub> = 0V , F=1MHZ	---	4.2	---	Ω

#### Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> = V <sub>D</sub> = 0V , Force Current	---	---	-90	A
I <sub>SM</sub>	Pulsed Source Current		---	---	-180	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> = 0V , I <sub>S</sub> = -1A , T <sub>J</sub> = 25°C	---	---	-1	V

#### NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=-25V, V<sub>GS</sub>=-10V, L=0.1mH, I<sub>AS</sub>=-59A, R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
4. Essentially independent of operating temperature.



Characteristics Curves

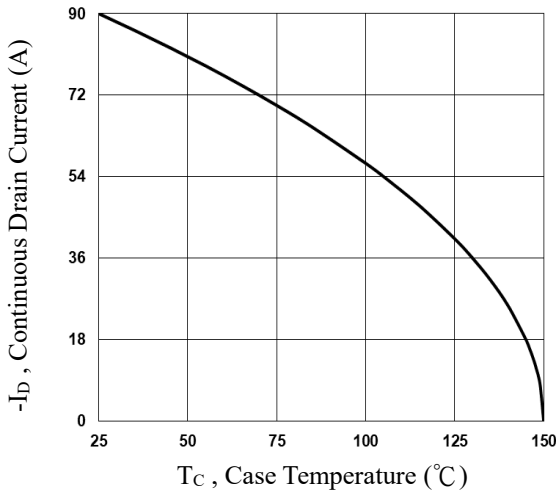


Fig.1 Continuous Drain Current vs. TC

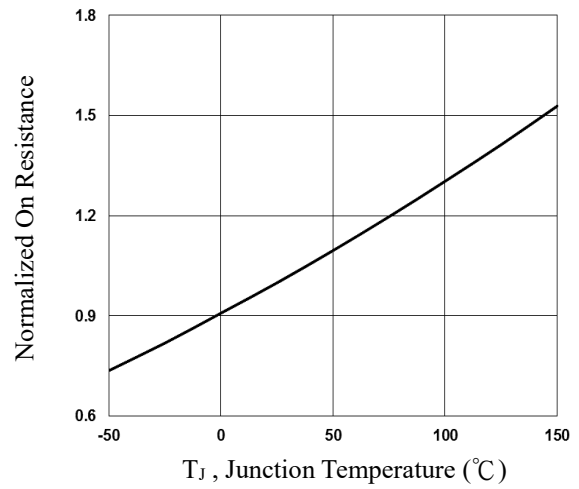


Fig.2 Normalized RDS(on) vs. TJ

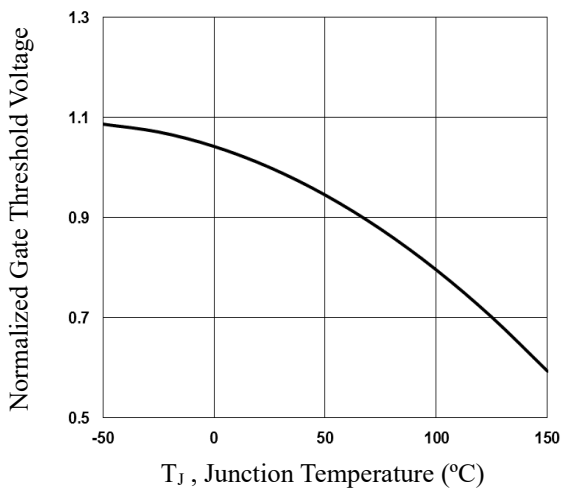


Fig.3 Normalized Vth vs. TJ

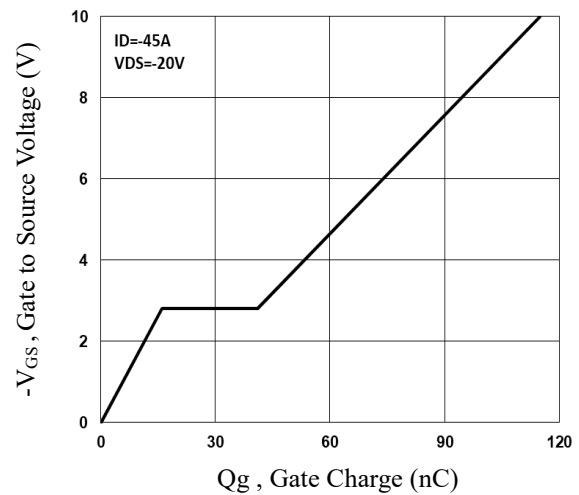


Fig.4 Gate Charge Waveform

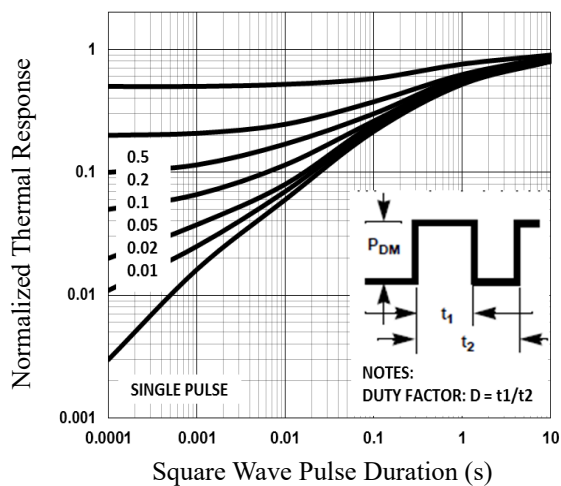


Fig.5 Normalized Transient Impedance

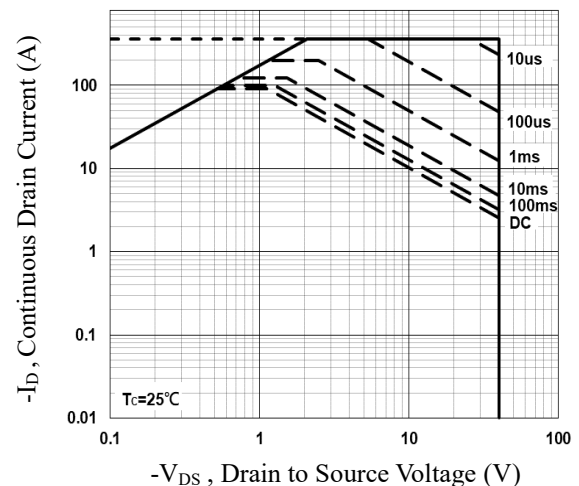


Fig.6 Maximum Safe Operation Area



Characteristics Curves

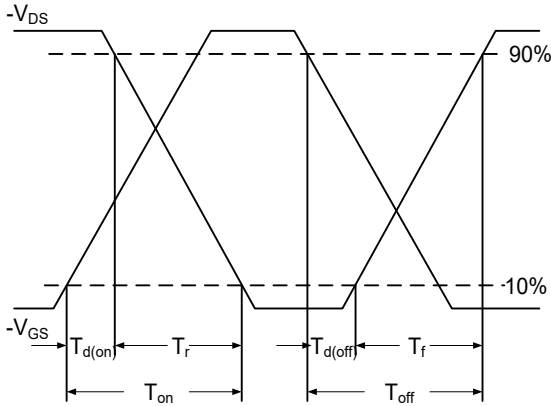


Fig.7 Switching Time Waveform

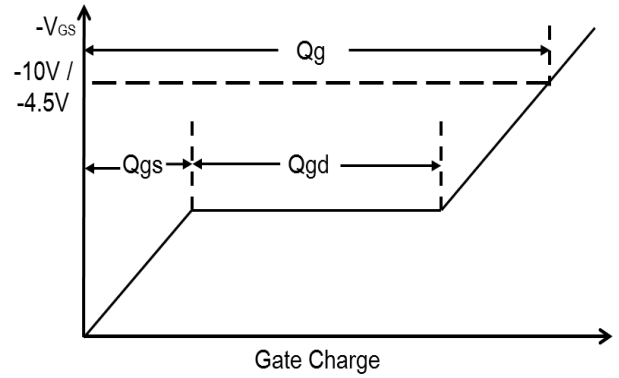
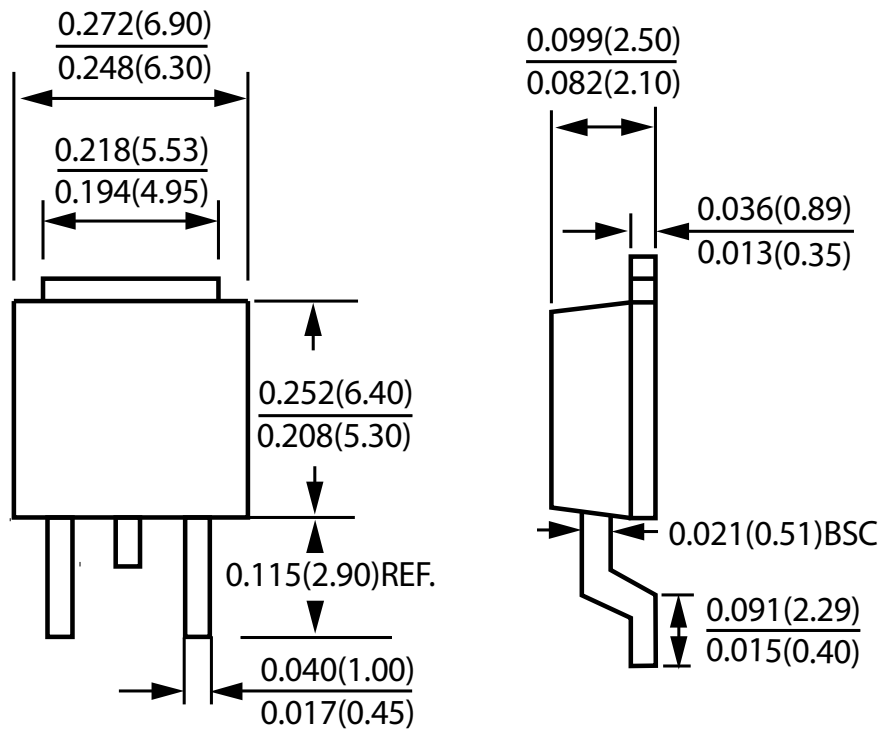


Fig.8 Gate Charge Waveform

Package Outline Dimensions



TO-252

Dimensions in inches and (millimeters)



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