



## **General Description**

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

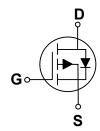
| BV <sub>DSS</sub> | R <sub>DS(ON)</sub> | I <sub>D</sub> |
|-------------------|---------------------|----------------|
| -40 V             | 15 mΩ               | -45 A          |

#### **Features**

- $R_{DS(ON)} \le 15 m\Omega @V_{GS} = -10V$
- · Fast switching
- · Green Device Available
- · Suit for -4.5V Gate Drive Applications

### TO-252 Pin Configuration





### **Applications**

- · MB / VGA / Vcore
- · POL Applications
- · Load Switch
- LED Application

| Symbol           | Parameter   | Rating     | Unit |  |
|------------------|---|------------|------|--|
| $V_{DS}$         | Drain-Source Voltage                              | -40        | V    |  |
| $V_{GS}$         | Gate-Source Voltage                               | ±20        | V    |  |
| I <sub>D</sub>   | Drain Current - Continuous (T <sub>C</sub> =25°C) | -45        | Α    |  |
| I <sub>DM</sub>  | Drain Current - Pulsed (NOTE 1)                   | -180       | Α    |  |
| EAS              | Single Pulse Avalanche Energy (NOTE 2)            | 130        | mJ   |  |
| IAS              | Single Pulse Avalanche Current (NOTE 2)           | -51        | Α    |  |
| $P_D$            | Power Dissipation (T <sub>C</sub> =25°C)          | 73.5       | W    |  |
| $T_J$            | Operating Junction Temperature Range              | -50 to 150 | °C   |  |
| T <sub>STG</sub> | Storage Temperature Range                         | -50 to 150 | °C   |  |
| Marking Code     |   | PD015      |      |  |

| Thermal Characteristics |  |      |     |      |  |
|-------------------------|--|------|-----|------|--|
| Symbol                  | Parameter                              | Тур. | Max | Unit |  |
| $R_{\theta JA}$         | Thermal Resistance Junction to Ambient |      | 62  | °C/W |  |
| $R_{	heta JC}$          | Thermal Resistance Junction to Case    |      | 1.7 | °C/W |  |





# Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

#### **Off Characteristics**

| Symbol            | Parameter                      | Conditions   | Min. | Тур. | Max. | Unit |
|-------------------|--------------------------------|--|------|------|------|------|
| BV <sub>DSS</sub> | Drain-Source Breakdown Voltage | V <sub>GS</sub> = 0V , I <sub>D</sub> = -250uA             | -40  |      |      | V    |
| I <sub>DSS</sub>  | Drain-Source Leakage Current   | $V_{DS}$ = -32V , $V_{GS}$ = 0V , $T_{J}$ =25 $^{\circ}$ C |      |      | -1   | uA   |
| I <sub>GSS</sub>  | Gate-Source Leakage Current    | $V_{GS}$ = ±20V , $V_{DS}$ = 0V                            |      |      | ±100 | nA   |

#### On Characteristics

| Symbol              | Parameter                         | Conditions                       | Min. | Тур. | Max. | Unit |
|---------------------|-----------------------------------|----------------------------------|------|------|------|------|
| R <sub>DS(ON)</sub> | Static Drain-Source On-Resistance | $V_{GS}$ = -10V , $I_D$ = -10A   |      |      | 15   | mΩ   |
|                     |                                   | $V_{GS}$ = -4.5V , $I_{D}$ = -8A |      |      | 22   |      |
| $V_{GS(th)}$        | Gate Threshold Voltage            | $V_{GS}=V_{DS}$ , $I_D=-250uA$   | -1.0 |      | -2.5 | V    |
| gfs                 | Forward Transconductance          | $V_{DS} = -10V$ , $I_{D} = -10A$ |      | 13   |      | S    |

# **Dynamic and switching Characteristics**

| Symbol           | Parameter                        | Conditions   | Min. | Тур. | Max. | Unit |
|------------------|----------------------------------|--|------|------|------|------|
| $Q_g$            | Total Gate Charge (NOTE 3 · 4)   | V <sub>DS</sub> = -32V , V <sub>GS</sub> = -4.5V ,                       | -    | 22.2 |      |      |
| $Q_gs$           | Gate-Source Charge (NOTE 3 · 4)  | I <sub>D</sub> = -10A  |      | 8.2  |      | nC   |
| $Q_{gd}$         | Gate-Drain Charge (NOTE 3 · 4)   | 11D107   |      | 8.8  |      |      |
| $T_{d(on)}$      | Turn-On Delay Time (NOTE 3 · 4)  | $V_{DD}$ = -20V , $V_{GS}$ = -10V , $R_{G}$ = 6 $\Omega$ , $I_{D}$ = -1A | -    | 23   |      |      |
| T <sub>r</sub>   | Rise Time (NOTE 3 \ 4)           |  |      | 10   |      | ns   |
| $T_{d(off)}$     | Turn-Off Delay Time (NOTE 3 · 4) |  |      | 135  |      | 115  |
| $T_f$            | Fall Time (NOTE 3 \ 4)           |  |      | 46   |      |      |
| C <sub>iss</sub> | Input Capacitance                |  |      | 2757 |      |      |
| C <sub>oss</sub> | Output Capacitance               | $V_{DS}$ = -25V , $V_{GS}$ = 0V , F= 1MHz                                |      | 240  |      | pF   |
| $C_{rss}$        | Reverse Transfer Capacitance     | 1  |      | 137  |      |      |

# **Drain-Source Diode Characteristics and Ratings**

| Symbol          | Parameter                 | Conditions  | Min. | Тур. | Max. | Unit |
|-----------------|---------------------------|---|------|------|------|------|
| Is              | Continuous Source Current | V <sub>G</sub> = V <sub>D</sub> = 0V , Force Current      |      | -    | -45  | Α    |
| I <sub>SM</sub> | Pulsed Source Current     |   |      | -    | -90  | Α    |
| $V_{SD}$        | Diode Forward Voltage     | $V_{GS}$ = 0V , $I_{S}$ = -1A , $T_{J}$ = 25 $^{\circ}$ C |      |      | -1   | V    |

### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =-25V,  $V_{GS}$ =-10V, L=0.1mH,  $I_{AS}$ =-51A,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$ =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 4. Essentially independent of operating temperature.





#### **Characteristics Curves**

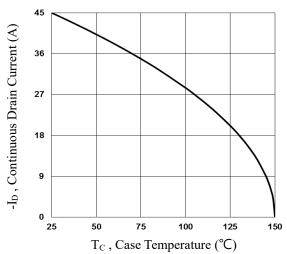


Fig.1 Continuous Drain Current vs. Tc

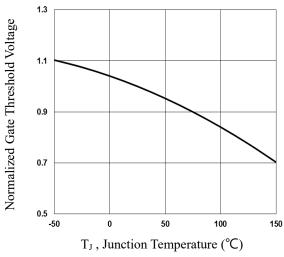


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

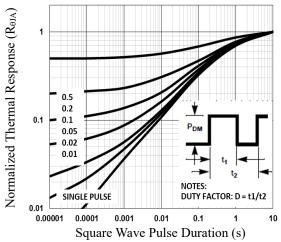


Fig.5 Normalized Transient Impedance

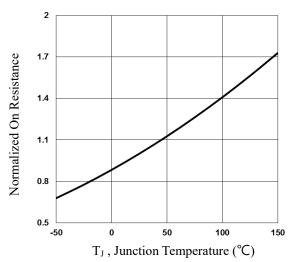


Fig.2 Normalized RDSON vs. T<sub>J</sub>

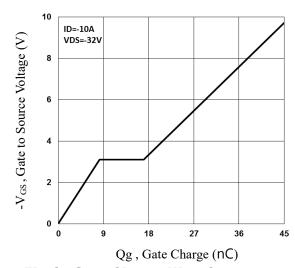


Fig.4 Gate Charge Waveform

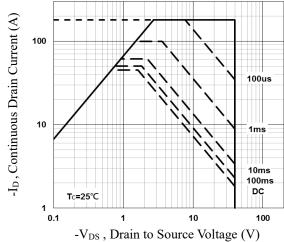
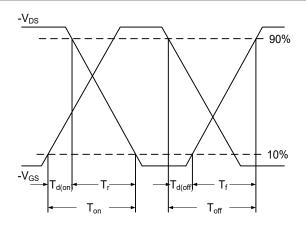


Fig.6 Maximum Safe Operation Area





## **Characteristics Curves**





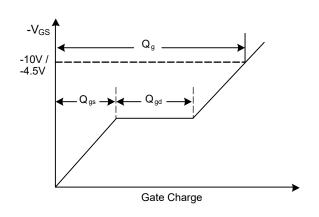
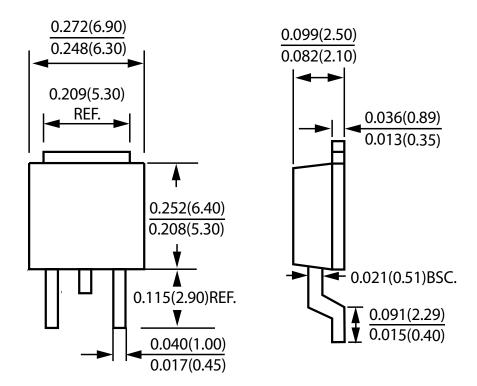


Fig.8 Gate Charge Waveform

# **Package Outline Dimensions**



**TO-252** Dimensions in inches and (millimeters)





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