



### **General Description**

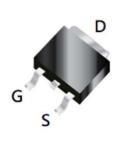
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

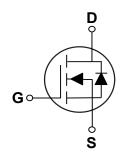
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	Ι <sub>D</sub>
150 V	52 mΩ	20 A

#### **Features**

- $\bullet \; R_{DS(ON)} \leq 52 m \Omega @V_{GS} = 10 V$
- · Improved dv/dt Capability
- · Fast Switching
- · Green Device Available

### TO-252 Pin Configuration





## **Applications**

- Notebook
- · Load Switch
- LED Applications
- · Li Battery Pack Application

bsolute Maximum Ratings T <sub>c</sub> =25°C unless otherwise noted						
Symbol	Parameter	Rating	Units			
$V_{DS}$	Drain-Source Voltage	150	V			
$V_{GS}$	Gate-Source Voltage	±20	V			
ı	Drain Current - Continuous (T <sub>C</sub> =25°C)	20	A			
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> =100°C)	13				
I <sub>DM</sub>	Drain Current - Pulsed (NOTE 1)	80	Α			
E <sub>AS</sub>	Single Pulse Avalanche Energy (NOTE 2)	33	mJ			
I <sub>AS</sub>	Single Pulse Avalanche Current (NOTE 2)	26	Α			
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> =25°C)	76	W			
гD	Power Dissipation – Derate above 25°C	0.61	W/°C			
T <sub>J</sub>	Operating Junction Temperature Range	-50 to 150	°C			
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C			
Marking Code		NP052				

Thermal Characteristics					
Symbol	Symbol Parameter		Max.	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction to Case		1.64	°C/W	





# Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	150			V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =120V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

### On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =15A	-	1	52	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	2	3	4	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =3A		7		S

### **Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$Q_g$	Total Gate Charge	V -00V V -10V I -10A		13.5		
$Q_{gs}$	Gate-Source Charge	$V_{DS}$ =80V , $V_{GS}$ =10V , $I_{D}$ =10A (NOTE 3 \ 4)		3.3		nC
$Q_{gd}$	Gate-Drain Charge	(NOTE 3 + 4)		4.3		
$T_{d(on)}$	Turn-On Delay Time			4.6		
T <sub>r</sub>	Rise Time	$V_{DD}$ =80V , $V_{GS}$ =10V , $R_{G}$ =6 $\Omega$ ,		15	-	nS
$T_{d(off)}$	Turn-Off Delay Time	I <sub>D</sub> =10A (NOTE 3 \ 4)		27		113
$T_f$	Fall Time			8		
C <sub>iss</sub>	Input Capacitance			1050		
$C_{oss}$	Output Capacitance	$V_{DS}$ =80V , $V_{GS}$ =0V , f=1MHz		80		pF
$C_{rss}$	Reverse Transfer Capacitance			6.5		
$R_g$	Gate Resistance	$V_{GS}$ =0V , $V_{DS}$ =0V , f=1MHz		8.0		Ω

### **Drain-Source Diode Characteristics and Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V,Force Current			20	Α
I <sub>SM</sub>	Pulsed Source Current				40	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A			1	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>R</sub> =100V , I <sub>S</sub> =10A ,		95		nS
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/us		370		nC

#### NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =50V, L=0.1mH,  $I_{AS}$ =26A,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$ =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 4. Essentially independent of operating temperature.





#### **Characteristics Curves**

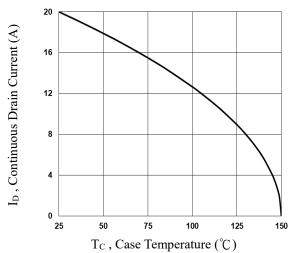


Fig.1 Continuous Drain Current vs. Tc

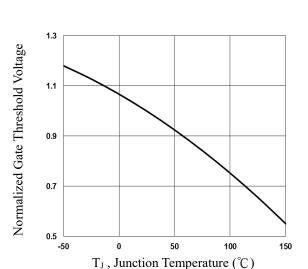


Fig.3 Normalized Vth vs. T<sub>J</sub>

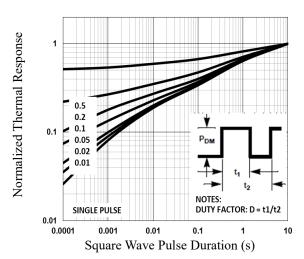


Fig.5 Normalized Transient Impedance

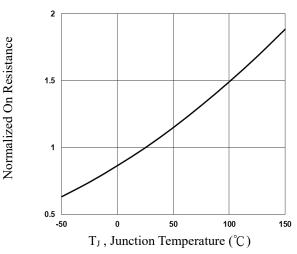


Fig.2 Normalized RDSON vs. T<sub>J</sub>

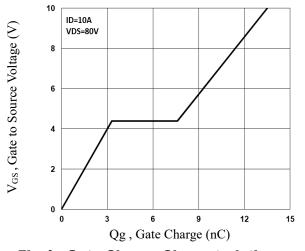


Fig.4 Gate Charge Characteristics

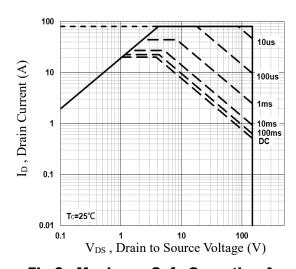
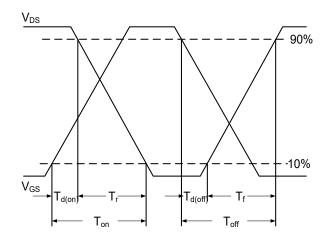


Fig.6 Maximum Safe Operation Area





### **Characteristics Curves**



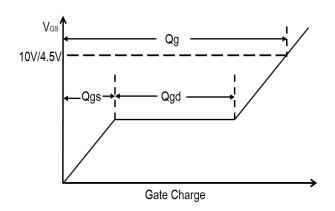
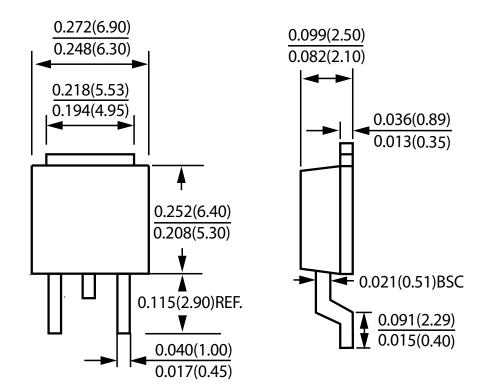


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

## **Package Outline Dimensions**



TO-252
Dimensions in inches and (millimeters)





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