



General Description

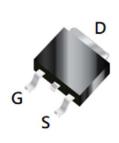
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

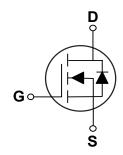
BV _{DSS}	R _{DS(ON)}	I _D
60 V	6 mΩ	75 A

Features

- $R_{DS(ON)} \leq 6m\Omega@V_{GS} = 10V$
- · Improved dv/dt Capability
- Fast Switching
- · Green Device Available

TO-252 Pin Configuration





Applications

- Motor Drive
- Power Tools
- · LED Lighting
- Quick Charger

Absolute Maximum Ratings T _c =25°C unless otherwise noted						
Symbol	Parameter	Rating	Units			
V_{DS}	Drain-Source Voltage	60	V			
V_{GS}	Gate-Source Voltage	±20	V			
I-	Drain Current - Continuous (T _C =25°C)	75	Α			
I _D	Drain Current - Continuous (T _C =100°C)	47	A			
I _{DM}	Drain Current - Pulsed (NOTE 1)	300	Α			
E _{AS}	Single Pulse Avalanche Energy (NOTE 2)	181	mJ			
I _{AS}	Single Pulse Avalanche Current (NOTE 2)	60.1	Α			
P_{D}	Power Dissipation (T _C =25°C)	101	W			
T_J	Operating Junction Temperature Range	-55 to 150	°C			
T _{STG}	Storage Temperature Range	-55 to 150	°C			
Marking Code		NG6P0				

Thermal Characteristics					
Symbol	Parameter	Тур.	Max.	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		62	°C/W	
$R_{\theta JC}$	Thermal Resistance Junction to Case		1.23	°C/W	





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	60			V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =60V , V _{GS} =0V			1	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	IStatic Drain-Source On-Resistance	V _{GS} =10V , I _D =30A			6	mΩ
		V _{GS} =4.5V , I _D =20A			7.5	
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.6	2.5	V
gfs	Forward Transconductance	V _{DS} =10V , I _D =3A		15		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge	\\ -20\\ \\ -4.5\\ \ \-4.0\		32.8		
Q_{gs}	Gate-Source Charge	V_{DS} =30V , V_{GS} =4.5V , I_{D} =10A V_{DS} =10A		10.8		nC
Q_{gd}	Gate-Drain Charge	(NOTE 3 \ 4)		11.6		
$T_{d(on)}$	Turn-On Delay Time			20		
T_r	Rise Time	V_{DD} =15V , V_{GS} =10V , R_{G} =3.3 Ω ,		14.2		nS
$T_{d(off)}$	Turn-Off Delay Time	I _D =1A (NOTE 3 \ 4)		61.2		113
T_f	Fall Time			16.8		
C _{iss}	Input Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		4740		
C_{oss}	Output Capacitance			325		pF
C_{rss}	Reverse Transfer Capacitance			161		
R_g	Gate Resistance	V _{GS} =0V , V _{DS} =0V , f=1MHz		1.6		Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V,Force Current			75	Α
I _{SM}	Pulsed Source Current				150	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A			1	V

NOTES:

- ${\bf 1.}\ Repetitive\ Rating: Pulsed\ width\ limited\ by\ maximum\ junction\ temperature.$
- 2. V_{DD} =25V, V_{GS} =10V, L=0.1mH, I_{AS} =60.1A, R_{G} =25 Ω , Starting T_{J} =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.





Characteristics Curves

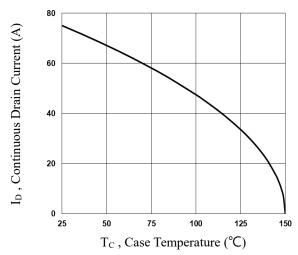


Fig.1 Continuous Drain Current vs. Tc

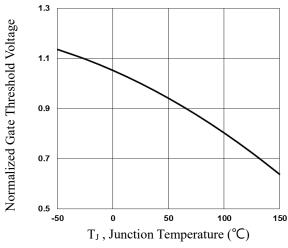


Fig.3 Normalized V_{th} vs. T_J

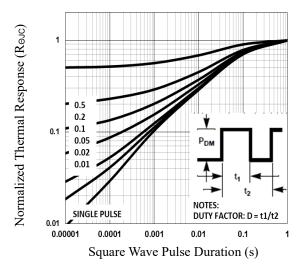


Fig.5 Normalized Transient Response

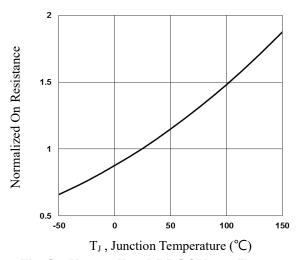


Fig. 2 Normalized RDSON vs. TJ

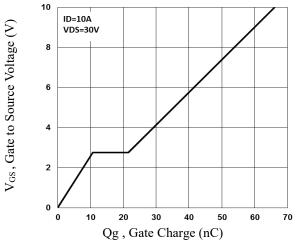


Fig.4 Gate Charge Waveform

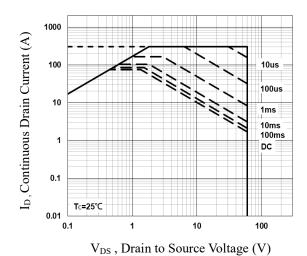


Fig.6 Maximum Safe Operation Area





Characteristics Curves

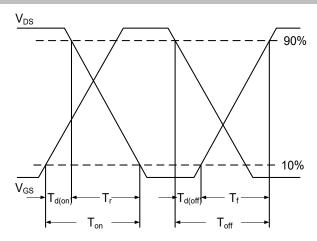
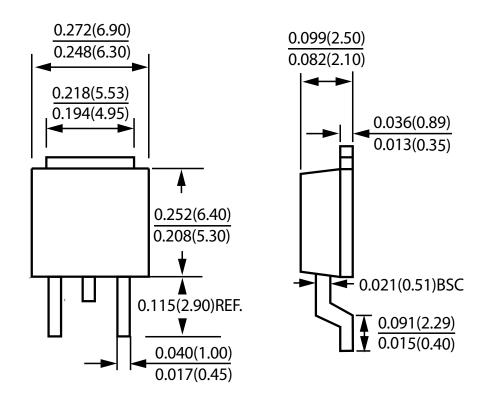


Fig.7 Switching Time Waveform

Package Outline Dimensions



TO-252
Dimensions in inches and (millimeters)





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